

**P1.6** The figure shows an elementary 1-bit RAM memory cell out of logic gates. The cell has three inputs,  $D$ ,  $RW$  and  $SL$ , and one output  $O$ . (a) Analyze the operation of this cell and identify, in particular, the roles played by the three inputs. (b) Modify the design of the cell to make it work as a ROM cell.

