Problem HH 1.30

Diode clamps are used to protect delicate electronic circuits, especially CMOS, from the effects of overvoltage. The figure below shows such a clamp that limits the output voltage to +5.6 V. Design a symmetrical clamp that limits the output voltage to the range -5.6V < V < +5.6V.

1.0 k

in

out

1N914

+5 V