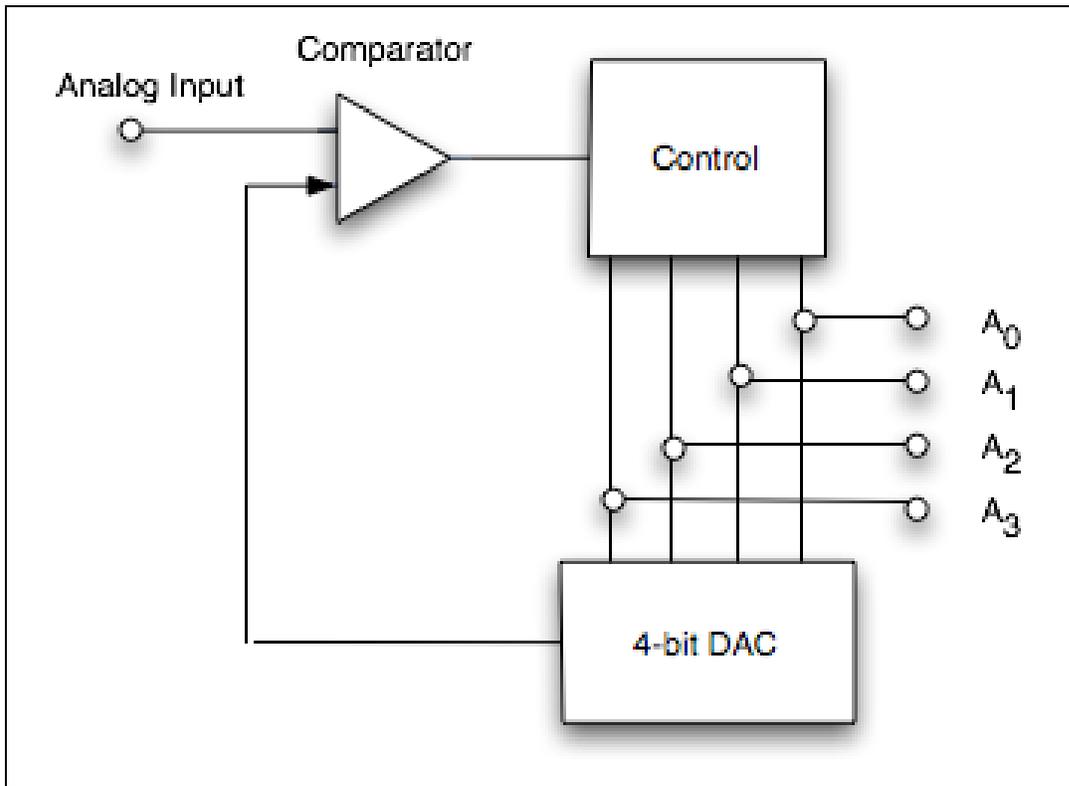Consider a successive approximation ADC utilizing a comparator and a DAC which outputs values between o and 15 V for a four bit input.



List the successive approximations to the analog input of 11.65 V in terms of the "guess" bits $A_n$ and the output voltage from the DAC. You can use Fig. 14.17 from Diefenderfer and Holton as guidance.