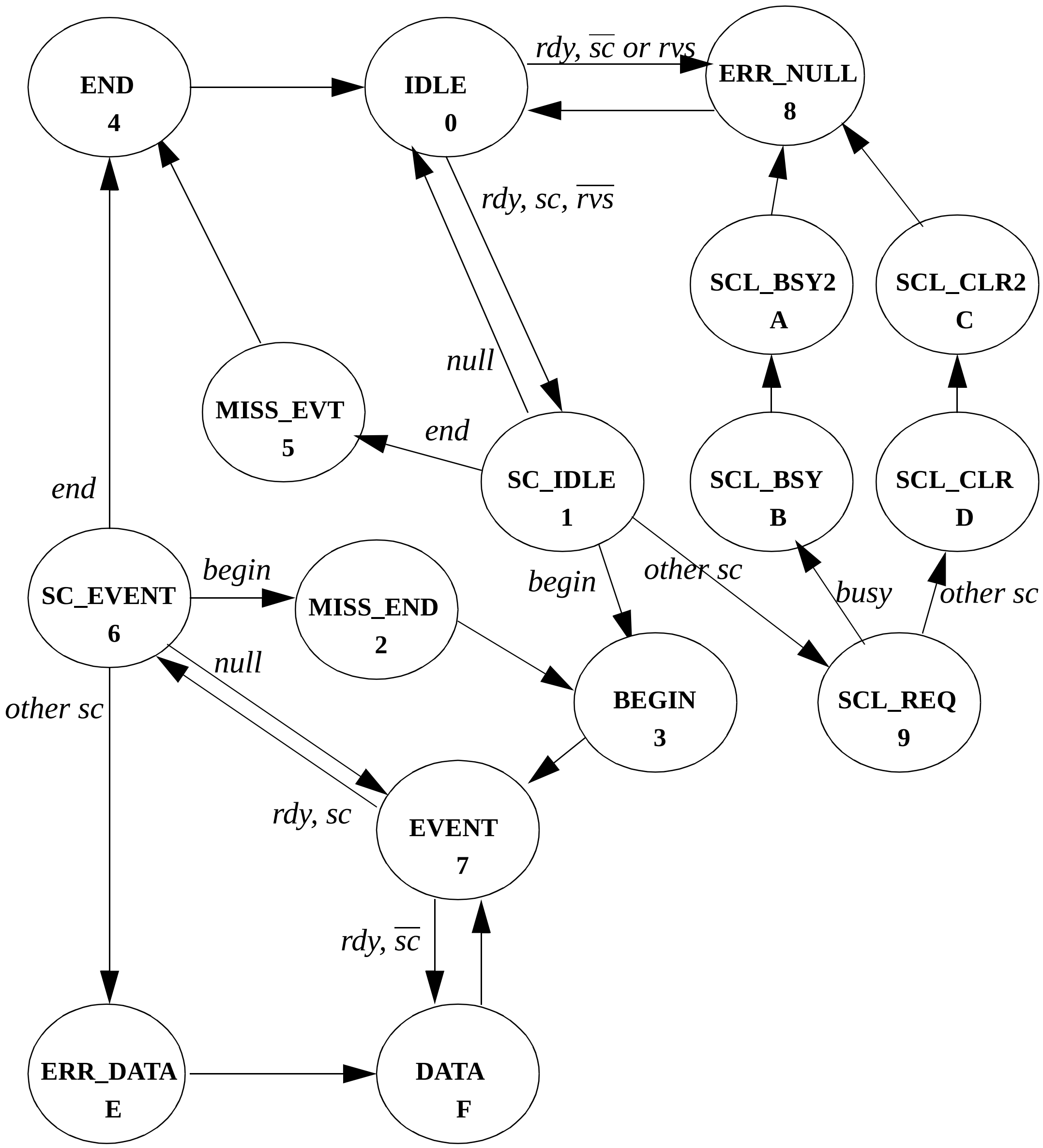
L2 HOTLink Input State Diagram



L2 HOTLink Input States

- IDLE (0)

  Task: Wait for next character on *rdy* flag

  Branches:

  sc and $\overline{\text{rvs}}$: SC_IDLE (1)

  $\overline{\text{sc}}$ or rvs: ERR_NULL (8)

- SC_IDLE (1)

  Task: Test special character received

  Branches:

  null 05 : IDLE (0)

  begin 00 : BEGIN (3)

  end 08 : MISS_EVT (5)

  all others : SCL_REQ (9)

- MISS_END (2)

  Task: Flag event with end, increment error count

  Control: go to BEGIN (3)

- BEGIN (3)

  Task: Increment fifo event counter

  Control: go to EVENT (7)

- END (4)

  Task: Flag event with end

  Control: go to IDLE (0)

- MISS_EVT (5)

  Task: Generate dummy event and place in fifo

  Control: go to END (4)

- SC_EVENT (6)

  Task: Test special character received

  Branches:

  begin 00 : MISS_END (2)

  end 08 : END (4)

  null 05 : EVENT (7)

  rvs and all others : ERR_DATA (E)

- EVENT (7)

  Task: Wait for next character on *rdy* flag

  Branches:

  sc : SC_EVENT (6)

  $\overline{\mathrm{sc}}$: DATA (F)

- ERR_NULL (8)
  Task: Increment error count
  Control: IDLE (0)

- SCL_REQ (9)
  Task: Test special character
  Branches:
  busy 01 : SCL_BSY (B)
  $\overline{\text{busy}}$ (incl. free 09) : SCL_CLR (D)

- SCL_BSY2 (A)
  Control: go to ERR_NULL (8)

- SCL_BSY (B)
  Task: Clear request for L1 Busy, decrement error counter
  Control: go to SCL_BSY2 (A)

- SCL_CLR2 (C)
  Control: go to ERR_NULL (8)

- SCL_CLR (D)
  Task: Set request for L1 Busy, decrement error counter
  Control: go to SCL_CLR2 (C)

- ERR_DATA (E)
  Task: Generate dummy character, increment error counter
  Control: go to DATA (F)

- DATA (F)
  Task: Place character in fifo for the event
  Control: go to EVENT (7)