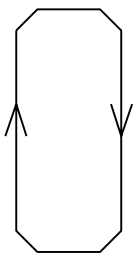
Update Receiver Section Mapper Registers

Well ahead of the actually transfering an event on the MBus, keep the map registers in the processor card fancy Receiver Section pointed at fresh Slots in the Main Memory Event Buffer.



A -> W "Point MBus Map "K" to Main Mem Buf Slot "R""

W -> A "OK, MBus Map "K" is now pointed at Main Mem Buf Slot "R""

Admin decides, "Are there free main memory event buffer slots that I may assign to mapper registers that have just finished being used ?"

There are 8 bits of High Order Address on the MBus that the fancy Receiver Section on the Processor Cards will use to identify "different Events" and "different Data Sources".

This will be setup for example 4 bits for event identification, i.e. 16 events and 4 bits for data source identification, 16 data sources.

Let's assume that the "Receiver Card" will just cycle through the 16 "Event ID" Addresses on the MBus.