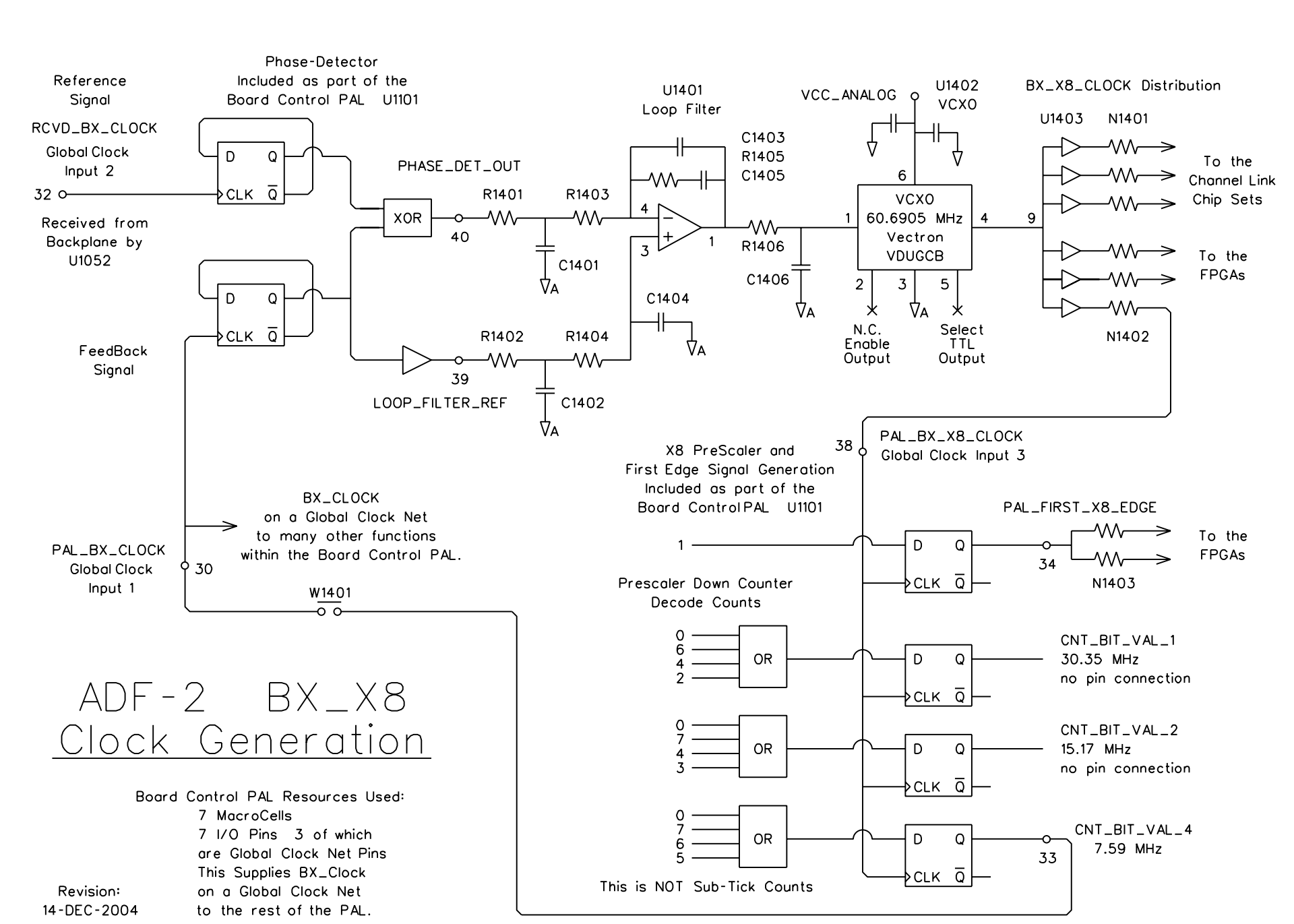


# ADF-2 BX_X8 Clock Generation

Board Control PAL Resources Used:
7 MacroCells
7 I/O Pins 3 of which are Global Clock Net Pins
This Supplies BX_Clock on a Global Clock Net to the rest of the PAL.

Revision:
14-DEC-2004