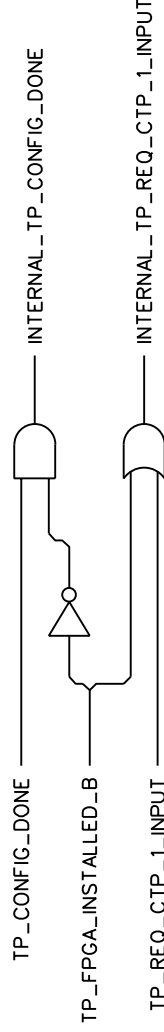


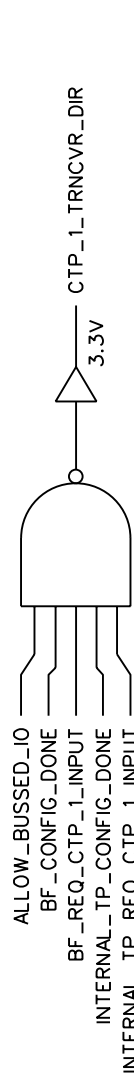
CTP Connector Management Logic in the BSPT FPGA

CTP Connector Management Rules

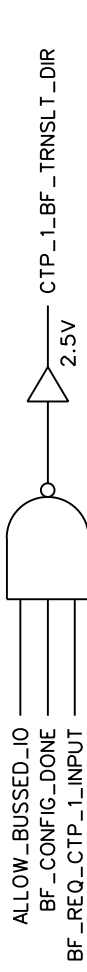
When the "TP Installed Jumper" says that the TP FPGA is NOT installed on this CMX card then: force the INTERNAL_TP_CONFIG_DONE signal Low force the INTERNAL_TP_REQ_CTP_1_INPUT signal Hi.



The LVDS Transceiver direction is is Output unless both the BF and TP request the direction to be Input.



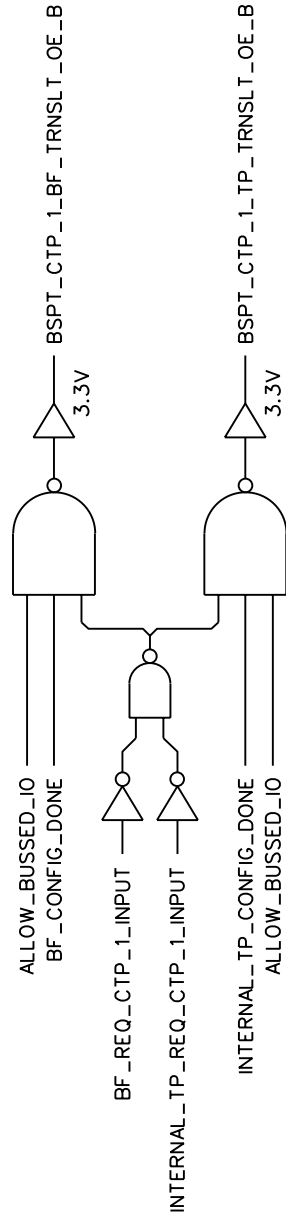
Never enable the Translator Output Drivers unless the associated FPGA (BF or TP) is Configured.



When both the BF and TP FPGAs are Configured you may Enable both the BF and the TP Translator Output Drivers except when both the BF and TP have requested the Direction to be Output. In that case disable both the BF and the TP Translator Output Drivers.



It is an Error Condition when both the BF and TP are requesting the Direction to be Output.



The logic for CTP Connector #1 is shown. The logic for CTP Connector #2 is similar.

