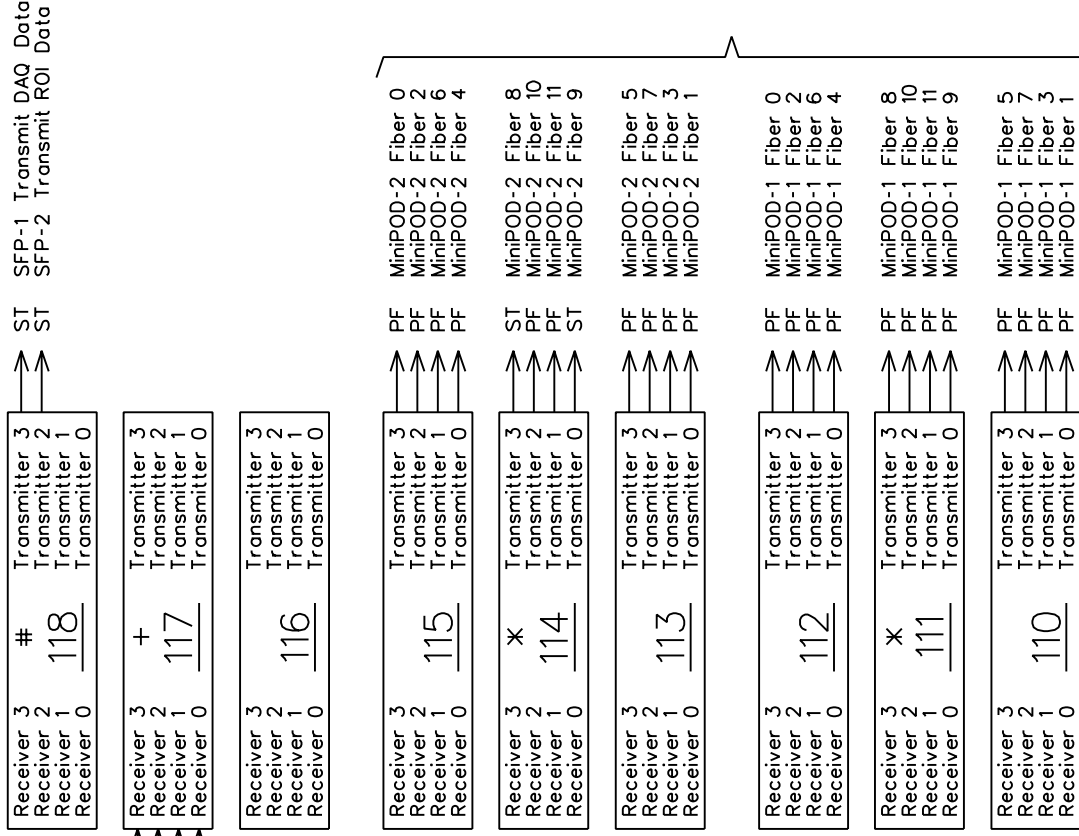


GTX Transceivers - Base Function FPGA

QUAD

Base Function
Readout to
DAQ and ROI



For Possible Use with S-Link Control Signals

- SFP-1 Received Data
- SFP-2 Received Data
- SFP-3 Received Data
- SFP-4 Received Data

Base Function FPGA

GTX Reference Clock Notes:

- # → This Quad receives the Crystal Oscillator #1 reference clock which is typically 120.000 MHz.
- + → This Quad receives the Crystal Oscillator #2 reference clock which is typically 120.000 MHz or 100.000 MHz
- * → These Quads receive the LHC locked 320.6296 MHz reference clock.

PF → Polarity Flip
ST → Straight Through