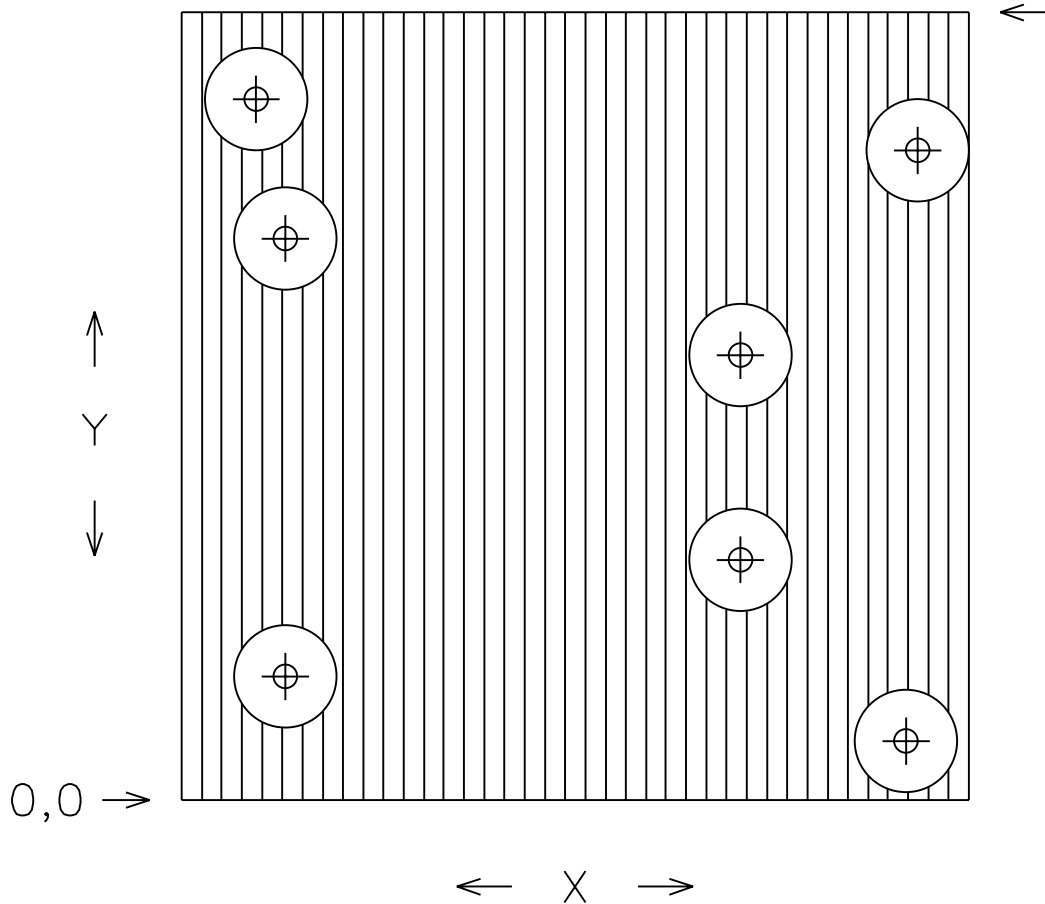


Base Function FPGA Heatsink

Rev. 10 Nov 2013

Y = 100 mm
X = about 100 mm



From the bar stock, cut the heatsink Y dimension to length.

Machine 7 holes into each heatsink.

Each hole consists of:

In a $\frac{1}{2}$ " diameter machine away the heatsink fins and level the base plate.

Drill a tight hole through the base plate for a 4-40 machine screw.

X,Y locations of the 7 holes:

13.2 mm, 71.3 mm	71.0 mm, 56.5 mm
13.2 mm, 15.7 mm	71.0 mm, 30.5 mm
93.5 mm, 82.5 mm	9.5 mm, 89.0 mm
92.0 mm, 7.5 mm	