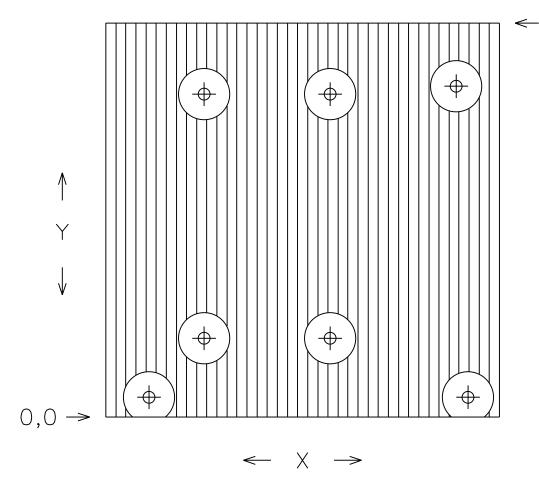
## Topological FPGA Heatsink

Rev. 11 Nov 2013

Y = 100 mm X = about 100 mm



From the bar stock, cut the heatsink Y dimension to length.

Machine 7 holes into each heatsink. Each hole consists of:

In a  $\frac{1}{2}$ " diameter machine away the heatsink fins and level the base plate. Drill a tight hole through the base plate for a 4-40 machine screw.

X,Y locations of the 7 holes:

25.0 mm, 82.0 mm 57.0 mm, 82.0 mm 25.0 mm, 20.0 mm 57.0 mm, 20.0 mm 92.0 mm, 5.0 mm 89.0 mm, 84.0 mm 11.0 mm, 5.0 mm