

Project file:cmx_vme_vat.cpm

SYSTEM: SPB 16.3

EDA-02515-V1-0

PROJECT:CMX VME Testboard

MODULE:cmx_vme_vat

Sheet: 1/15 Module: 1/15 Project: 1/15

LAST_MODIFIED=Wed May 23 16:20:51 2012

Design by: Y. Ermoline

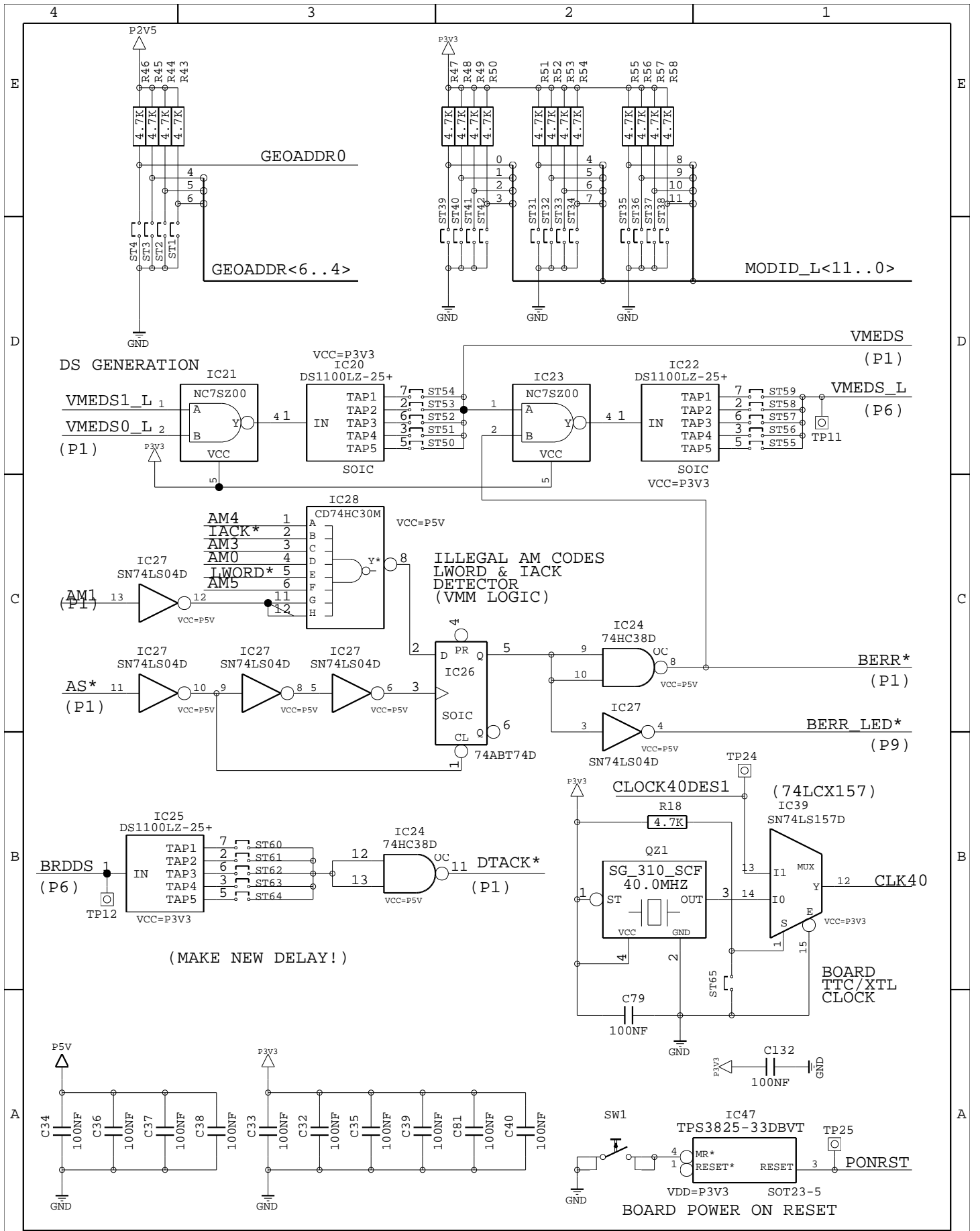
PCB by: <PCB_BY>

DATE: 11/04/2012



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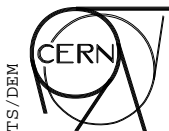
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Design by: Y. Ermoline

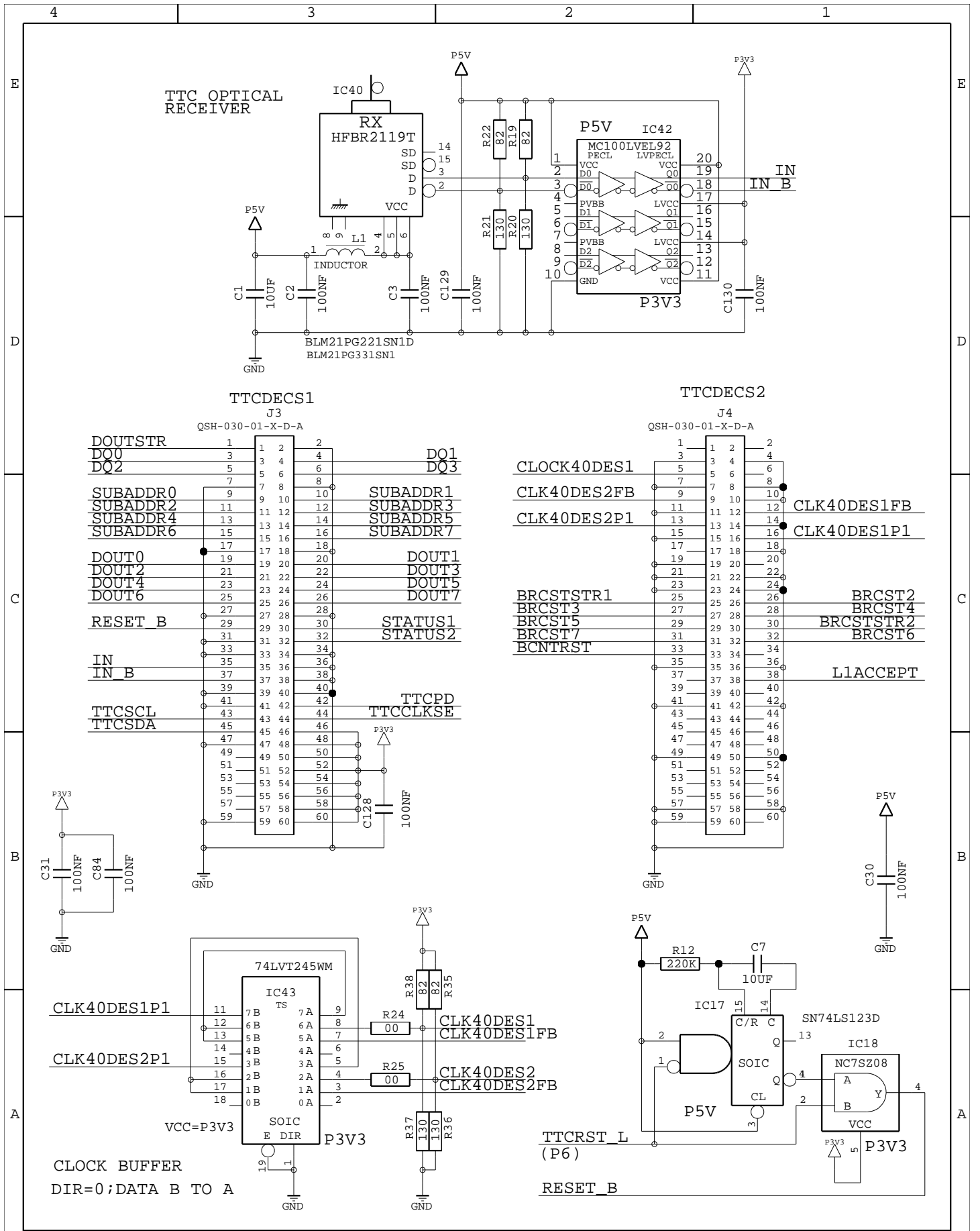
PCB by: <PCB_BY>

DATE: 11/04/2012



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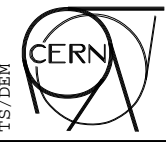


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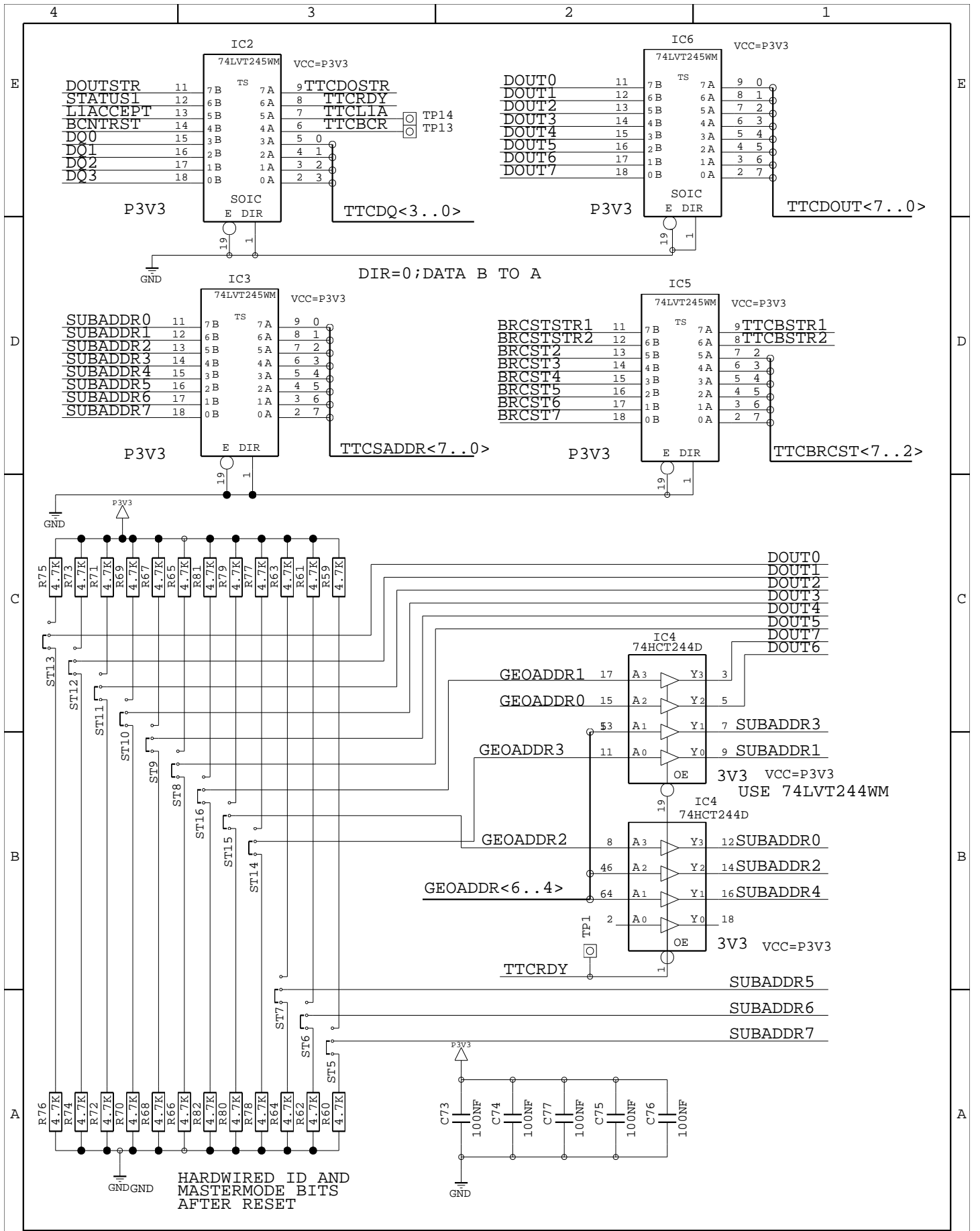
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MODULE: cmx_vme_vat	Sheet: 3/15	Project: 3/15
LAST_MODIFIED=Wed May 23 16:20:53 2012		
Design by: Y. Ermoline	PCB by: <PCB_BY>	DATE: 11/04/2012

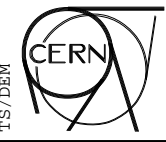


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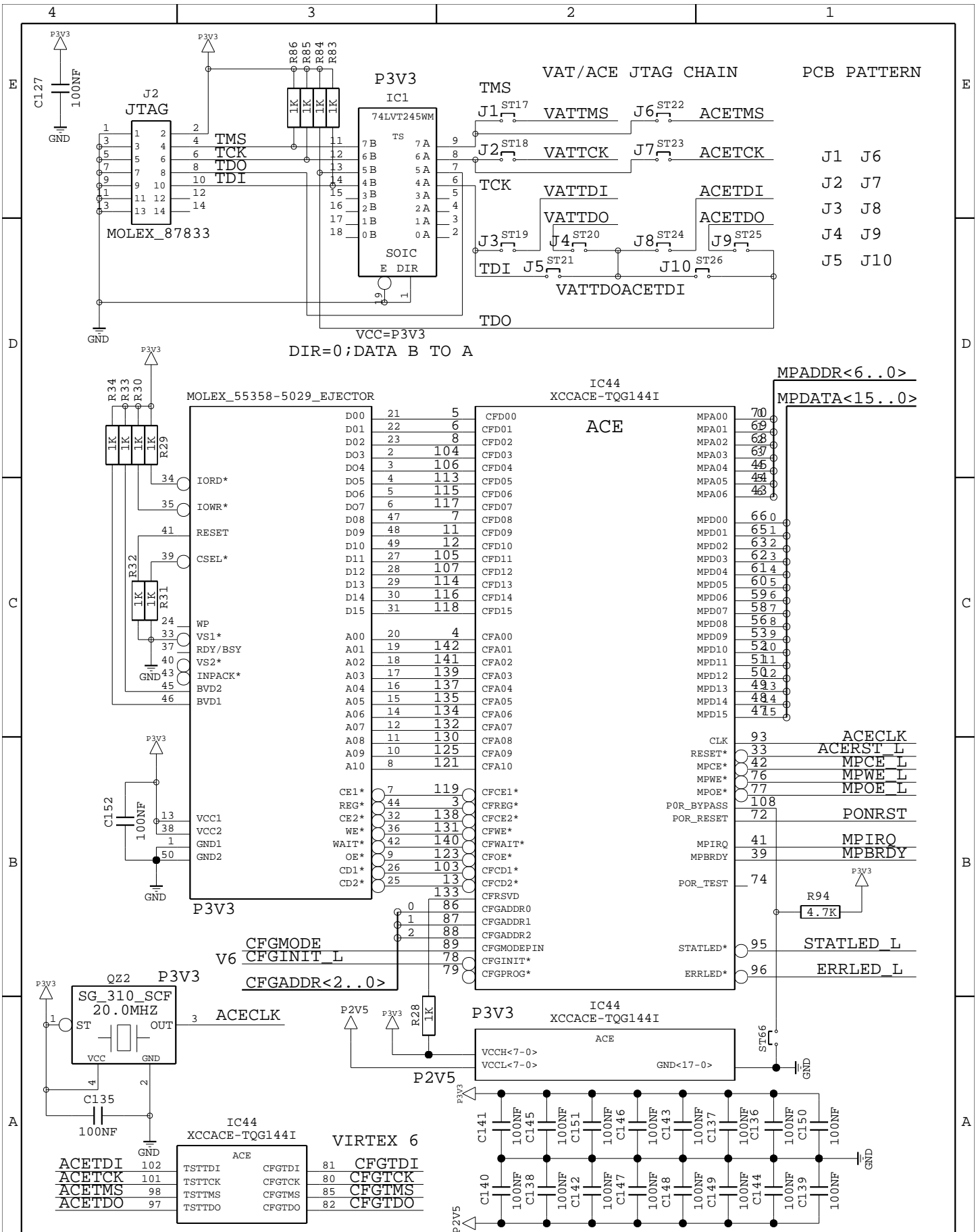
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MODULE:cmx_vme_vat	Sheet: 4/15	Project: 4/15
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Design by: Y. Ermoline	PCB by: <PCB_BY>	DATE: 11/04/2012



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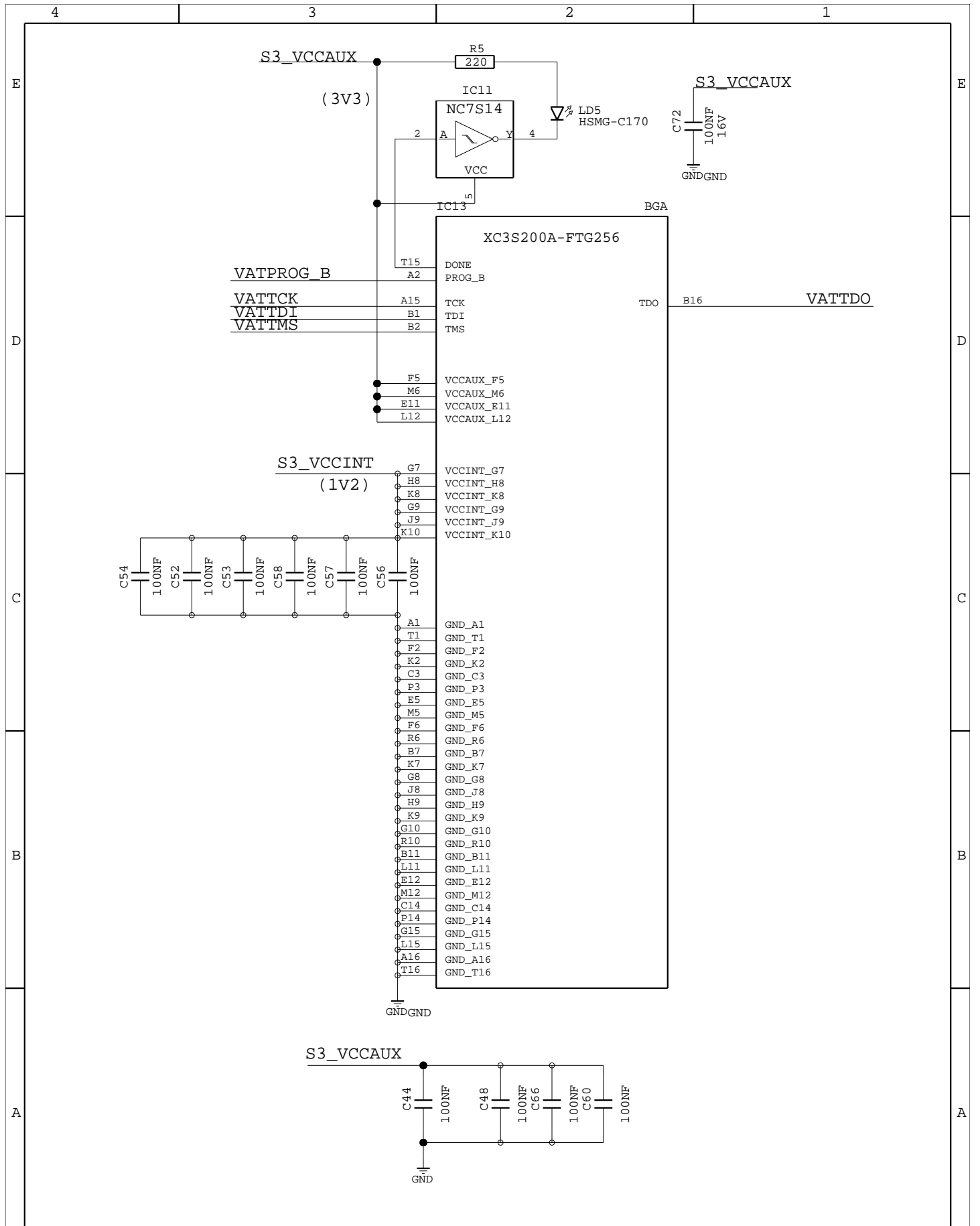
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MODULE:cmx_vme_vat	Sheet: 5/15	Project: 5/15
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Design by: Y. Ermoline	PCB by: <PCB_BY>	DATE: 11/04/2012



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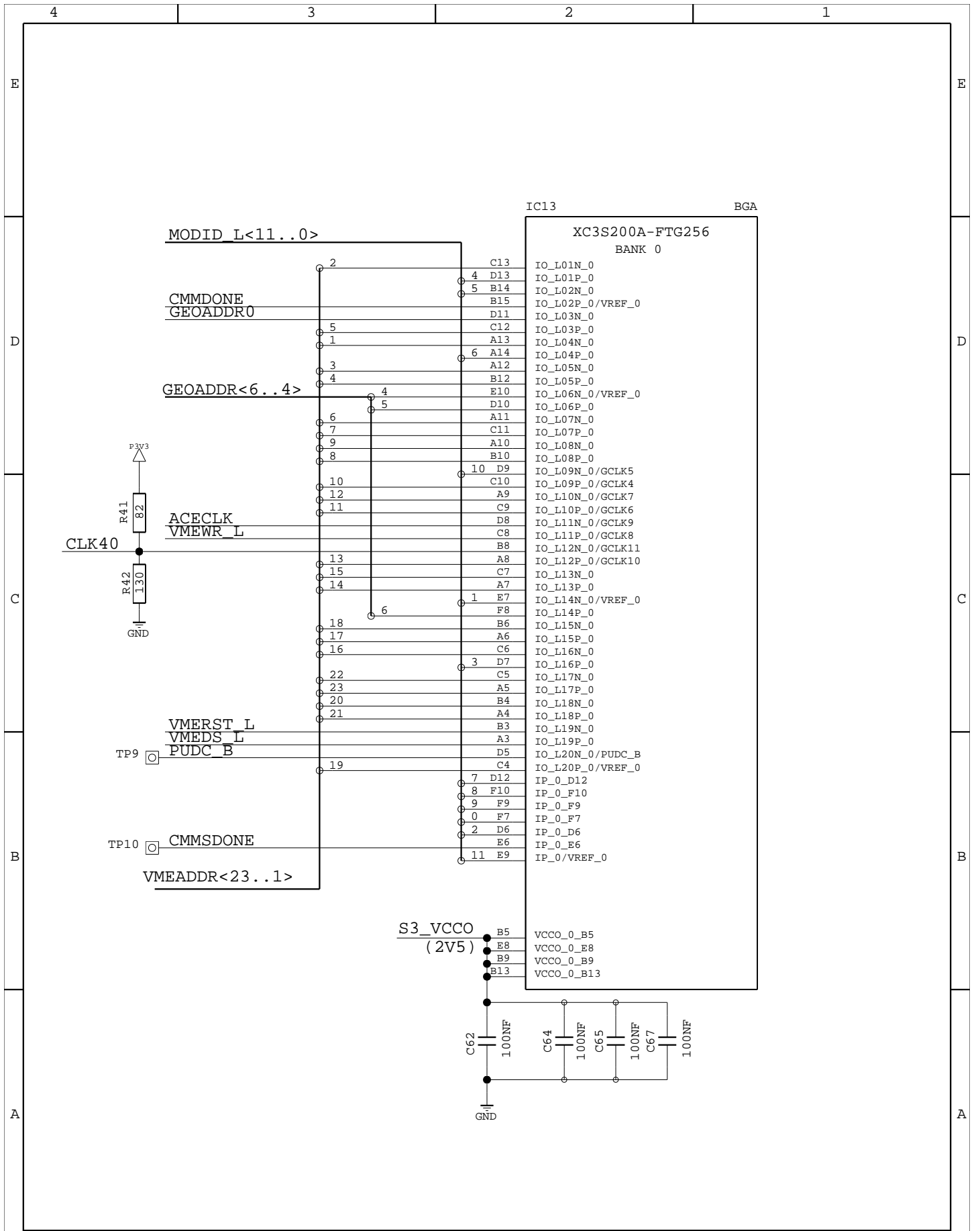
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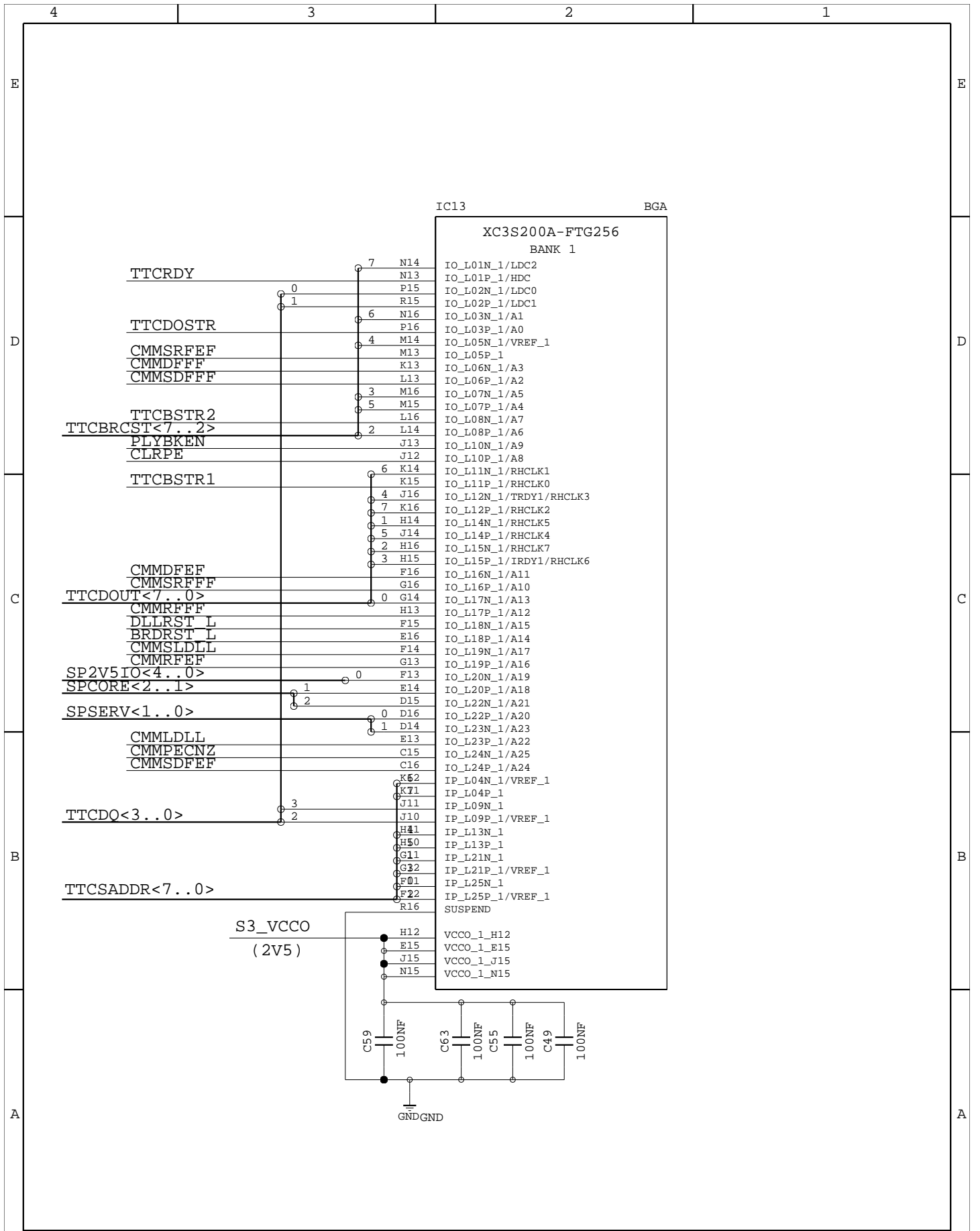
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Design by: Y. Ermoline

PCB by: <PCB_BY>

DATE: 11/04/2012

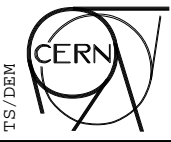


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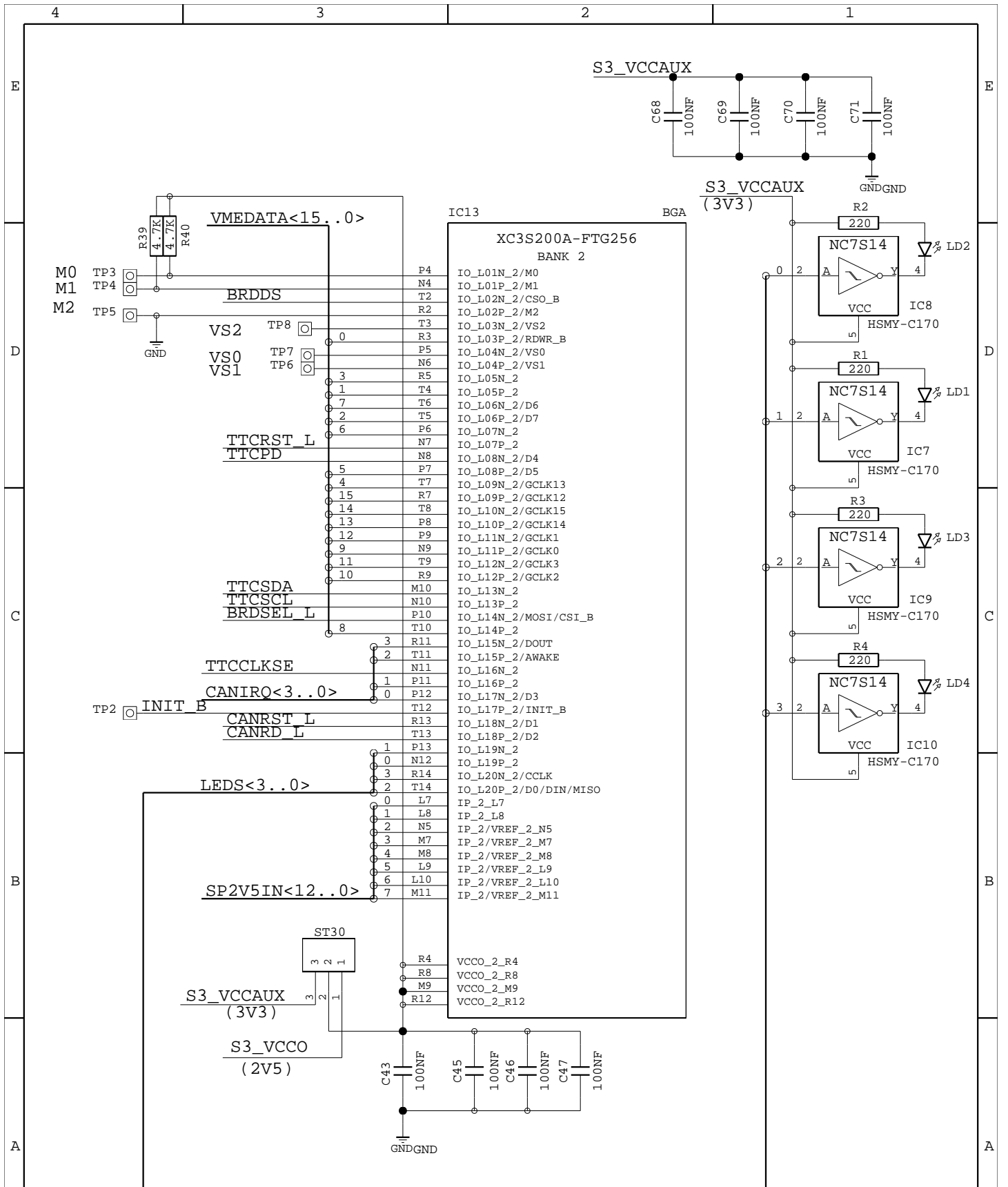
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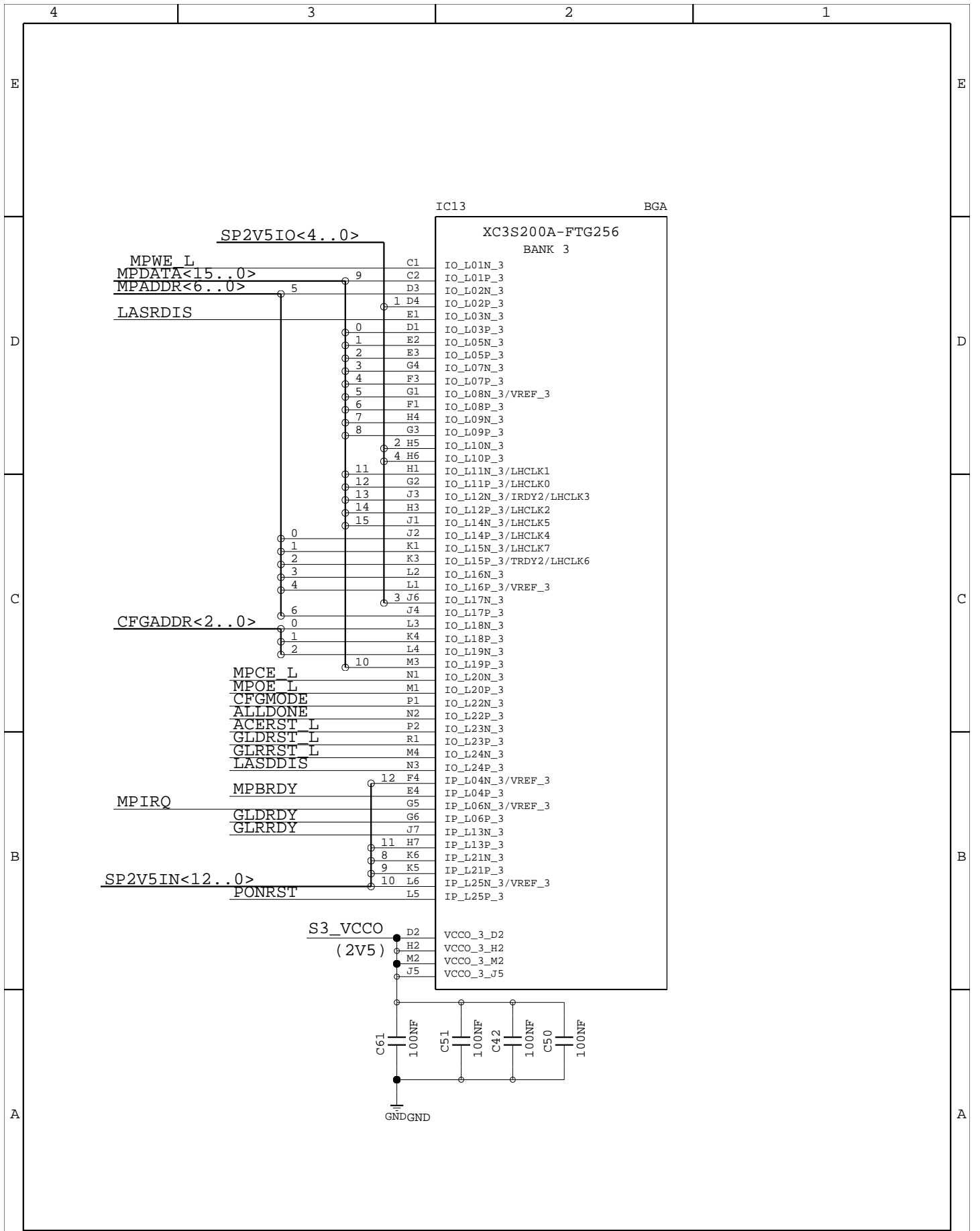
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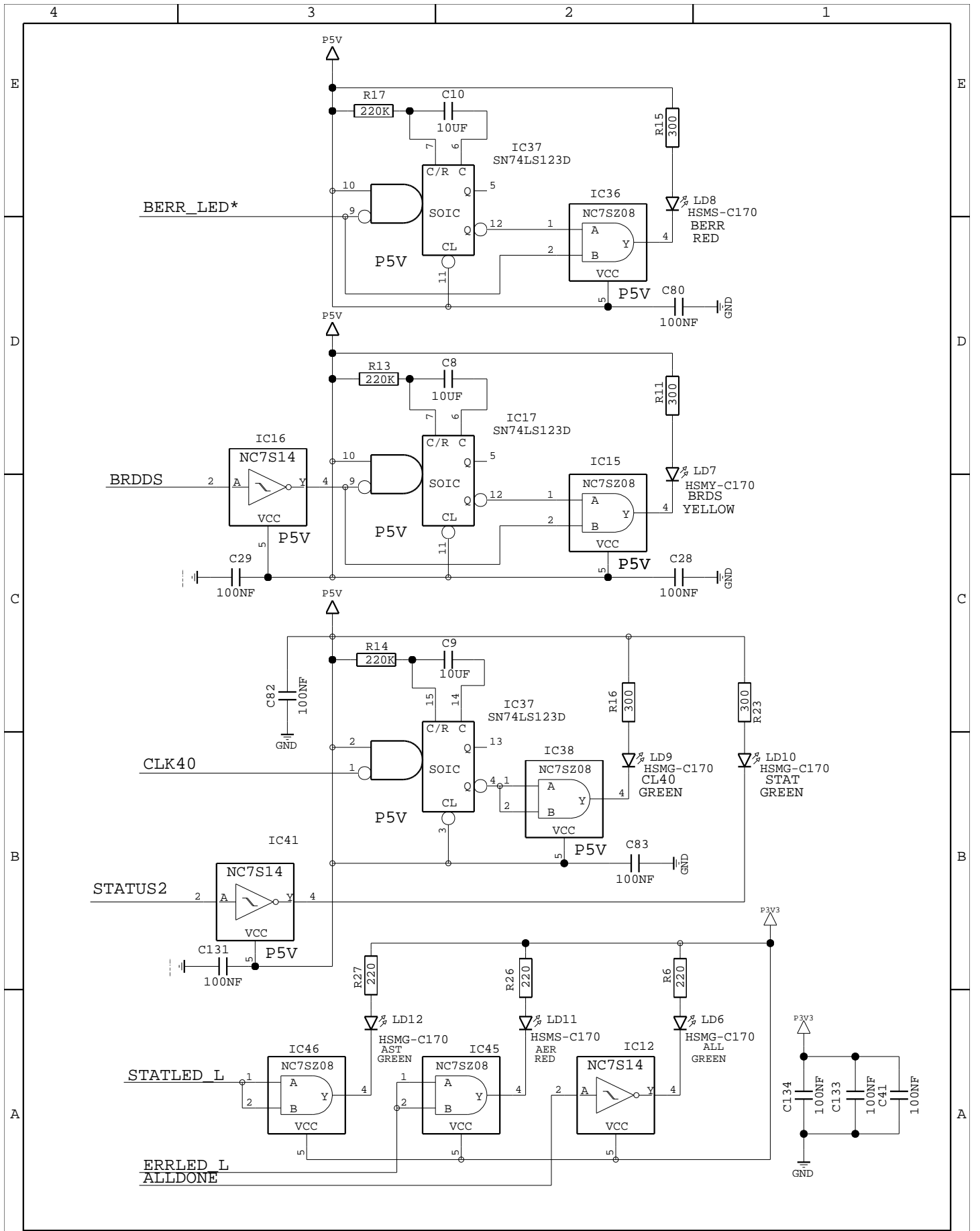
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Design by: Y. Ermoline

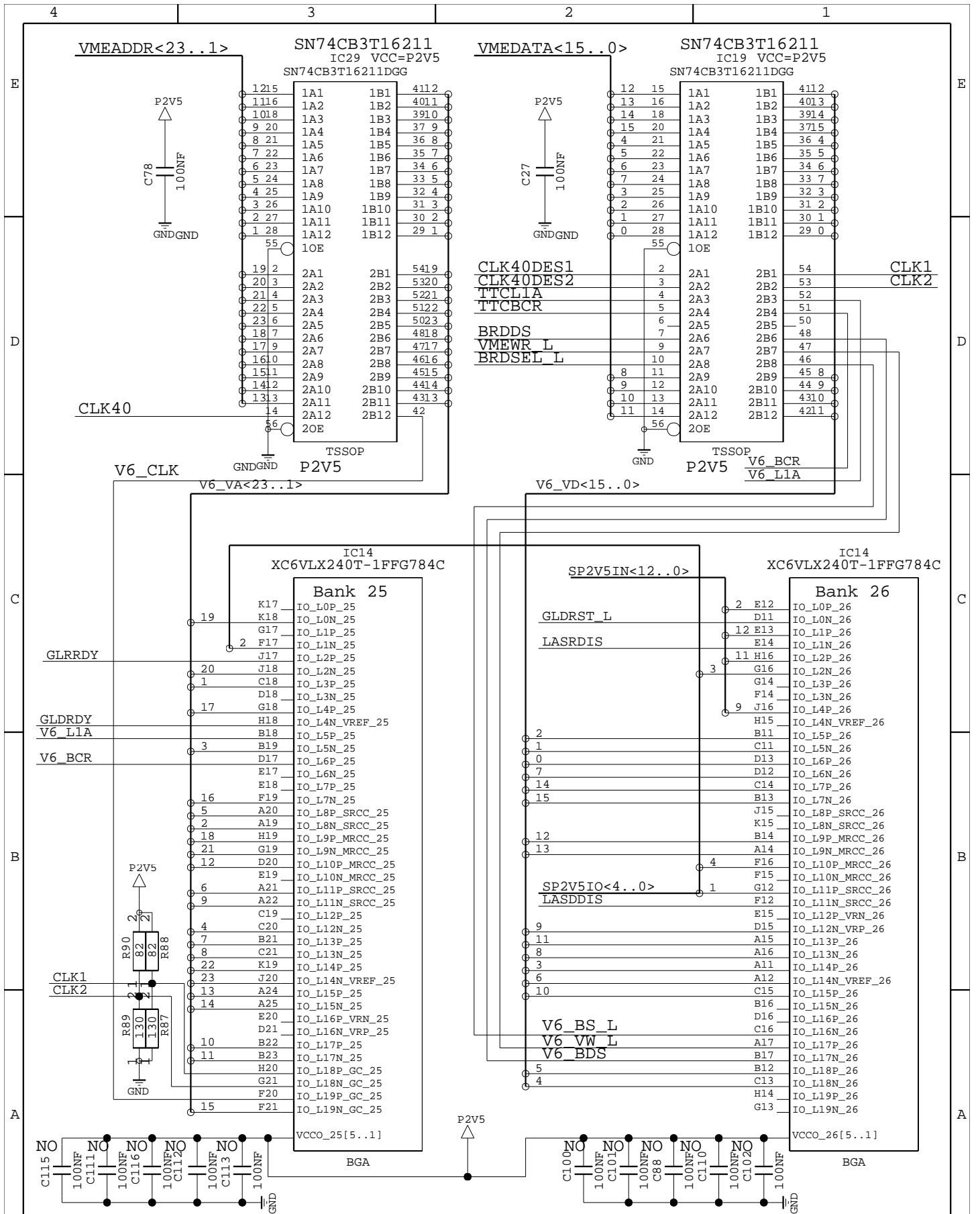
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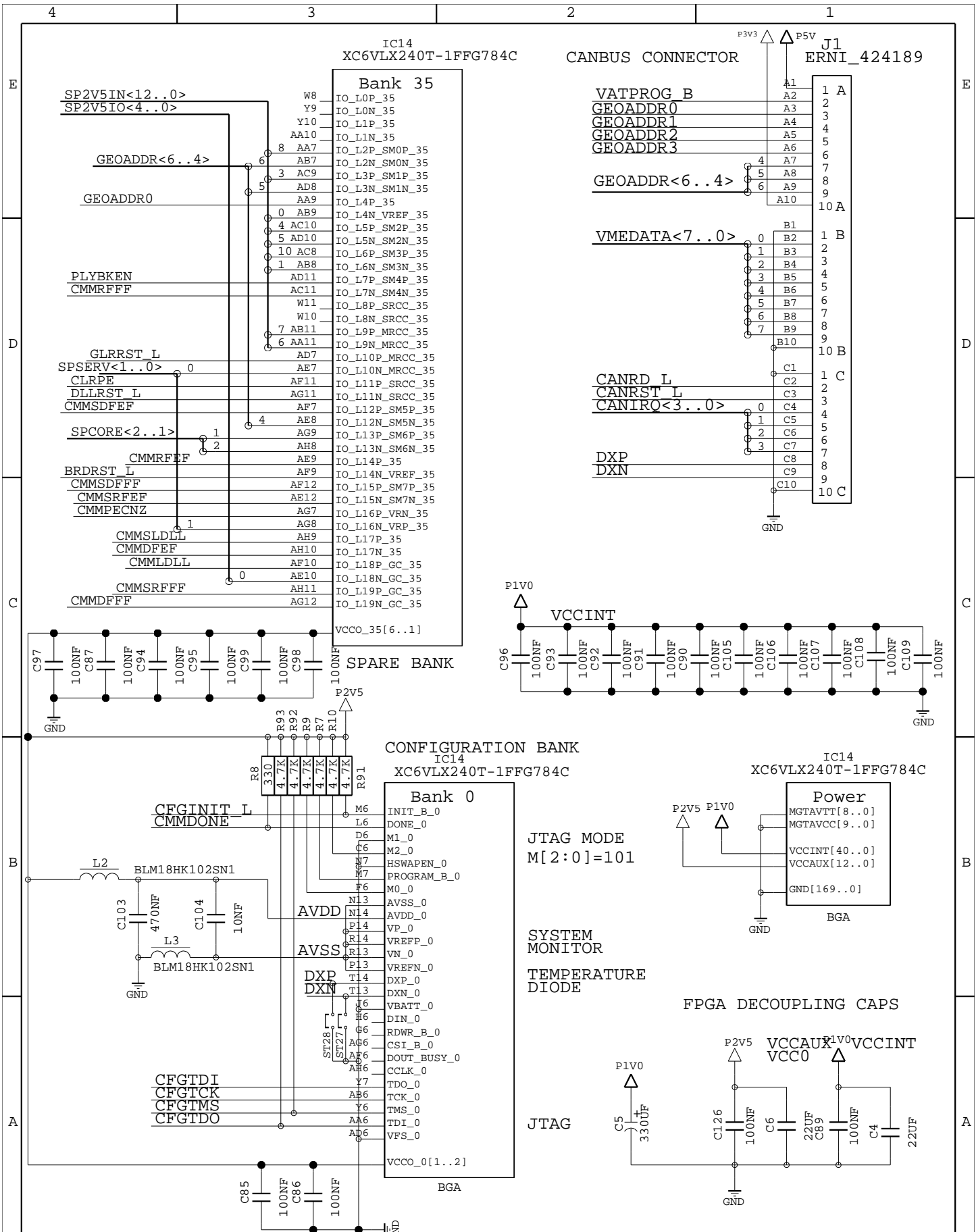
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Design by: Y. Ermoline

PCB by: <PCB_BY>

DATE: 11/04/2012

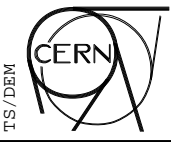


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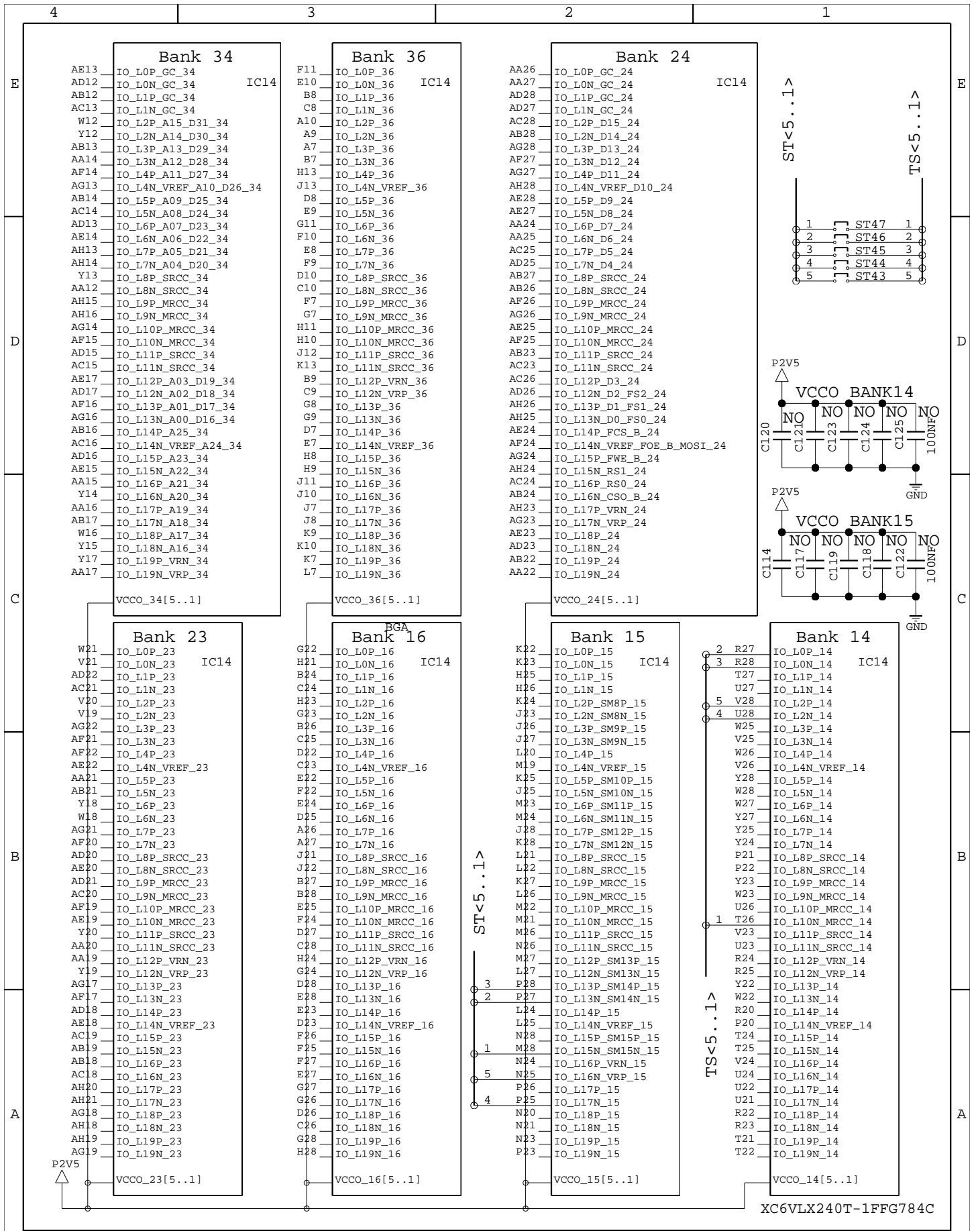
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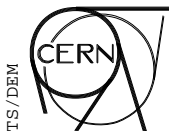
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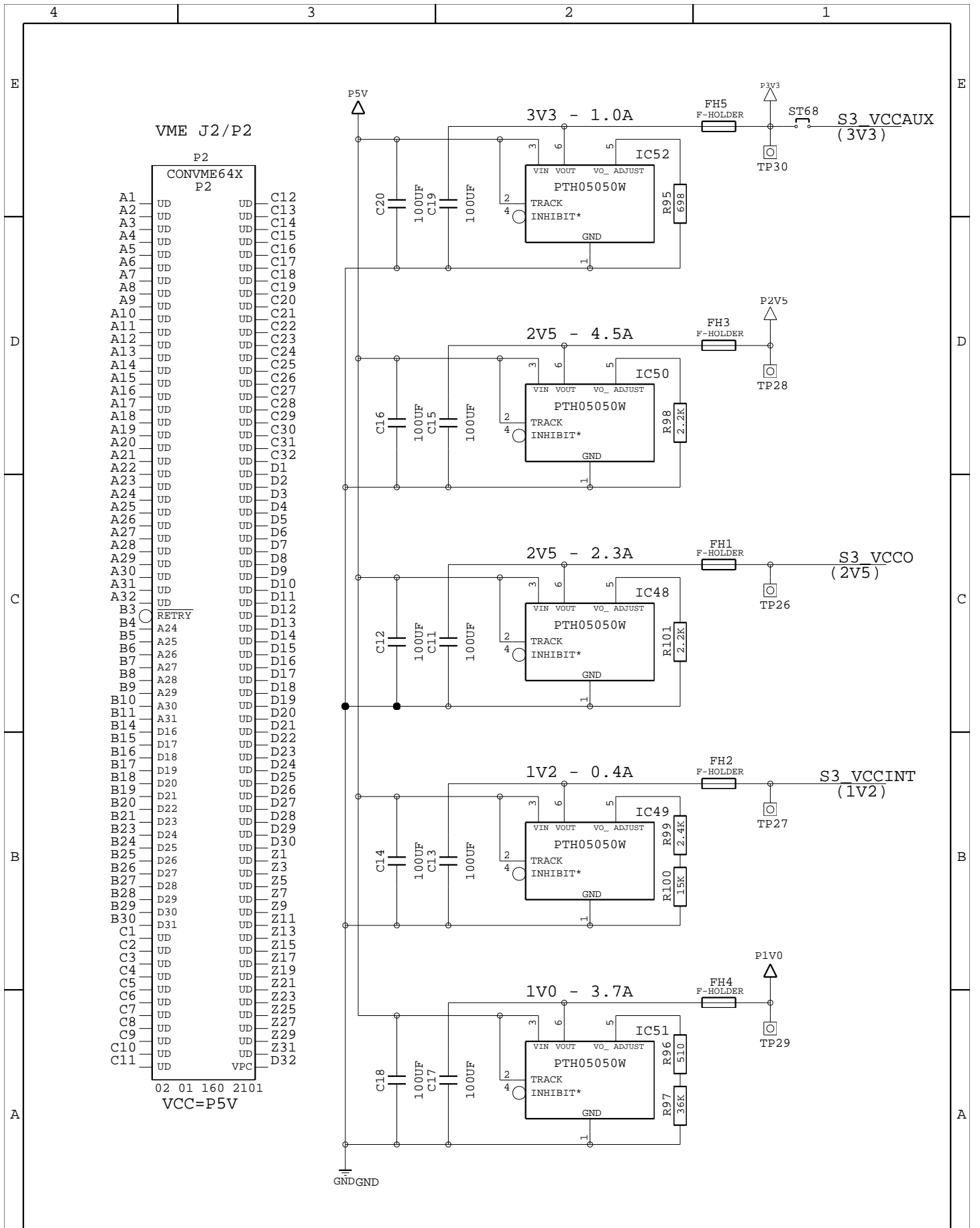
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