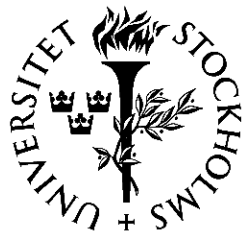# CMX Preliminary Design Review

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- Purpose and focus
- Agenda
- Goals of the review

Purpose and focus

- Purpose: Examine status of the CMX project
- Focus on:
  - Functional requirements and interfaces
  - Technical aspects
  - Different modes of operation
  - Organizational aspects
    - schedules for hardware development, production and commissioning,
    - strategies for software and hardware development

Agenda

- Introduction (S. Silverstein)
- Overview of CMX project (Y. Ermoline)
- Responses to comments already received (Y.E.)
- Panel Q&A (All)
- Closed panel discussion (Reviewers only)
- Summary and close-out (All)

Goals

- Review functional requirements
  - Important points include
    - Interfaces
    - Different operational modes
    - Standalone mode
    - Algorithms and physics sign-off
- Evaluate technical and engineering aspects
  - Important topics include
    - # and types of FPGAs
    - FPGA signal counts
    - Clocks and timing
    - VME address space
    - Optical link speed and numbers
    - Front panel space

Goals

- Project organization and schedule
  - Important points include
    - Overall timeline
    - Hardware development, testing and commissioning plans
    - Software and firmware development
- Miscellaneous corrections and amendments

- And of course, decision whether to approve the CMX project as described.