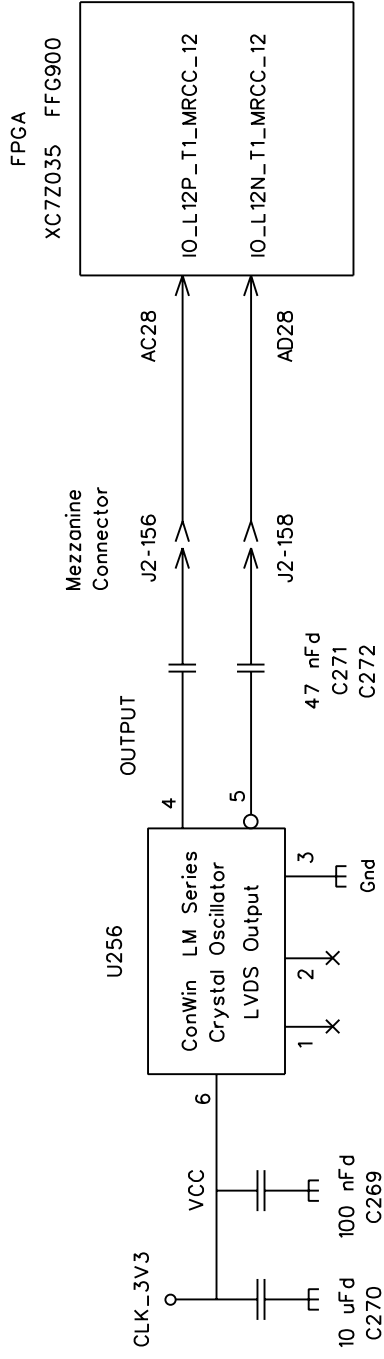


HTM Card - Spare Clock Oscillator



ConWin LM Series Oscillator

- *- 25 ppm LVDS Output
- Hi / Open Enable on pin *1
- 2 msec Startup

The ZYNQ Select I/O pins that receive the AC coupled LVDS Clock signals must use their internal DC Bias source. See: Select I/O pg 127, DQS_BIAS, EQ_LEVEL_0.

