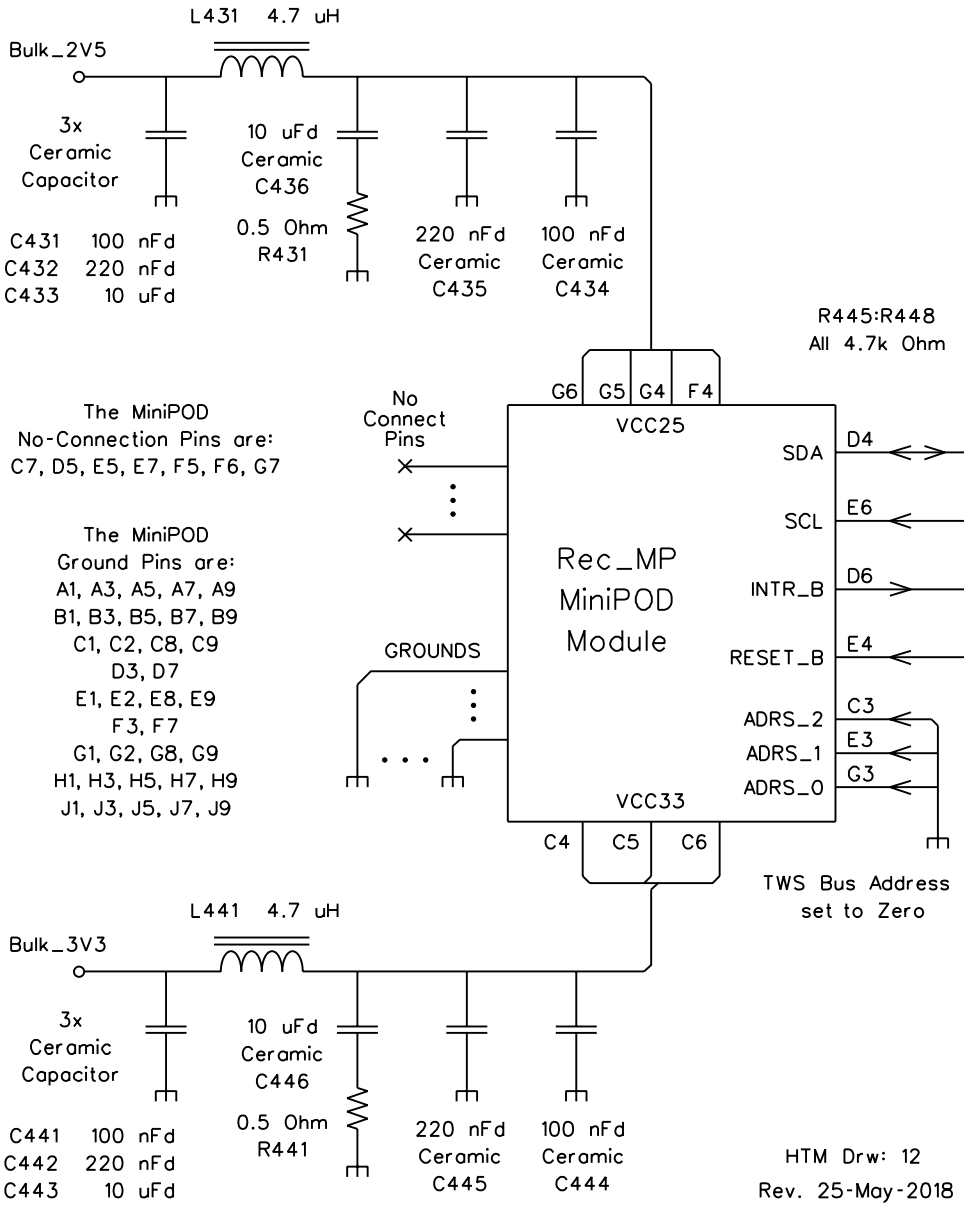
HTM Receiver MiniPOD



MiniPOD Rec. Data Signal	Pin	Coupling Capacitor	Polar	Mezzanine Connector	HTM's ZYNQ FPGA Pin	Signal
D0+	D1	C25	—	J3-32	AH10	MGT RX P0 109
D0-	D2	C26	—	J3-30	AH9	MGT RX N0 109
D1+	F1	C27	X	J1-29	P6	MGT RX P3 112
D1-	F2	C28	X	J1-31	P5	MGT RX N3 112
D2+	A2	C29	X	J3-28	AJ8	MGT RX P1 109
D2-	B2	C30	X	J3-26	AJ7	MGT RX N1 109
D3+	J2	C31	—	J1-25	T6	MGT RX P2 112
D3-	H2	C32	—	J1-27	T5	MGT RX N2 112
D4+	A4	C33	—	J3-24	AG8	MGT RX P2 109
D4-	B4	C34	—	J3-22	AG7	MGT RX N2 109

MiniPOD Rec. Data Signal	Pin	Coupling Capacitor	Polar	Mezzanine Connector	HTM's ZYNQ FPGA Pin	Signal
D5+	J4	C35	—	J1-21	U4	MGT RX P1 112
D5-	H4	C36	—	J1-23	U3	MGT RX N1 112
D6+	A6	C37	—	J3-20	AE8	MGT RX P3 109
D6-	B6	C38	—	J3-18	AE7	MGT RX N3 109
D7+	J6	C39	X	J1-17	V6	MGT RX P0 112
D7-	H6	C40	X	J1-19	V5	MGT RX N0 112
D8+	A8	C41	—	J3-16	AH6	MGT RX P0 110
D8-	B8	C42	—	J3-14	AH5	MGT RX N0 110
D9+	J8	C43	X	J1-13	AA4	MGT RX P3 111
D9-	H8	C44	X	J1-15	AA3	MGT RX N3 111
D10+	D9	C45	—	J3-12	AG4	MGT RX P1 110
D10-	D8	C46	—	J3-10	AG3	MGT RX N1 110
D11+	F9	C47	—	J1-9	Y6	MGT RX P2 111
D11-	F8	C48	—	J1-11	Y5	MGT RX N2 111