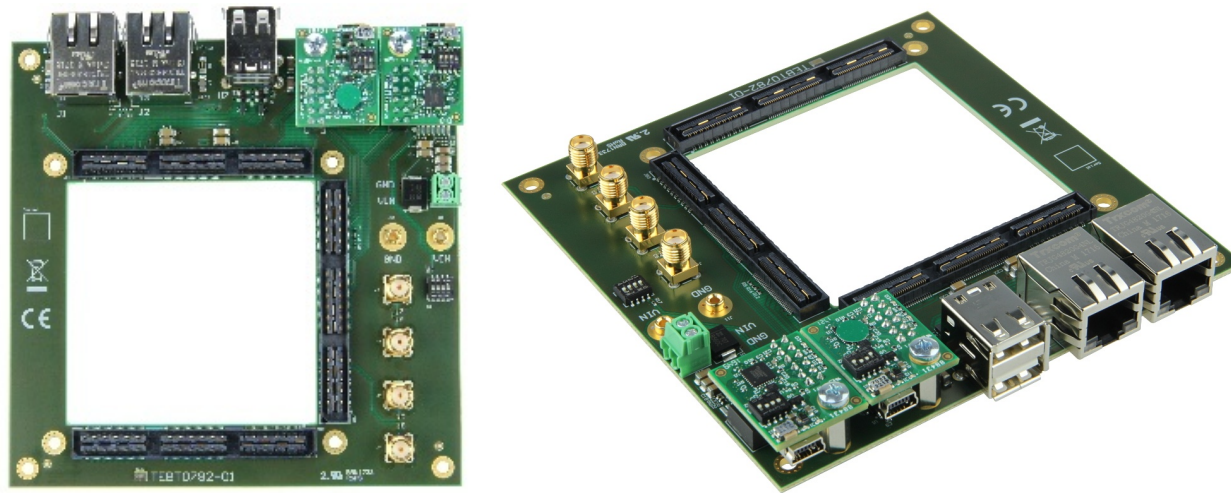


TEBT0782 TRM

Created by Thorsten Trenz, last modified by John Hartfiel on 13 03, 2018



TEBT0782 Power supply

12V Power Supply (VIN) for TEBT0782 on J9/J11 (2 mm MC LB2-A solder-in) or on J10 (TE1.5/2-3.5H).

TEBT0782 DIP Settings

DIP	Description
S1-1	ON: JTAG routed into CPLD (currently not used), OFF: TE0782 CPLD Access
S1-2	N.C.
S1-3	N.C.
S1-4	N.C.

TE0790 DIP Settings

TE0790 DIP switch setting for use with TEBT0782

DIP	Default Setting
1	ON
2	OFF
3	OFF
4	OFF

XMOD access

CPLD JTAG is available over XMOD on J8.

FPGA JTAG is available over XMOD on J7.

FPGA UART is available over XMOD on J7.

Modul main reset is possible over XMOD Button on J7 XMOD.