<u>TinyLogic®</u>

FAIRCHILD

Fairchild's Offering

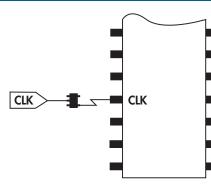
Fairchild's TinyLogic[®] family consists of a broad spectrum of high speed, low power, CMOS single and dual gate logic functions in a choice of six space saving packages: SOT23-5, SC70 6-lead, US8 8-lead, and MicroPak 6 and 8 terminal leadless packages.

TinyLogic can facilitate efficient system designs in any application. Placement of single and dual logic functions exactly where needed simplifies signal routing while minimizing propagation delays and noise generation.

Benefits

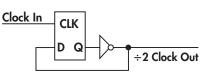
- Reduces routing complexity
- Allows shorter metal traces to minimize EMI generation
- Prevents unnecessary power consumption from unused gates
- HS and HST family intended for 5V or very low speed applications
- UHS family ideal for high speed, low voltage operation
- ULP family ideal for extremely high speed low voltage operation
- ULPA family ideal for low power consumption, low voltage operation

Single Gate for Added Clock Frequency and Buffering



Use a single-gate inverter, such as NC7SZ04M5 or NC7SZ14M5, to clean clock signals traveling lengthy distances across a board or when signals are compromised by layout conditions. Buffering a clock signal near its destination ensures signal integrity.

Utilizing Fairchild's TinyLogic parts NC7SZ374 and NC7SZ04 enables the additional clock.



Family Comparison

Family	Standard Logic Family Equivalent	l _{cc} (μΑ)	$I_{cc}(\mu A) = V_{cc}(V)$ Drive (n		Speed (ns @ V)					
HS	HC	10	2–6	±1.1 @ 3.0; ±2.0 @ 4.5	25 @ 4.5					
HST	НСТ	10	4.5-5.5	±2.0 @ 4.5	20@2.0					
UHS	LCX/LVC	20	1.8–5.5	±4.0 @ 1.65; ±24.0 @ 3.3	4.7@3.3					
ULP		0.9	0.9–3.6	±1.0 @ 1.5; ±2.6 @ 3.0	16@1.5;7.0@3.3					
ULP-A	VCX	0.9	0.9–3.6	±4.0 @ 1.5; 24.0 @ 3.3	7.2@1.5					











TinyLogic[®]

S	Single-Bit Logic	Family				Packages						
Functional W	Dual-Bit Logic Triple-Bit Logic	Device Type	HS (NC7)	HST (NC7×T)	UHS (NC7xZ)	ULP/ ULP-A (NC7xP/ NC7xV)	SOT23 5-lead	SC70 5-lead	SC70 6-lead	US8 8-lead	MicroPak 6-lead	MicroPak 8-lead
NAND Gate		00	S	S	SW	S W	S	S		\mathbb{W}	S	W
NOR Gate		02	S	S	SW	S W	S	S		W	S	W
Inverter		04	S	S	SWN	SWN	S	S	W	N	SW	N
Unbuffered Inverter	-	U04	S		SWN	S	S	S	W	N	SW	N
Inverter w/ Open [Drain Output	05			S	S	S	S			S	
Buffer w/ Open Dr	ain Output	07			W	W			W		W	
AND Gate		08	S	S	SW	SW	S	S		W	S	W
3-Input NAND Gat	e	10			S				S		S	
3-Input AND Gate		11			S	S			S		S	
Inverter w/ Schmitt	Trigger Input	14	S		SWN	SWN	S	S	W	N	SW	N
Dual Buffer		16			W				W		W	
Buffer w/ Schmitt T	rigger Input	17			WN	SW		S	W	N	SW	N
1 of 2 Demux w/ 3		18			S				S		S	
1 of 2 Decoder/De		19				S			S		S	
3-Input NOR Gate		27			S				S		S	
OR Gate		32	S	S	SW	SW		S		W	S	W
Buffer		34			N	S N		S		N	S	N
NAND Gate w/ Open Drain Output		38			s w	s w		S	S	W	S	W
Universal Configurable 2-Input Gate		57			S	S			S		S	
Universal Configura 2-Input Gate	able	58			S	S					S	
D Flip-Flop w/ Pre-	Set and Clear	74			S	S				S		S
XOR Gate		86	S	S	SW	S W	S	S		W	S	W
Buffer w/ Low-Enabled 3-STATE Output		125			s w	S W	S	S		\mathbb{W}	S	\mathbb{W}
Buffer w/ High-Enabled 3-STATE Output		126			S W	S W	S	S		W	S	W
NAND Gate w/ So Trigger Input	chmitt	132			W	W				W		W
2-Input Non-Inverti	ng Multiplexer	157			S	S			S		S	
2-Input Inverting Multiplexer		158				S			S		S	
D Flip-Flop w/ Asynchronous Clear		175			S				S		S	
Inverting Buffer w/ 3-STATE Output		240			W	W				W		W
Inverting Buffer w/ Low-Enabled 3-STA		241			W	W				W		W
3-Input OR Gate		332			S				S		S	
D Latch w/ 3-STATI	E Output	373			S				S		S	
D Flip-Flop w/ 3-S1	TATE Output	374			S				S		S	
3-Input XOR Gate	•	386			S				S		S	

Note: **x** is a variable (either S, W, or N).





Ordering Guide		NC7	хх	х	XX	хх	х
TinyLogic			T	\top		\top	\top
Guide		<u> </u>					
S = Single-Bit	"blank" = HS	1					
W = Dual-Bit	T = HST						
N = Triple-Bit	Z = UHS						
	P = ULP						
	V = ULP-A						
Function Descri	otion	<u> </u>					
D = Diode		1					
U = Unbuffered]					
Device Type		<u> </u>					
Package Code							
M5 = 5-lead SOT	23-5]					
P5 = 5-lead SC70)						
P6 = 6-lead SC70)						
I6 = 6-terminal M	icroPak						
I8 = 8-terminal M	icroPak						
K8 = 8-lead US8							
Special Variatio	ons	<u> </u>					
X = tape and reel	in quantities of 3000 and 5000]					
"blank" = tape	and reel in quantities of 250						

					••••	
Measurements (mm)	SOT23-5	SC70-5	SC70-6	MicroPak-6	MicroPak-8	US8-8
Mounted Width	3.00	2.10	2.10	1.00	1.60	3.10
Body Width	1.70	1.25	1.25	1.00	1.60	2.30
Length	3.00	2.00	2.00	1.45	1.60	2.00
Height	1.40	0.90	0.90	0.55	0.55	0.70
Pin Pitch	0.95	0.65	0.65	0.50	0.50	0.50



TinyLogic[®]

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TRIACs

TRIACs

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- Video Switches
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