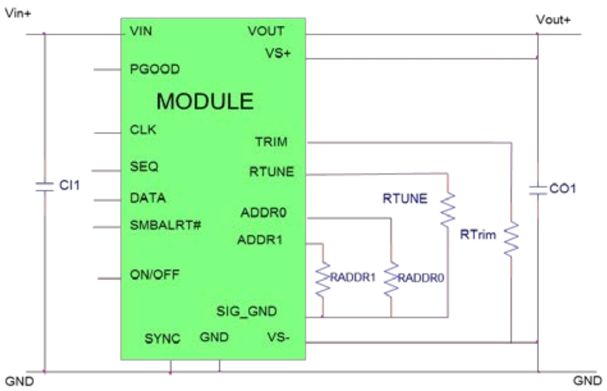
80A GigaDLynx^{TM}: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.5Vdc to 2.0Vdc output; 80A Output Current

RoHS Compliant

Applications

- Networking equipment
- Telecommunications equipment
- Servers and storage applications
- Distributed power architectures
- Intermediate bus voltage applications
- Industrial equipment



Features

- Compliant to RoHS EU Directive 2002/95/EC (Z versions)
- Compliant to IPC-9592 (September 2008), Category 2, Class II
- Compatible in a Pb-free or SnPb reflow environment (Z versions)
- Wide Input voltage range (4.5Vdc-14Vdc)
- Output voltage programmable from 0.6Vdc to 2.0Vdc via external resistor. Digitally adjustable down to 0.5Vdc output.
- Digital interface through the PMBus^{TM} # protocol
- Digital output voltage control loop
- Remote On/Off
- Digital Sequencing
- Power Good signal
- Fixed switching frequency with capability for external synchronization
- Ability to sink and source current
- Output overcurrent protection (non-latching)
- Over temperature protection
- Cost efficient open frame design
- Small size: 33.02mm x 22.86mm x 12.7mm [1.3" x 0.9" x 0.5"]
- Wide operating temperature range [-40°C to 85°C]
- *UL** 60950-1 2nd Ed. Recognized, *CSA*† C22.2 No. 60950-1-07 Certified, and *VDE*‡ (EN60950-1 2nd Ed.) Licensed
- ISO** 9001 and ISO 14001 certified manufacturing facilities

Description

The 80A Digital GigaDLynx^{TM} power modules are non-isolated dc-dc converters that deliver up to 80A of output current. These modules operate over a wide range of input voltage (V_{IN} =4.5Vdc - 14Vdc) and provide a precisely regulated output voltage from 0.6Vdc to 2Vdc, programmable via an external resistor and/or PMBus control. Features include a digital interface using the PMBus protocol, remote On/Off, adjustable output voltage, over current, over voltage and over temperature protection. The PMBus interface supports many commands to both control and monitor the module. The module also include a digital output voltage control loop that allows optimizing the dynamic response of the converter with reduced amounts of output capacitance leading to savings on cost and PWB area.

* *UL* is a registered trademark of Underwriters Laboratories, Inc.
† *CSA* is a registered trademark of Canadian Standards Association.
‡ *VDE* is a trademark of Verband Deutscher Elektrotechniker e.V.
** ISO is a registered trademark of the International Organization of Standards
# The PMBus name and logo are registered trademarks of the System Management Interface Forum (SMIF)

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Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the technical requirements. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

Parameter	Device	Symbol	Min	Max	Unit
Input Voltage Continuous	All	V_{IN}	-0.3	15	V
SEQ, ADDR0, ADDR1, RTUNE, VTRACK				2.0	V
VS+	All			3.0	V
ON/OFF				15	V
SYNC, CLK, DATA, SMBALERT#, PGOOD	All			5.5	V
Operating Ambient Temperature (see Thermal Considerations section)	All	T_A	-40	85	°C
Storage Temperature	All	T_{stg}	-55	125	°C

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Parameter	Device	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	All	V_{IN}	4.5	—	14	Vdc
Maximum Input Current (V_{IN}=4.5V to 14V, I_O=$I_{O,\ max}$)	All	$I_{IN,max}$			46	Adc
Input No Load Current (V_{IN} = 12Vdc, I_O = 0, module enabled)	$V_{O,set}$ = 0.6 Vdc	$I_{IN,No\ load}$		145		mA
	$V_{O,set}$ = 2.0Vdc	$I_{IN1No\ load}$		190		mA
Input Stand-by Current (V_{IN} = 12Vdc, module disabled)	All	$I_{IN,stand\text{-}by}$		45		mA
Inrush Transient	All	I^2t		1		A^2s
Input Noise on nominal output (VIN=VIN, nom and IO=IO, min to IO, Cin = TBD) Peak-to-Peak (Full Bandwidth)	All				500	mVpp
Input Reflected Ripple Current, peak-to-peak (5Hz to 20MHz, 1μH source impedance; V_{IN} =0 to 14V, I_O= I_{Omax} ; See Test Configurations)	All			40		mAp-p
Input Ripple Rejection (120Hz)	All			-55		dB

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Electrical Specifications (continued)

Parameter	Device	Symbol	Min	Typ	Max	Unit
Output Voltage Set-point (with 0.1% tolerance for external resistor used to set output voltage)	0 to 70°C	$V_{O, set}$	-0.5		+0.5	% $V_{O, set}$
	-40 to +85°C		-1.0		+1.0	
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life)	All	$V_{O, set}$	-2.5	—	+2.5	% $V_{O, set}$
Adjustment Range (selected by an external resistor) (Some output voltages may not be possible depending on the input voltage – see Feature Descriptions Section)	All	V_{OUT}	0.6		2.0	Vdc
PMBus Adjustable Output Voltage Range	All	$V_{O,adj}$	0.5	-	2.0	%$V_{O,set}$
PMBus Output Voltage Adjustment Step Size	All		0.4			%$V_{O,set}$
Remote Sense Range	All				0.4	Vdc
Output Regulation						
Line (V_{IN}=$V_{IN, min}$ to $V_{IN, max}$)	All			—	4	mV
Load (I_O=$I_{O, min}$ to $I_{O, max}$)	All			—	5	mV
Temperature (T_{ref}=$T_{A, min}$ to $T_{A, max}$)	All			0.4		%$V_{O,set}$
Output Ripple and Noise on nominal output (V_{IN}=$V_{IN, nom}$ and I_O=$I_{O, min}$ to $I_{O, max}$ Co = TBD, Cin = TBD						
Peak-to-Peak (Full bandwidth)					30	mV_{pk-pk}
RMS (Full bandwidth)	All				12	mV_{rms}
External Capacitance						
Minimum output capacitance (ESR ≥ TBD mΩ)	All	$C_{O,min}$	470	—	—	μF
Maximum output capacitance (ESR ≥ TBD mΩ)	All	$C_{O, max}$	—	—	16000	μF
Output Current (in either sink or source mode)	All	I_o	0		80	Adc
Output Current Limit Inception (Hiccup Mode) (current limit does not operate in sink mode)	All	$I_{O, lim}$		91		Adc
Output Short-Circuit Current (V_O≤250mV) (Hiccup Mode)	All	$I_{O, s/c}$		TBD		Arms
Efficiency	$V_{O,set}$ = 0.6Vdc	η		82.4		%
V_{IN}= 12Vdc, T_A=25°C	$V_{O, set}$ = 0.8Vdc	η		85.7		%
I_O=$I_{O, max}$, V_O= $V_{O,set}$	$V_{O,set}$ = 1.0Vdc	η		88.1		%
	$V_{O,set}$ = 1.2Vdc	η		89.6		%
	$V_{O, set}$ = 1.5Vdc	η		91.2		%
	$V_{O,set}$ = 2.0Vdc	η		92.8		%
Switching Frequency	All	f_{sw}	-	400	-	kHz
Frequency Synchronization	All					
Synchronization Frequency Range	All		-10		+10	%
High-Level Input Voltage	All	$V_{IH,SYNC}$	2.0			V
Low-Level Input Voltage	All	$V_{IL,SYNC}$			0.4	V
Minimum Pulse Width, SYNC	All	t_{SYNC}	50			ns

General Specifications

Parameter	Device	Min	Typ	Max	Unit
Calculated MTBF (I_O=0.8$I_{O, max}$, T_A=40°C) Telecordia Issue 2 Method 1 Case 3	All		TBD		Hours
Weight		—	TBD	—	g (oz.)

Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Device	Symbo	Min	Typ	Max	Unit
On/Off Signal Interface						
(V_{IN}=$V_{IN, min}$ to $V_{IN, max}$; open collector or equivalent,						
Signal referenced to GND)						
Device Code- XXXXXX Negative Logic (See Ordering Information)						
(On/OFF pin is open collector/drain logic input with						
external pull-up resistor; signal referenced to GND)						
Logic High (Module OFF)						
Input High Current	All	I_{IH}	—	—	1	mA
Input High Voltage	All	V_{IH}	2	—	$V_{IN, max}$	Vdc
Logic Low (Module ON)						
Input low Current	All	I_{IL}	—	—	10	μA
Input Low Voltage	All	V_{IL}	-0.2	—	0.4	Vdc
Device Code- XXXXXX Positive Logic (See Ordering Information)						
(On/OFF pin is open collector/drain logic input with						
external pull-up resistor; signal referenced to GND)						
Logic High (Module OFF)						
Input High Current	All	I_{IH}	—	—	10	uA
Input High Voltage	All	V_{IH}	2	—	$V_{IN, max}$	Vdc
Logic Low (Module ON)						
Input low Current	All	I_{IL}	—	—	10	μA
Input Low Voltage	All	V_{IL}	-0.2	—	0.4	Vdc
Turn-On Delay and Rise Times						
(V_{IN}=$V_{IN, nom}$, I_O=$I_{O, max}$, V_O to within ±1% of steady state)						
Case 1: On/Off input is enabled and then input power is applied (delay from instant at which V_{IN} = $V_{IN, min}$ until V_O = 10% of $V_{O, set}$)	All	Tdelay	—	5.0	—	ms
Case 2: Input power is applied for at least one second and then the On/Off input is enabled (delay from instant at which Von/Off is enabled until V_O = 10% of $V_{O, set}$)	All	Tdelay	—	500	—	μs
Output voltage Rise time (time for V_O to rise from 10% of Vo, set to 90% of Vo, set)	All	Trise	—	2.0	—	msec
Output voltage overshoot (T_A = 25°C V_{IN}= $V_{IN, min}$ to $V_{IN, max}$,I_O = $I_{O, min}$ to $I_{O, max}$) With or without maximum external capacitance		Output			3.0	% $V_{O, set}$
Over Temperature Protection (See Thermal Considerations section)	Vin ≤ 6.5V	T_{ref}		105		°C
	Vin > 6.5V			125		
PMBus Over Temperature Warning Threshold*	Vin ≤ 6.5V	T_{WARN}		95		°C
	Vin > 6.5V			115		
Tracking Accuracy ($V_{IN, min}$ to $V_{IN, max}$; $I_{O, min}$ to $I_{O, max}$ V_{SEQ} < V_O)						
(Power-Up: 0.5V/ms)	All	V_{SEQ} –V_O			100	mV
(Power-Down: 0.5V/ms)	All	V_{SEQ} –V_O			100	mV
Input Undervoltage Lockout						
Turn-on Threshold	All				4.4	Vdc
Turn-off Threshold	All		4.1			Vdc
Hysteresis	All			0.25		Vdc
PMBus Adjustable Input Under Voltage Lockout Thresholds	All		4.5		14	Vdc
Resolution of Adjustable Input Under Voltage Threshold	All				10	mV

* Over temperature Warning – Warning may not activate before alarm and unit may shut down before warning.

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Feature Specifications (cont.)

Parameter	Device	Symbol	Min	Typ	Max	Units
PGOOD (Power Good)						
Signal Interface Open Drain, $V_{supply} \leq$ 5VDC						
Overvoltage threshold for PGOOD ON	All			108		$\%V_{O, set}$
Overvoltage threshold for PGOOD OFF	All			110		$\%V_{O, set}$
Undervoltage threshold for PGOOD ON	All			92		$\%V_{O, set}$
Undervoltage threshold for PGOOD OFF	All			90		$\%V_{O, set}$
Pulldown resistance of PGOOD pin	All				TBD	Ω
Sink current capability into PGOOD pin	All				29	mA

* Over temperature Warning – Warning may not activate before alarm and unit may shut down before warning.

Digital Interface Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
PMBus Signal Interface Characteristics						
Input High Voltage (CLK, DATA)		V_{IH}	2.1			V
Input Low Voltage (CLK, DATA)		V_{IL}			0.8	V
Input high level current (CLK, DATA)		I_{IH}	-10		10	μA
Input low level current (CLK, DATA)		I_{IL}	-10		10	μA
Output Low Voltage (CLK, DATA, SMBALERT#)	I_{OUT}=2mA	V_{OL}			0.4	V
Output high level open drain leakage current (DATA, SMBALERT#)	V_{OUT}=3.6V	I_{OH}	0		10	μA
Pin capacitance		C_O			10	pF
PMBus Operating frequency range	Slave Mode	F_{PMB}	10		400	kHz
Data hold time		$t_{HD:DAT}$	300			μs
Data setup time		$t_{SU:DAT}$	100			ns
Measurement System Characteristics						
Read delay time		t_{DLY}	153	192	231	μs
Output current measurement range		I_{RNG}	0		100	A
Output current measurement resolution		I_{RES}	197			mA
Output current measurement gain accuracy	0°C to 85°C	I_{ACC}			±5	% of Io,max
	-40°C to +85°C		-3		+7	A
Output current measurement offset		I_{OFST}			0.2	A
V_{OUT} measurement range		V_{OUT}	0.5		2.0	V
V_{OUT} measurement resolution		$V_{OUT(res)}$		0.7		mV
V_{OUT} measurement accuracy		$V_{OUT(gain)}$		±1		%
V_{OUT} measurement offset		$V_{OUT(ofst)}$	-5		5	mV
V_{IN} measurement range		$V_{IN(rng)}$	0		14	V
V_{IN} measurement resolution		$V_{IN(res)}$		7.0		mV
V_{IN} measurement offset		$V_{IN(ofst)}$		TBD		%
V_{IN} measurement accuracy		V_{IN}		±1		%

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Characteristic Curves

The following figures provide typical characteristics for the 80A Digital GigaDLynx™ at 0.6Vo and 25°C.

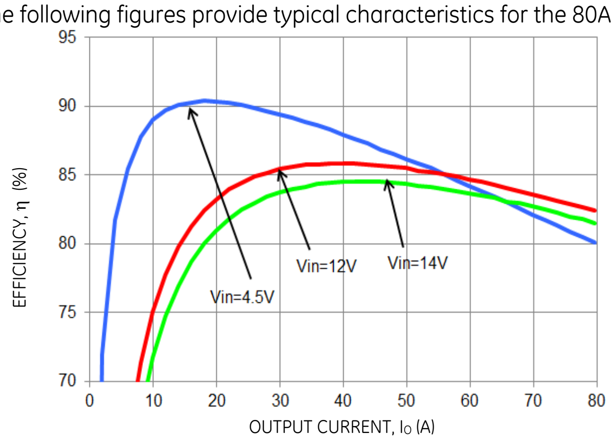


Figure 1. Converter Efficiency versus Output Current.

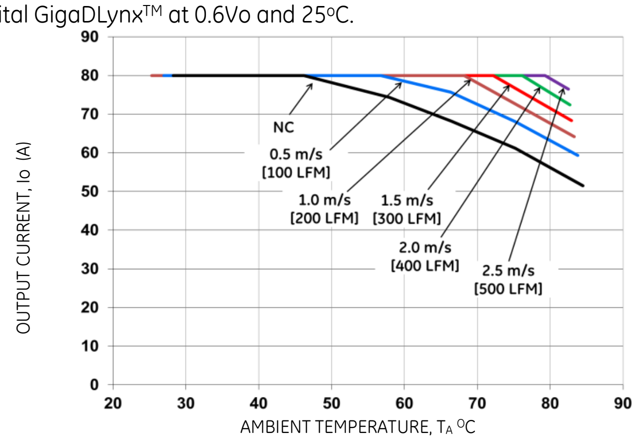


Figure 2. Derating Output Current versus Ambient Temperature and Airflow.

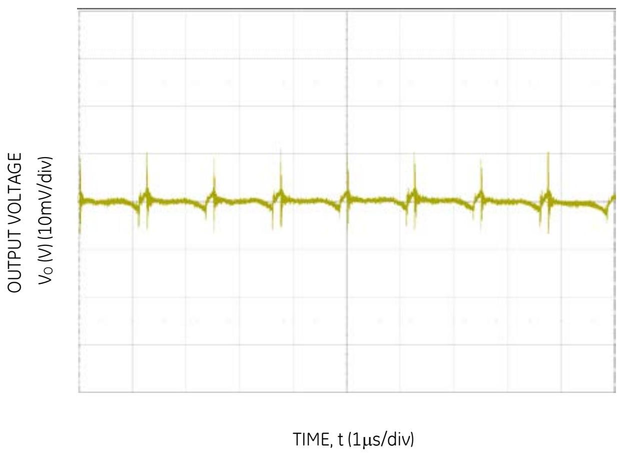


Figure 3. Typical output ripple and noise (Co= 6x47μF ceramic, VIN = 12V, Io = Io,max,).

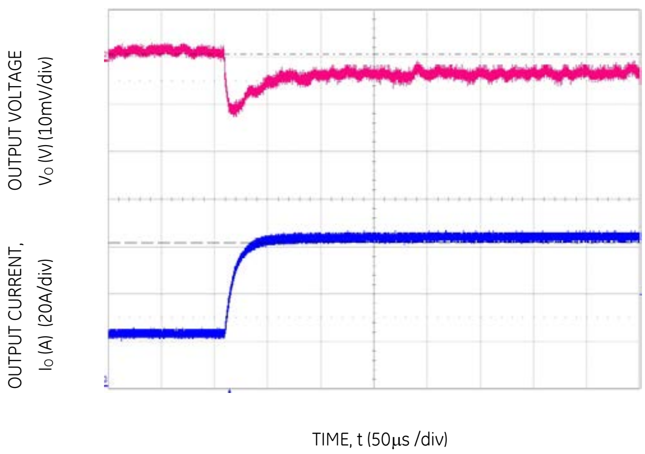


Figure 4. Transient Response to Dynamic Load Change from 25% to 75% at 12Vin, Co= 36x 47μF + 14x 1000μF, RTUNE = 4.22kΩ

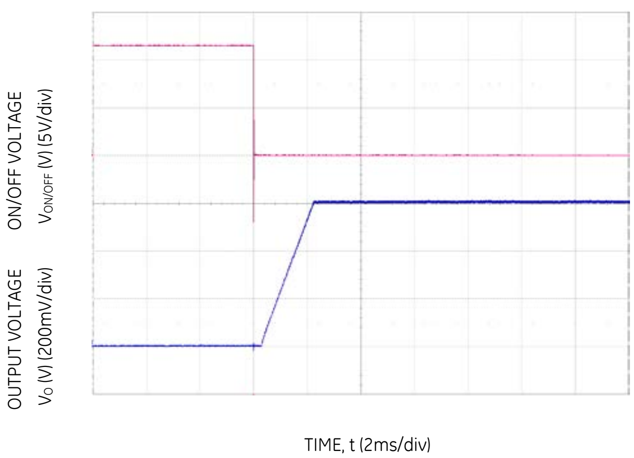


Figure 5. Typical Start-up Using On/Off Voltage (Io = Io,max).

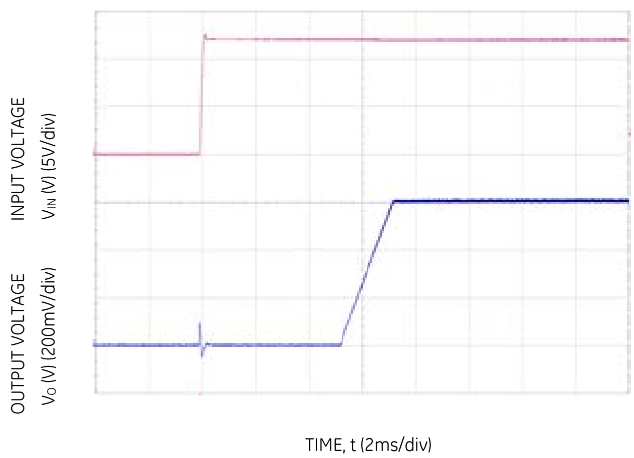


Figure 6. Typical Start-up Using Input Voltage (VIN = 12V, Io = Io,max).

Characteristic Curves

The following figures provide typical characteristics for the 80A GigaDLynxTM at 1.0Vo and 25°C

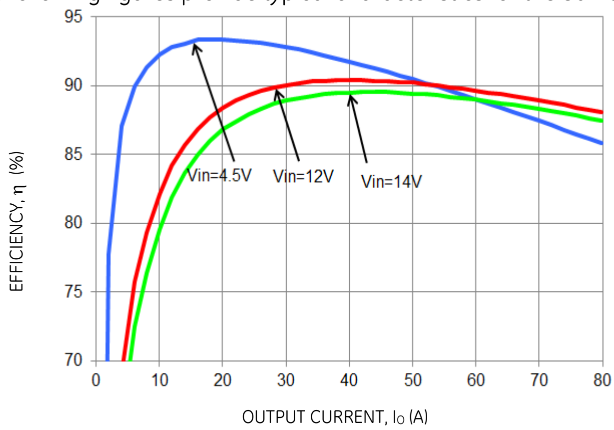


Figure 7. Converter Efficiency versus Output Current.

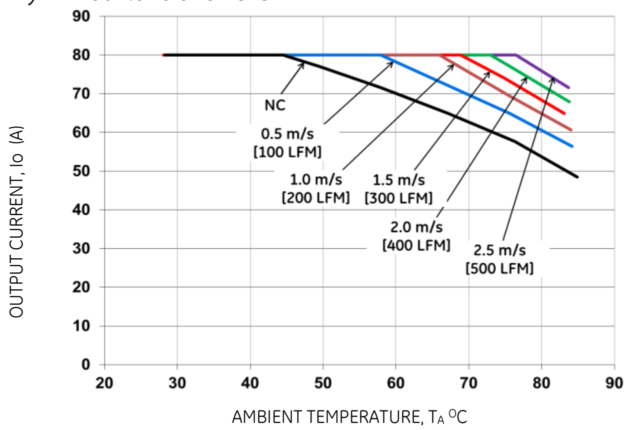


Figure 8. Derating Output Current versus Ambient Temperature and Airflow.

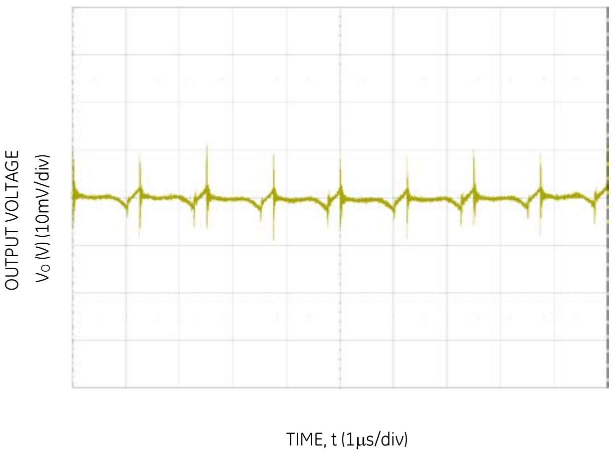


Figure 9. Typical output ripple and noise (C_O= 6x47μF Ceramic, V_{IN} = 12V, I_o = $I_{o,max}$,).

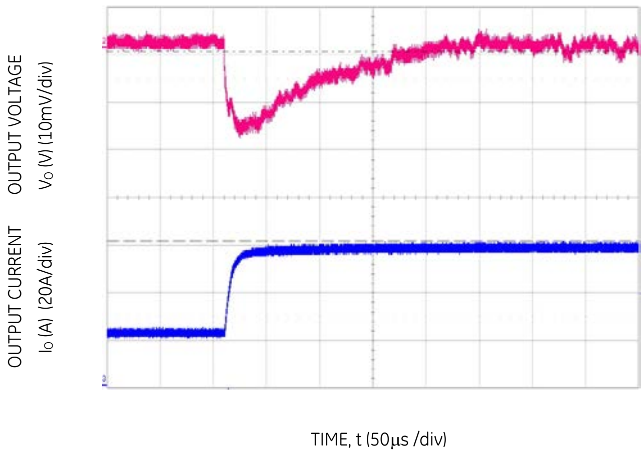


Figure 10. Transient Response to Dynamic Load Change from 25% to 75% at 12Vin, Co= 30x 47μF + 11x 1000μF, R_{TUNE} = 3.74kΩ

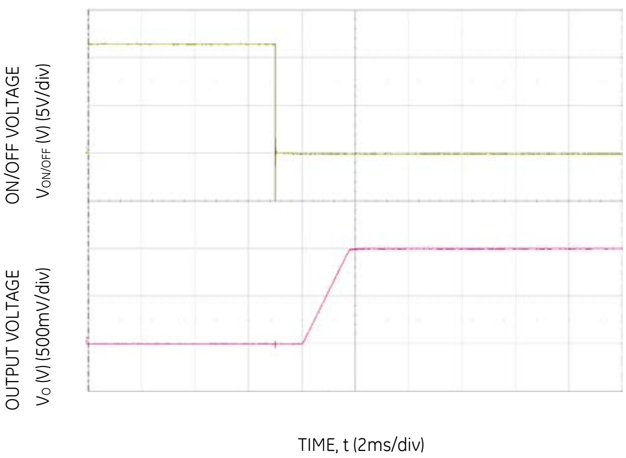


Figure 11. Typical Start-up Using On/Off Voltage (I_o = $I_{o,max}$).

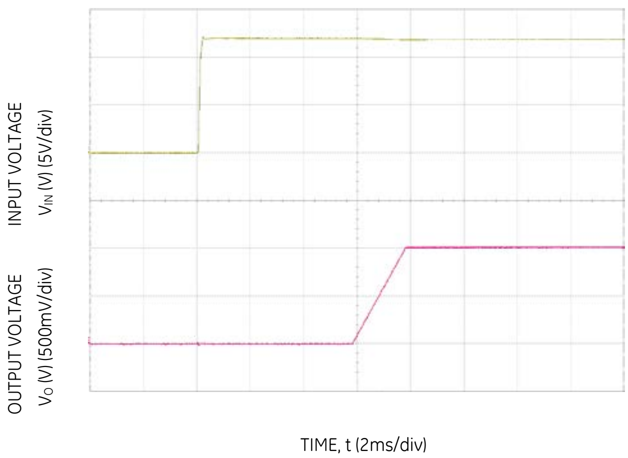


Figure 12. Typical Start-up Using Input Voltage (V_{IN} = 12V, I_o = $I_{o,max}$).

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Characteristic Curves

The following figures provide typical characteristics for the 80A Digital GigaDLynx™ at 1.2Vo and 25°C.

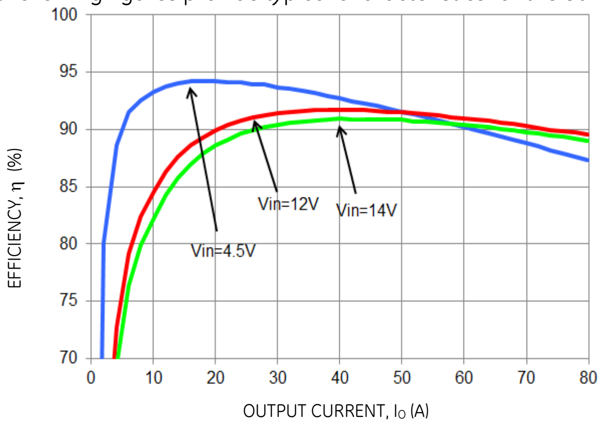


Figure 13. Converter Efficiency versus Output Current.

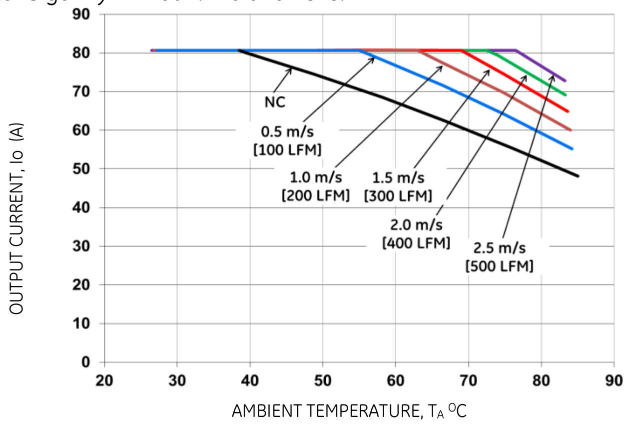


Figure 14. Derating Output Current versus Ambient Temperature and Airflow.

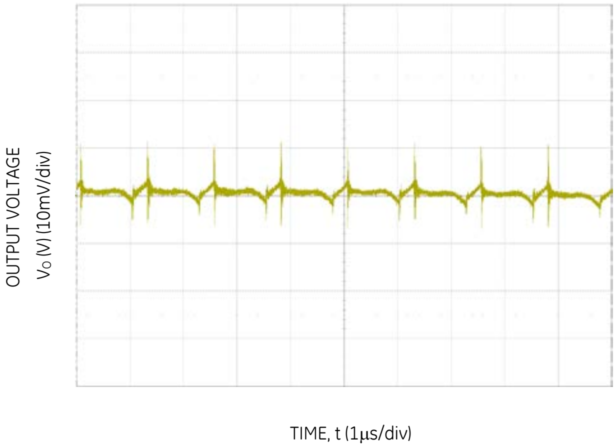


Figure 15. Typical output ripple and noise (Co=6x47μF ceramic, V_{IN} = 12V, I_o = $I_{o,max}$,).

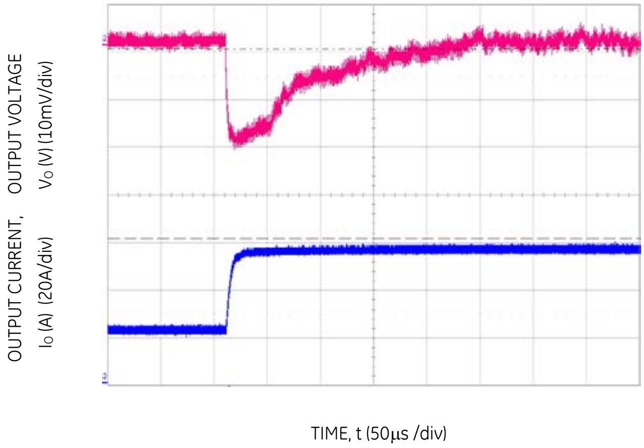


Figure 16. Transient Response to Dynamic Load Change from 25% to 75% at 12Vin, Co= 26x 47μF + 9x 1000μF, R_{TUNE} = 3.24kΩ

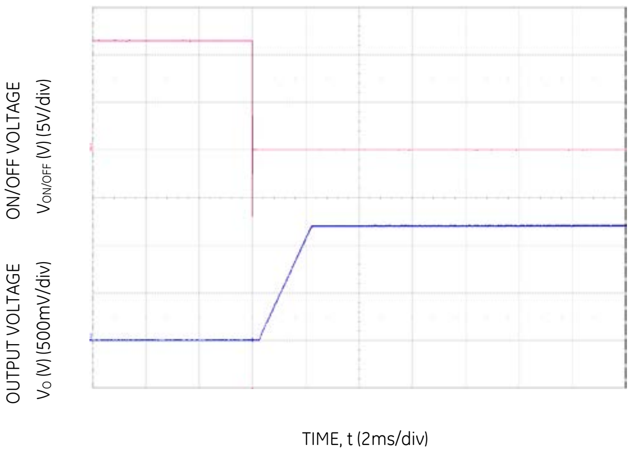


Figure 17. Typical Start-up Using On/Off Voltage (I_o = $I_{o,max}$).

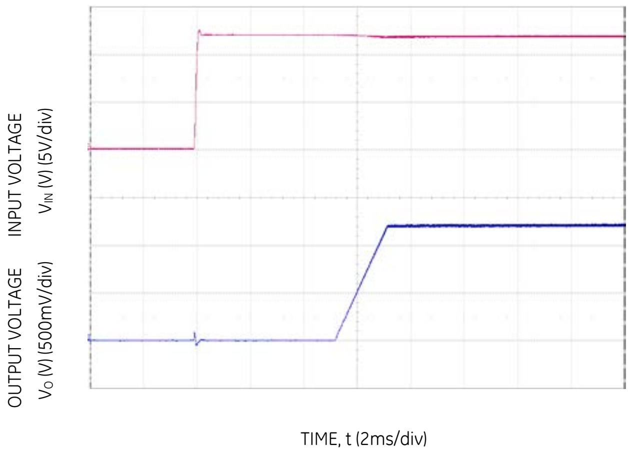


Figure 18. Typical Start-up Using Input Voltage (V_{IN} = 12V, I_o = $I_{o,max}$).

80A GigaDLynxTM: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Characteristic Curves

The following figures provide typical characteristics for the 80A Digital GigaDLynxTM at 1.5Vo and 25°C.

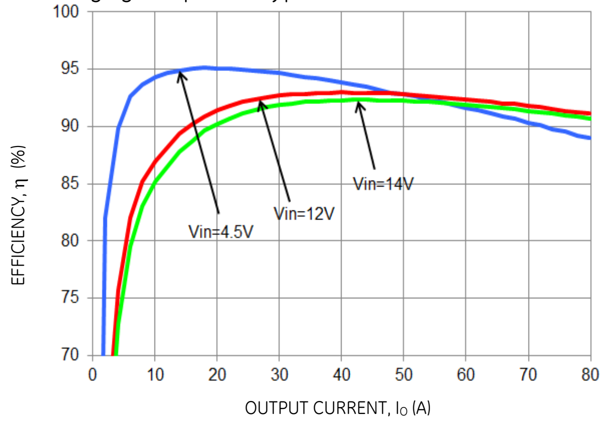


Figure 19. Converter Efficiency versus Output Current.

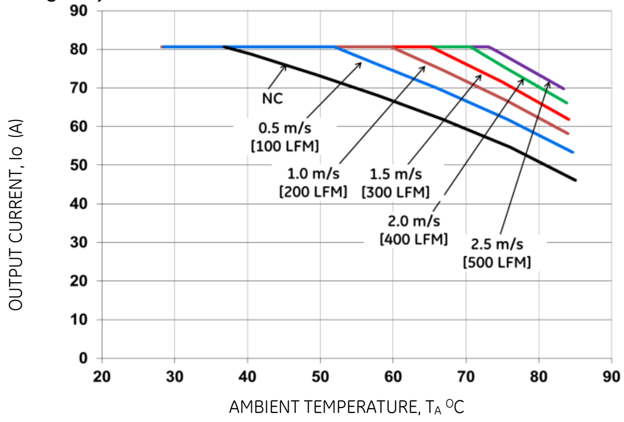


Figure 20. Derating Output Current versus Ambient Temperature and Airflow.

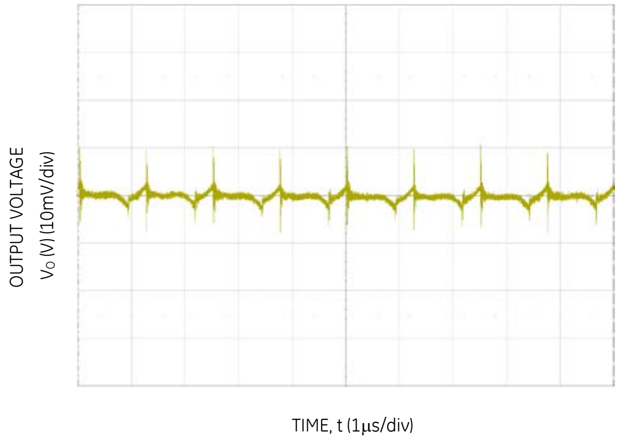


Figure 21. Typical output ripple and noise (C_O= 6x47μF ceramic, V_{IN} = 12V, I_o = $I_{o,max}$,).

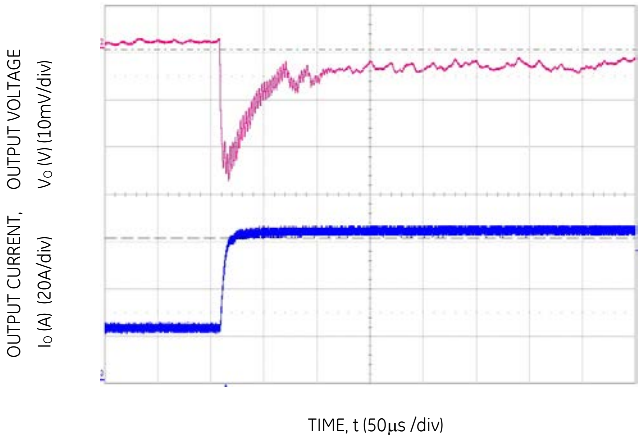


Figure 22. Transient Response to Dynamic Load Change from 25% to 75% at 12Vin, Co= 25x 47μF + 8x 1000μF, R_{TUNE} = 6.81kΩ

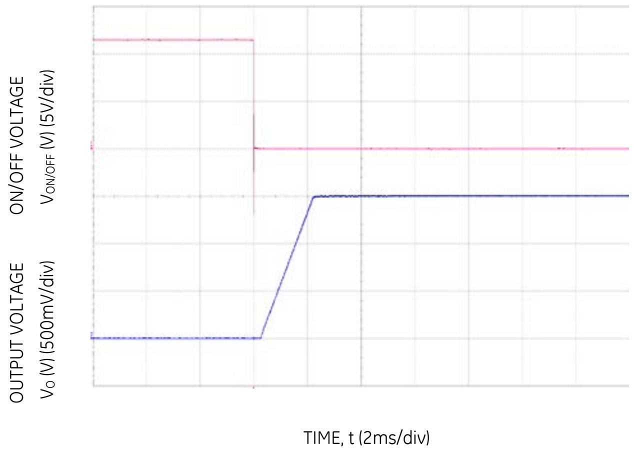


Figure 23. Typical Start-up Using On/Off Voltage (I_o = $I_{o,max}$).

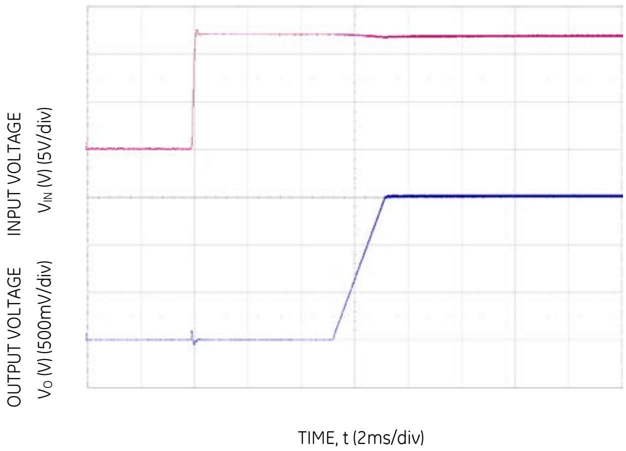


Figure 24. Typical Start-up Using Input Voltage (V_{IN} = 12V, I_o = $I_{o,max}$).

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Characteristic Curves

The following figures provide typical characteristics for the 80A Digital GigaDLynx™ at 2.0Vo and 25°C.

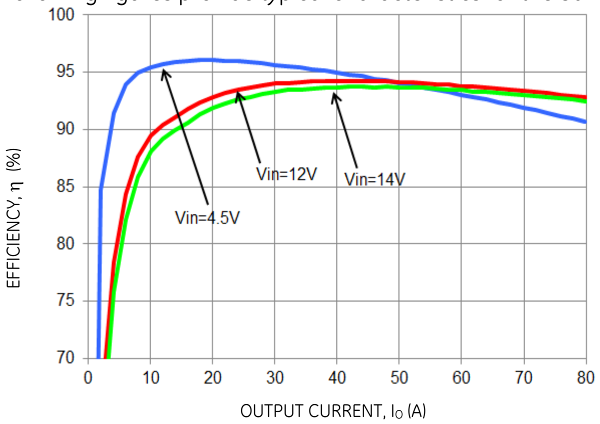


Figure 25. Converter Efficiency versus Output Current.

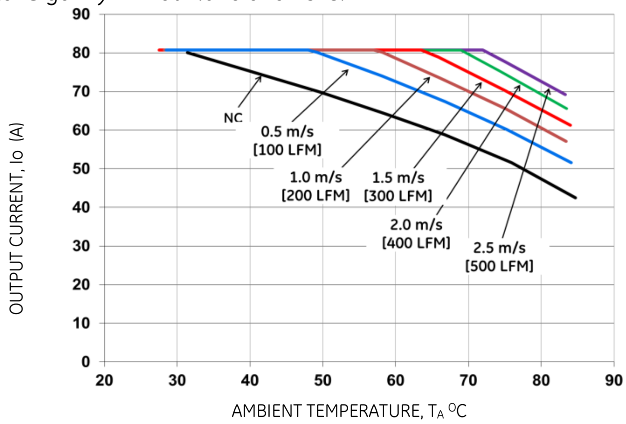


Figure 26. Derating Output Current versus Ambient Temperature and Airflow.

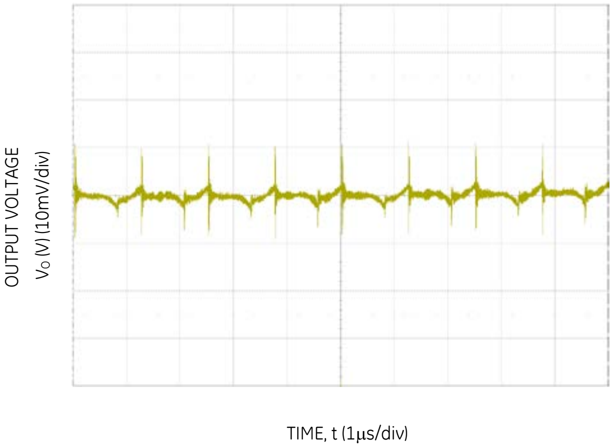


Figure 27. Typical output ripple and noise (C_O= 6x47μF ceramic, V_{IN} = 12V, I_o = $I_{o,max}$,).

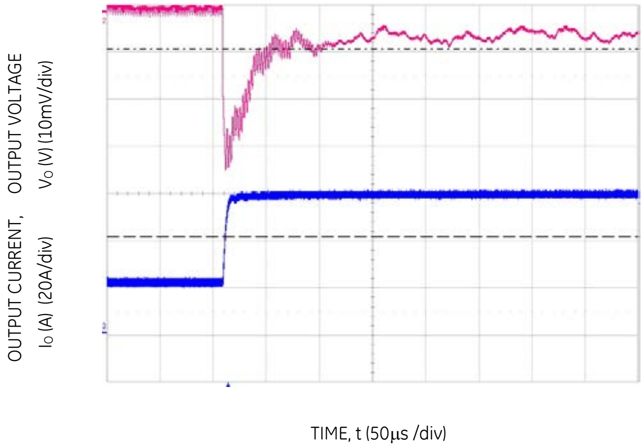


Figure 28. Transient Response to Dynamic Load Change from 25% to 75% at 12Vin, Co= 20x 47μF + 7x 1000μF, R_{TUNE} = 6.04KΩ

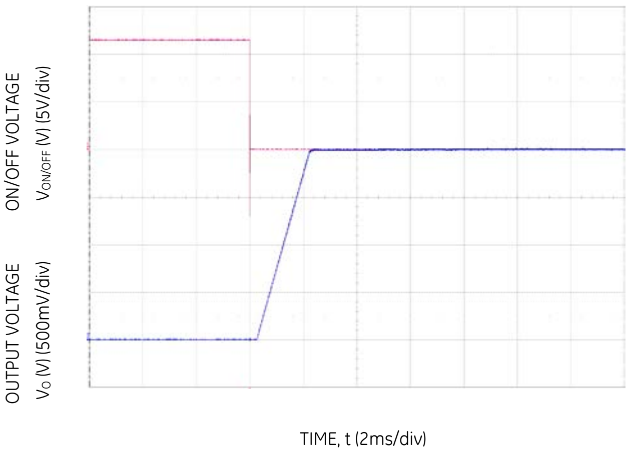


Figure 29. Typical Start-up Using On/Off Voltage (I_o = $I_{o,max}$).

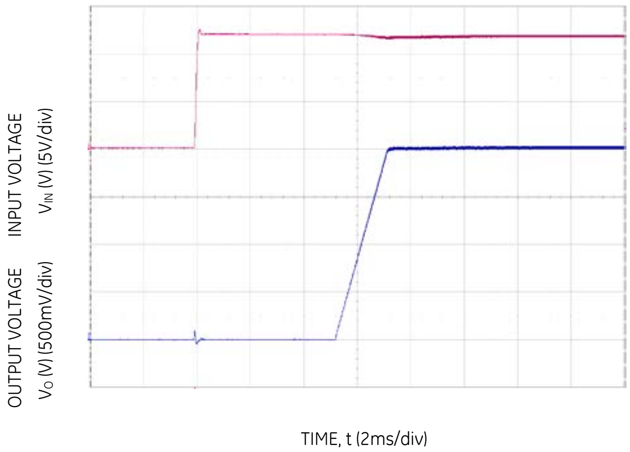


Figure 30. Typical Start-up Using Input Voltage (V_{IN} = 12V, I_o = $I_{o,max}$).

Design Considerations

Input Filtering

The 80A GigaDLynxTM module should be connected to a low ac-impedance source. A highly inductive source can affect the stability of the module. An input capacitance must be placed directly adjacent to the input pins of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, ceramic capacitors are recommended at the input of the module. Figure 37 shows the input ripple voltage for various output voltages at 12A of load current with 2x22 µF or 3x22 µF ceramic capacitors and an input of 12V.

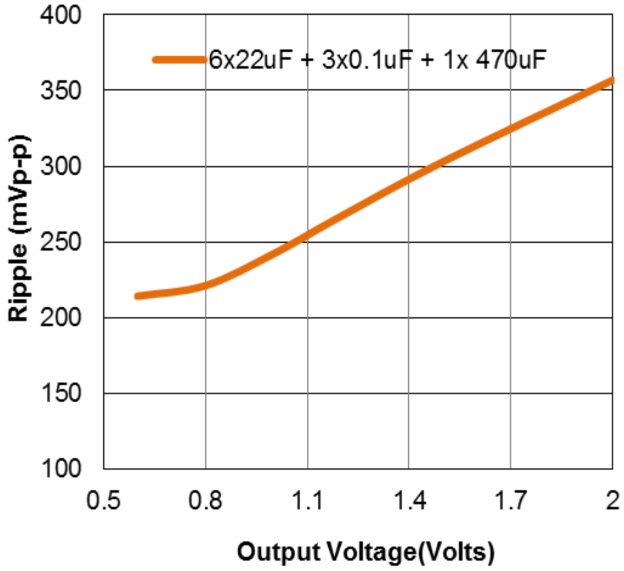


Figure 31. Input ripple voltage for various output voltages with (3 x 0.1µF + 6 x 22 µF) ceramic + 1 x 470µF OSCON electrolytic capacitor at the input (80A load). Input voltage is 12V.

Output Filtering

These modules are designed for low output ripple voltage and will meet the maximum output ripple specification with minimum of 6x47µF ceramic capacitors at the output of the module. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR polymer and ceramic capacitors are recommended to improve the dynamic response of the module.

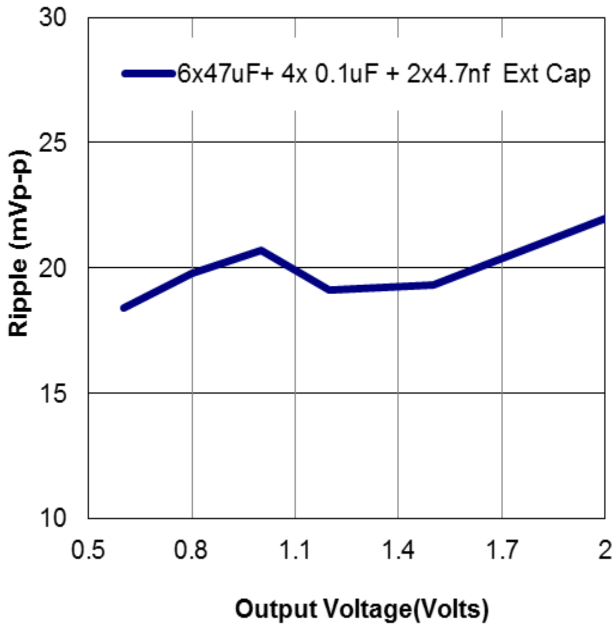


Figure 32. Output ripple voltage for various output voltages with external 6 x 47µF + 4 x 0.1 µF + 2 x 4.7nF ceramic capacitors at the output (80A load). Input voltage is 12V.

Safety Considerations

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL 60950-1 2nd, CSA C22.2 No. 60950-1-07, DIN EN 60950-1:2006 + A11 (VDE0805 Teil 1 + A11):2009-11; EN 60950-1:2006 + A11:2009-03.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

These units are to be protected with a TBD fuse in the positive input path.

Analog Feature Descriptions

Remote On/Off

The GigaDLynx 80A module can be turned ON and OFF either by using the ON/OFF pin (Analog interface) or through the PMBus interface (Digital). The module can be configured in a number of ways through the PMBus interface to react to the ON/OFF input:

- Module ON/OFF is controlled only through the analog interface (digital interface ON/OFF commands are ignored)
- Module ON/OFF is controlled only through the PMBus interface (analog interface is ignored)
- Module ON/OFF is controlled by either the analog or digital interface

The default state of the module (as shipped from the factory) is to be controlled by the analog interface only. If the digital interface is to be enabled, or the module is to be controlled only through the digital interface, this change must be made through the PMBus. These changes can be made and written to non-volatile memory on the module so that it is remembered for subsequent use.

Analog On/Off

The 80A GigaDLynxTM power modules feature an On/Off pin for remote On/Off operation. With the Negative Logic On/Off option, (see Ordering Information), the module turns OFF during logic High and ON during logic Low. The On/Off signal should be always referenced to ground. Leaving the On/Off pin disconnected will turn the module ON when input voltage is present. With the positive logic on/off option, the module turns ON during logic high and OFF during logic low.

Digital On/Off

Please see the Digital Feature Descriptions section.

Monotonic Start-up and Shutdown

The module has monotonic start-up and shutdown behavior on the output voltage for any rated input voltage, output voltage and current, and operating temperature.

Startup considerations at low temperature.

GDT080 is able to handle specified full-load start-up for ambient temperatures above or equal to -20°C. Below -20°C ambient temperature, the load has to be limited to 75% of specified full-load.

Startup into Pre-biased Output

The module can start into a pre-biased output as long as the pre-bias voltage is 0.5V less than the set output voltage.

Analog Output Voltage Programming

The output voltage of the module is programmable to any voltage from 0.6 to 2.0 Vdc by connecting a resistor between the Trim and VS- pins of the module as shown in Fig 33.

Without an external resistor between the Trim and VS- pins, the output of the module will be 0.6 Vdc. The value of the trim resistor, R_{TRIM} for a desired output voltage, should be selected as per the following equation:

$$R_{TRIM} = \frac{Vref}{(Vout - Vref)} \times 2k\Omega$$

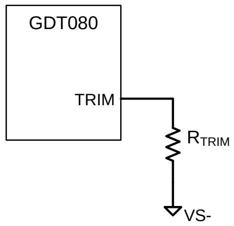


Figure 33. Circuit configuration for programming output voltage using an external resistor.

R_{TRIM} is the external resistor in kΩ

Vout is the desired output voltage.

Vref is the programmable internal reference

Table 1 provides Rtrim values required for some common output voltages.

Table 1

$V_{O,\,set}$ (V)	R_{TRIM} (KΩ)
0.6	Open
0.8	6.0
1.0	3.0
1.2	2.0
1.5	1.33
2.0	0.866

Digital Output Voltage Adjustment

Please see the Digital Feature Descriptions section.

Remote Sense

The power module has a differential Remote Sense feature to minimize the effects of distribution losses by regulating the voltage between the sense pins (VS+ and VS-). The voltage drop between the sense pins and the VOUT and GND pins of the module should not exceed 0.4V.

Output Voltage Sequencing

The power module includes a sequencing feature, EZ-SEQUENCE that enables users to implement various types of output voltage sequencing in their applications. This is accomplished via an additional sequencing pin. When not using the sequencing feature, leave it unconnected.

The voltage applied to the SEQ pin should be scaled down by the same ratio as used to scale the output voltage down to the reference voltage of the module. This is accomplished by an external resistive divider connected across the sequencing voltage before it is fed to the SEQ pin as shown in Fig 34. In addition, a small capacitor (suggested value 100pF) should be connected across the lower resistor R1.

For all DLynx modules, the minimum recommended delay between the ON/OFF signal and the sequencing signal is

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

10ms to ensure that the module output is ramped up according to the sequencing signal. This ensures that the module soft-start routine is completed before the sequencing signal is allowed to ramp up.

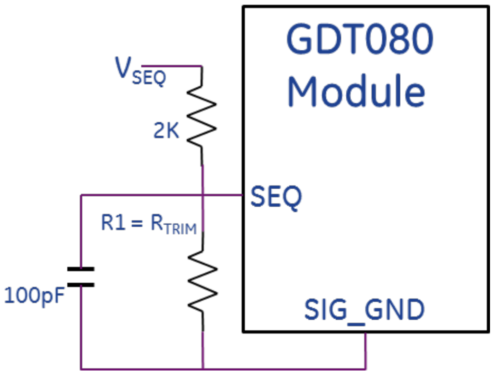


Figure 34. Circuit showing connection of the sequencing signal to the SEQ pin.

When the scaled down sequencing voltage is applied to the SEQ pin, the output voltage tracks this voltage until the output reaches the set-point voltage. The final value of the sequencing voltage must be set higher than the set-point voltage of the module. The output voltage follows the sequencing voltage on a one-to-one basis. By connecting multiple modules together, multiple modules can track their output voltages to the voltage applied on the SEQ pin.

The module's output can track the SEQ pin signal with slopes of up to 0.5V/msec during power-up or power-down.

To initiate simultaneous shutdown of the modules, the SEQ pin voltage is lowered in a controlled manner. The output voltage of the modules tracks the voltages below their set-point voltages on a one-to-one basis. A valid input voltage must be maintained until the tracking and output voltages reach ground potential.

Note that in all digital DLynx series of modules, the PMBus Output Undervoltage Fault will be tripped when sequencing is employed. This will be detected using the STATUS_WORD and STATUS_VOUT PMBus commands. In addition, the SMBALERT# signal will be asserted low as occurs for all faults and warnings. To avoid the module shutting down due to the Output Undervoltage Fault, the module must be set to continue operation without interruption as the response to this fault (see the description of the PMBus command VOUT_UV_FAULT_RESPONSE for additional information).

Digital Compensator

The GDT080 module uses digital control to regulate the output voltage. As with all POL modules, external capacitors are usually added to the output of the module for two reasons: to reduce output ripple and noise (see Figure 32) and to reduce output voltage deviations from the steady-state value in the presence of dynamic load current changes. Adding external capacitance however affects the voltage control loop of the module, typically causing the loop to slow down with sluggish response. Larger values of external capacitance could also cause the module to become unstable. In the GDT080, the digital compensation can be adjusted externally to optimize transient response and also ensure stability for a wide range of external capacitance, as well as with different types of output capacitance. This is done by allowing the user to select among several pre-tuned compensation choices to select the one most suited to the transient response needs of the load. Figure 35 shows how the resistor RTune is connected between the RTUNE and GND pins to select the appropriate pre-tuned compensation.

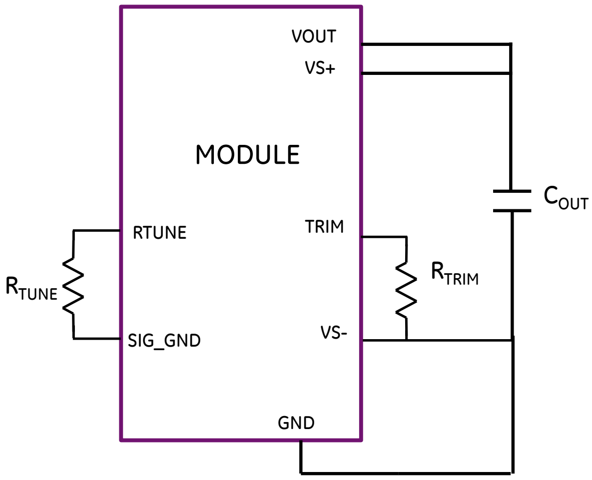


Figure 35. Circuit diagram showing connection of R_{TUNE} to tune the control loop of the module.

Recommended values of R_{TUNE} for different output capacitor combinations are given in Table 1. The GDT080 pre-tuned compensation can be divided into four different banks (COMP0, COMP1, COMP2 and COMP3) that are available to the user to compensate the control loop for various values and combinations of output capacitance and to obtain reliable and stable performance under different conditions. Each bank consists of seven different sets of compensation coefficients pre-calculated for different values of output capacitance. The four banks are set up as follows:

- COMP0: Recommended for the case where all of the output capacitance is composed of only ceramic capacitors. The range of output capacitance is from the required minimum value of 470μF to a maximum of 7500μF.
- COMP1: For the most commonly used mix of ceramic and polymer type capacitors that have higher output capacitance in a smaller size and for output voltages between 0.6V to 1.2V. The range of output capacitance is from 470μF to a maximum of 15,692uF. This is the combination of output capacitance and compensation that can achieve the best transient response at lowest cost and smallest size. For example, with the maximum output capacitance of 15,692uF, and selecting RTUNE = 4.22kΩ, transient deviation can be as low as 15mV, for a 50% load step (0 to 40A).
- COMP2: Same range and types of capacitance as COMP1, but for an output voltage range from 1.2V to 2V.

- COMP3: Suitable also for a mix of ceramic and higher ESR polymers or electrolytic capacitors such as OSCON.

Selecting R_{TUNE} according to Table 2 will ensure stable operation of the module with sufficient stability margin as well as yield optimal transient response.

In applications with tight output voltage limits in the presence of dynamic current loading, additional output capacitance will be required. Table 4 lists recommended values of R_{TUNE} in order to meet 2% output voltage deviation limits for some common output voltages in the presence of a 40A to 80A step change (50% of full load), with an input voltage of 12V.

Please contact your GE technical representative to obtain more details of this feature as well as for guidelines on how to select the right value of external RTUNE to tune the module for best transient performance and stable operation for other output capacitance values. Simulation models are also available via the GE Power Module Wizard to predict stability characteristics and transient response.

Table 2. R_{TUNE} compensation table

Output Capacitance Type	Number of Output Capacitors**	Total Output Capacitance (μF)**	Compensation Bank	R_{TUNE} resistor (kΩ)
Ceramic	18 x 47μ	756	Comp 0	0
Ceramic	18 x 47μ + 14 x 100μ	1485	Comp 0	0.392
Ceramic	18 x 47μ + 16 x 100μ	2052	Comp 0	0.576
Ceramic	18 x 47μ + 22 x 100μ	2538	Comp 0	0.787
Ceramic	18 x 47μ + 40 x 100μ	3996	Comp 0	1
Ceramic	18 x 47μ + 52 x 100μ	4968	Comp 0	1.24
Ceramic	18 x 47μ + 83 x 100μ	7479	Comp 0	1.5
Ceramic + Polymer	16 x 47μ + 2 x 1000μ	2672	Comp 1	1.78
Ceramic + Polymer	16 x 47μ + 3 x 1000μ	3672	Comp 1	2.1
Ceramic + Polymer	16 x 47μ + 5 x 1000μ	5672	Comp 1	2.43
Ceramic + Polymer	16 x 47μ + 7 x 1000μ	7672	Comp 1	2.8
Ceramic + Polymer	16 x 47μ + 9 x 1000μ	9672	Comp 1	3.24
Ceramic + Polymer	18 x 47μ + 12 x 1000μ	12,756	Comp 1	3.74
Ceramic + Polymer	18 x 47μ + 14 x 1000μ	14,756	Comp 1	4.22
Ceramic + Polymer	16 x 47μ + 2 x 1000μ	2672	Comp 2	4.75
Ceramic + Polymer	16 x 47μ + 3 x 1000μ	3672	Comp 2	5.36
Ceramic + Polymer	16 x 47μ + 5 x 1000μ	5672	Comp 2	6.04
Ceramic + Polymer	16 x 47μ + 7 x 1000μ	7672	Comp 2	6.81
Ceramic + Polymer	16 x 47μ + 9 x 1000μ	9672	Comp 2	7.68
Ceramic + Polymer	18 x 47μ + 12 x 1000μ	12,756	Comp 2	8.66
Ceramic + Polymer	18 x 47μ + 14 x 1000μ	14,756	Comp 2	9.53
Ceramic + Electrolytic	16 x 47μ + 4 x 470μ	2552	Comp 3	10.5
Ceramic + Electrolytic	16 x 47μ + 7 x 470μ	3962	Comp 3	11.8
Ceramic + Electrolytic	16 x 47μ + 9 x 470μ	4902	Comp 3	13
Ceramic + Electrolytic	18 x 47μ + 14 x 470μ	7336	Comp 3	14.3
Ceramic + Electrolytic	18 x 47μ + 20 x 470μ	10,156	Comp 3	15.8
Ceramic + Electrolytic	18 x 47μ + 24 x 470μ	12,036	Comp 3	17.4
Ceramic + Electrolytic	18 x 47μ + 30 x 470μ	14,856	Comp 3	19.1

** Total output capacitance includes the capacitance inside the module of value 8 x 47μF (3mΩ ESR).

Note: The capacitors used in the digital compensation Loop tables are 47μF/3 mΩ ESR ceramic, 100uF/3.2mΩ ceramic, 1000 μF/6mΩ ESR polymer capacitor and 470uF/9mΩ ESR Polymer capacitor.

Table 3. General recommended values of R_{TUNE} for Vin=12V and various external ceramic capacitor combination

C_O	14x 47μF	19x 100μF	25x 100μF	35x 100μF	50x 100μF
R_{TUNE} (kΩ)	0.0	0.576	0.787	1.0	1.24

Table 4. Recommended values of R_{TUNE} to obtain transient deviation of 2% of Vout for a 40A step load with Vin=12V.

V_O	2V	1.2V	0.6V
C_O	14x47uF + 5x1000μF polymer	28x47uF + 9x1000μF polymer	36x47uF + 14x1000μF polymer
R_{TUNE} (kΩ)	9.53	3.24	4.22
ΔV (mV)	32	19.8	12

Digital Output Voltage Margining

Please see the Digital Feature Descriptions section.

Overcurrent Protection (OCP)

To provide protection in a fault (output overload) condition, the unit has internal current-limiting circuitry on the output and can endure current limiting continuously. The module overcurrent response is non-latching shutdown with automatic recovery. The Overcurrent Protection response time is programmable via the PMBus through manufacturer-specific commands. The unit operates normally once the output current is brought back into its specified range.

Load Transient Considerations

The GDT080 module can achieve 100% load transient above -20°C ambient temperature. Below -20°C ambient temperature, the load transient is limited to a maximum of 75% of specified full load current.

Digital Sequencing

The module supports digital sequencing operation. Both ratiometric and simultaneous sequencing are supported.

Overtemperature Protection

To provide protection in a fault condition, the unit has a thermal shutdown circuit. The unit will shut down if the overtemperature threshold of TBD (typ) is exceeded at the thermal reference point T_{ref} . Once the unit goes into thermal shutdown it will wait to cool before attempting to restart.

Digital Temperature Status via PMBus

Please see the Digital Feature Descriptions section.

Digitally Adjustable Output Over and Under Voltage Protection

Please see the Digital Feature Descriptions section.

Input Undervoltage Lockout

At input voltages below the input undervoltage lockout limit, module operation is disabled. The module will begin to operate at an input voltage above the undervoltage lockout turn-on threshold.

Digitally Adjustable Input Undervoltage Lockout

Please see the Digital Feature Descriptions section.

Digitally Adjustable Power Good Thresholds

Please see the Digital Feature Descriptions section.

Power Good

The module provides a Power Good (PGOOD) signal that goes high to indicate output voltage being within a specified range. The signal is implemented as an open-drain output.. The PGOOD signal is de-asserted to a low state if any condition such as overtemperature, overcurrent or loss of regulation occurs that would result in the output voltage going outside the specified thresholds. The default PGOOD thresholds are ± 12.5%.

The PGOOD terminal should be connected through a pullup resistor (suggested value 100KΩ) to a source of 5VDC or lower.

Synchronization

The module switching frequency can be synchronized to an external signal within the specified range. Synchronization is done by applying the external signal to the SYNC pin of the module as shown in Fig. 36, with the converter being synchronized by the rising edge of the external signal. The Electrical Specifications table specifies the requirements of the external SYNC signal. If the SYNC pin is not used, the module runs at the default switching frequency.

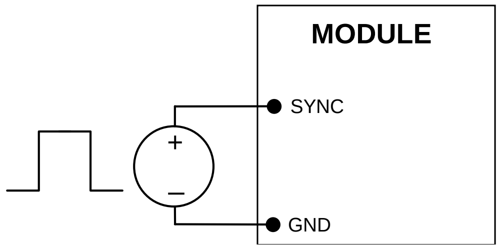


Figure 36. External source connections to synchronize switching frequency of the module.

Measuring Output Current, Output Voltage and Input Voltage

Please see the Digital Feature Descriptions section.

Digital Feature Descriptions

PMBus Interface Capability

The 80A Digital GigaDLynx™ power modules have a PMBus interface that supports both communication and control. The modules supports a subset of version 1.1 of the PMBus specification (see Table 6 for a list of the specific commands supported). Most module parameters can be programmed using PMBus and stored as defaults for later use.

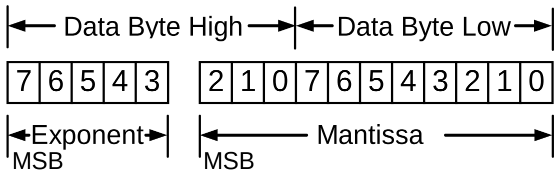
All communication over the module PMBus interface will work with or without Packet Error Checking (PEC) . The module generates the correct PEC byte for all transactions, and checks the PEC byte if sent by the master.

The module also supports the SMBALERT# response protocol whereby the module alerts the bus master if it wants to talk. For more information on the SMBus alert response protocol, see the System Management Bus (SMBus) specification.

The module has non-volatile memory that is used to store configuration settings. Not all settings programmed into the device are automatically saved into this non-volatile memory, only those specifically identified as capable of being stored are saved (see Table 6 for which command parameters can be saved in non-volatile storage).

PMBus Data Format

For commands that set thresholds, voltages or report such quantities, the module supports the “Linear” data format among the three data formats supported by PMBus. The Linear Data Format is a two byte value with an 11-bit, two’s complement mantissa and a 5-bit, two’s complement exponent. The format of the two data bytes is shown below:



The value is of the number is then given by

$$\text{Value} = \text{Mantissa} \times 2^{\text{Exponent}}$$

PMBus Addressing

The power module is addressed through the PMBus using a device address. The module supports 128 possible addresses (0 to 127 in decimal) which can be set using resistors connected from the ADDR0 and ADDR1 pins to SIG_GND. Note that some of these addresses (0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 12, 40, 44, 45, 55 in decimal) are reserved according to the SMBus specification and may not be useable. The address is set in the form of two octal (0 to 7) digits, with each pin setting one digit. The ADDR1 pin sets the high order digit and ADDR0 sets the low order digit. The resistor values suggested for each digit are shown in Table 5 (E96 series resistors are recommended). Note that if either address resistor value is outside the range specified in Table 5, the module will respond to address 127.

Table 5

PMBus Address Table								
	ADDR1 Resistor Values							
ADDR0 Resistor Values	0	680	1.2K	1.8K	2.7K	3.9K	4.7K	5.6K
0	64	16	32	48	64	80	96	112
680	1	17	33	49	65	81	97	113
1.2K	2	18	34	50	66	82	98	114
1.8K	3	19	35	51	67	83	99	115
2.7K	4	20	36	52	68	84	100	116
3.9K	5	21	37	53	69	85	101	117
4.7K	6	22	38	54	70	86	102	118
5.6K	7	23	39	55	71	87	103	119
6.8K	8	24	40	56	72	88	104	120
8.2K	9	25	41	57	73	89	105	121
10K	10	26	42	58	74	90	106	122
12K	11	27	43	59	75	91	107	123
15K	12	28	44	60	76	92	108	124
18K	13	29	45	61	77	93	109	125
22K	14	30	46	62	78	94	110	126
27K	15	31	47	63	79	95	111	127

Both 100kHz and 400kHz bus speeds are supported by the module. Connection for the PMBus interface should follow the High Power DC specifications given in section 3.1.3 in the SMBus specification V2.0 for the 400kHz bus speed or the Low Power DC specifications in section 3.1.2.

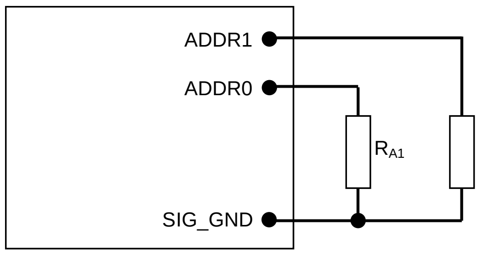


Figure 37. Circuit showing connection of resistors used to set the PMBus address of the module.

PMBus Enabled On/Off

The output of the module can be turned on and off via the PMBus interface. The OPERATION command is used to actually turn the module on and off via the PMBus, while the ON_OFF_CONFIG command configures the combination of analog ON/OFF pin input and PMBus commands needed to turn the module on and off. Bit [7] in the OPERATION command data byte enables the module, with the following functions:

0 : Output is disabled
1 : Output is enabled

This module shall use the lower five bits of the ON_OFF_CONFIG data byte to set various ON/OFF options as follows:

Bit Position	4	3	2	1	0
Access	r/w	r/w	r/w	r/w	r
Function	PU	CMD	CPR	POL	CPA
Default Value	***1***	***0***	***1***	***1***	***0***

PU: Sets the default to either operate any time input power is present or for the ON/OFF to be controlled by the analog ON/OFF input and the PMBus OPERATION command. This bit shall be used together with the CP, CMD and ON bits to determine startup.

Bit Value	Action
0	Module powers up any time power is present regardless of state of the analog ON/OFF pin
1	Module does not power up until commanded by the analog ON/OFF pin and the OPERATION command as programmed in bits [2:0] of the ON_OFF_CONFIG register.

CMD: The CMD bit controls how the device responds to the OPERATION command.

Bit Value	Action
0	Module ignores the ON bit in the OPERATION command
1	Module responds to the ON bit in the OPERATION command

CPR: Sets the response of the analog ON/OFF pin. This bit isused together with the CMD, PU and ON bits to determine startup.

Bit Value	Action
0	Module ignores the analog ON/OFF pin, i.e. ON/OFF is only controlled through the PMBUS via the OPERATION command
1	Module requires the analog ON/OFF pin to be asserted to start the unit

PMBus Adjustable Soft Start Rise Time

The soft start rise time of module output is adjustable in the module via PMBus. The TON_RISE command can set the rise time in ms, and allows choosing soft start times between 200μs and 14ms.

Output Voltage Adjustment Using the PMBus

The VOUT_SCALE_MONITOR parameter is important for a number of PMBus commands related to output voltage trimming, margining, over/under voltage protection and the PGOOD thresholds. The output voltage of the module is determined by the value of the R_{Trim} resistor connected between TRIM pin and analog ground VS-, as specified earlier in the data sheet. The information on the output voltage divider ratio is conveyed to the module through the VOUT_SCALE_MONITOR parameter. The read-out of output voltage also depends on VOUT_SCALE_PARAMETER. If correct VOUT_SCALE_PARAMETER is not used, the output voltage read-out will be wrong. The VOUT_SCALE_MONITOR parameter is defined by the ratio of internal reference of the controller to the nominal output voltage selected by R_{Trim} resistor.

$$\text{VOUT\_SCALE\_MONITOR} = \frac{0.6\text{V}}{\text{Nominal Output Voltage}}$$

For example, for a nominal output voltage of 1.2V, the VOUT_SCALE_PARAMETER is equal to 0.5. Table 6 below defines values of VOUT_SCALE_MONITOR to the various nominal output voltages.

Table 6

$V_{O,\,set}$ (V)	VOUT_SCALE_MONITOR
0.6	1
0.8	0.75
1.0	0.6
1.2	0.5
1.5	0.4
2.0	0.3

When PMBus commands are used to trim or margin the output voltage, the value of V_{REF} is what is changed inside the module, which in turn changes the regulated output voltage of the module.

The nominal output voltage of the module is adjustable with a minimum step size of TBD over a ± 25% range from nominal using the VOUT_TRIM command over the PMBus.

Output Voltage Margining Using the PMBus

Output voltage of the module can also be margined via PMBus commands. The command MFR_VOUT_MARGIN_HIGH sets the margin high voltage, while the command MFR_VOUT_MARGIN_LOW sets the margin low voltage. Both the MFR_VOUT_MARGIN_HIGH and MFR_VOUT_MARGIN_LOW commands use the "Linear" mode. Two bytes are used for data. The actual margined output voltage is determined by the resistor on the TRIM, which as explained earlier is taken into consideration by VOUT_SCALE_MONITOR command. The module then sets the output voltage to the margined high or low voltage levels using the OPERATION command. Bits [7:4] shall be used to enable margining as follows:

1001: Vout set to MFR_VOUT_MARGIN_LOW (Ignore Fault)
1010: Vout set to MFR_VOUT_MARGIN_HIGH (Ignore Fault)

Temperature Status via PMBus

The module provides information related to temperature of the module through standardized PMBus commands. Commands READ_TEMPERATURE1, READ_TEMPERATURE_2 are mapped to module temperature (at TBD location) and internal temperature of the PWM controller, respectively.

The temperature readings are returned in °C and is two bytes.

PMBus Adjustable Output Over and Under Voltage Protection

The module has output over and under voltage protection capability. The PMBus command VOUT_OV_FAULT_LIMIT is used to set the output over voltage threshold. The default value is configured to be 115% of the commanded output. The command VOUT_UV_FAULT_LIMIT sets the threshold that detects an output under voltage fault. The default values are 85% of the commanded output voltage. Both commands use two data bytes formatted in the Linear format.

PMBus Adjustable Input Undervoltage Lockout

The module allows adjustment of the input under voltage lockout and hysteresis. The command VIN_ON allows setting the input voltage turn on threshold, while the VIN_OFF command sets the input voltage turn off threshold. For both the VIN_ON and VIN_OFF commands, possible values are 4.5V to 14V . Both VIN_ON and VIN_OFF commands use the "Linear" format with two data bytes.

Measurement of Output Current, Output Voltage, Input Voltage and output power

The module can measure key module parameters such as output current, output voltage and input voltage and provide this information through the PMBus interface.

Measuring Output Current Using the PMBus

The module measures output current by using the inductor winding resistance as a current sense element. The inductor winding resistance is then multiplied by the current gain factor that will be used to scale the measured voltage into a current reading. This gain factor shall be the argument of the IOUT_CAL_GAIN command, and consists of two bytes in the Linear data format. During manufacture, each module is calibrated by measuring and storing the current gain factor into non-volatile storage.

The current measurement accuracy is also improved by each module being calibrated during manufacture with the offset in the current reading. The IOUT_CAL_OFFSET command is used to store and read the current offset. The READ_IOUT command provides tmodule average output current information. This command only supports positive output current, i.e. current sourced from the module. If the converter is sinking current a reading of 0 is provided. The READ_IOUT command returns two bytes of data in the Linear data format.

Measuring Output Voltage Using the PMBus

The module provides output voltage information using the READ_VOUT command. The command returns two bytes of data in Linear format

Measuring Input Voltage Using the PMBus

The module provides input voltage information using the READ_VIN command. The command returns two bytes of data in the Linear format.

Reading the Status of the Module using the PMBus

The module supports a number of status information commands implemented in PMBus. A 1 in the bit position shall indicate the fault that is flagged.

STATUS_BYTE : Returns one byte of information with a summary of the most critical device faults.

Bit Position	Flag	Default Value
7	X	0
6	OFF	0
5	VOUT Overvoltage	0
4	IOUT Overcurrent	0
3	VIN Undervoltage	0
2	Temperature	0
1	CML (Comm. Memory Fault)	0
0	None of the above	0

STATUS_WORD : Returns two bytes of information with a summary of the module's fault/warning conditions.

Low Byte

Bit Position	Flag	Default Value
7	X	0
6	OFF	0
5	VOUT Overvoltage	0
4	IOUT Overcurrent	0
3	VIN Undervoltage	0
2	Temperature	0
1	CML (Comm. Memory Fault)	0
0	None of the above	0

High Byte

Bit Position	Flag	Default Value
7	VOUT fault or warning	0
6	IOUT fault	0
5	VIN Fault	0
4	X	0
3	PowerGOOD	0
2	Fan Fault	0
1	Shortcircuit	0
0	X	0

STATUS_VOUT : Returns one byte of information relating to the status of the module's output voltage related faults.

Bit Position	Flag	Default Value
7	VOUT OV Fault	0
6	X	0
5	X	0
4	VOUT UV Fault	0
3	X	0
2	X	0
1	X	0
0	X	0

STATUS_IOUT : Returns one byte of information relating to the status of the module's output voltage related faults.

Bit Position	Flag	Default Value
7	IOUT OC Fault	0
6	X	0
5	X	0
4	X	0
3	X	0
2	X	0
1	X	0
0	X	0

STATUS_INPUT : Returns one byte of information relating to the status of the module's output voltage related faults.

Bit Position	Flag	Default Value
7	VIN_OV_FAULT	0
6	X	0
5	X	0
4	VIN_UV_FAULT	0
3	X	0
2	X	0
1	X	0
0	X	0

STATUS_TEMPERATURE : Returns one byte of information relating to the status of the module's temperature related faults.

Bit Position	Flag	Default Value
7	OT Fault	0
6	OT Warning	0
5	X	0
4	X	0
3	X	0
2	X	0
1	X	0
0	X	0

STATUS_CML : Returns one byte of information relating to the status of the module's communication related faults.

Bit Position	Flag	Default Value
7	Invalid/Unsupported Command	0
6	Invalid/Unsupported data	0
5	Packet Error Check Failed	0
4	Memory Fault	0
3	X	0
2	X	0
1	X	0
0	X	0

MFR_VIN_MIN : Returns minimum input voltage as two data bytes of information in Linear format (upper five bits are exponent – fixed at -2, and lower 11 bits are mantissa in two's complement format – fixed at 12)

MFR_VOUT_MIN : Returns minimum output voltage as two data bytes of information in Linear format (upper five bits are exponent – fixed at -10, and lower 11 bits are mantissa in two's complement format – fixed at 614)

MFR_SPECIFIC_00 : Returns information related to the type of module and revision number. Bits [7:2] in the Low Byte indicate the module type (xxxxxx corresponds to the PLX002 series of module), while bits [7:3] indicate the revision number of the module.

Low Byte

Bit Position	Flag	Default Value
7:2	Module Name	xxxxxx
1:0	Reserved	10

High Byte

Bit Position	Flag	Default Value
7:3	Module Revision Number	None
2:0	Reserved	000

Writing to OTP (One Time Programmable) Memory

The GDT080 EEPROM memory can be completely written in entirety, for example, using STORE_DEFAULT_ALL command, only four times. During the situation of partial rewrites, for example, when trying to store only four commands using STORE_DEFAULT_CODE command four times in succession, numerous writes are possible within the confines of available memory.

Summary of Supported PMBus Commands

Please refer to the PMBus 1.2 specification for more details of these commands.

Table 7

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>01</td><td>OPERATION</td><td>Turn Module on or off. Also used to margin the output voltage
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td>On</td><td>X</td><td colspan="4">Margin</td><td>X</td><td>X</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>X</td><td>X</td></tr>
</table></td><td></td></tr>
<tr><td>02</td><td>ON_OFF_CONFIG</td><td>Configures the ON/OFF functionality as a combination of analog ON/OFF pin and PMBus commands
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td></tr>
<tr><th>Function</th><td>X</td><td>X</td><td>X</td><td>pu</td><td>cmd</td><td>cpr</td><td>pol</td><td>cpa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>03</td><td>CLEAR_FAULTS</td><td>Clear any fault bits that may have been set, also releases the SMBALERT# signal if the device has been asserting it.</td><td></td></tr>
<tr><td>10</td><td>WRITE_PROTECT</td><td>Used to control writing to the module via PMBus. Copies the current register setting in the module whose command code matches the value in the data byte into non-volatile memory (EEPROM) on the module
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>x</td><td>x</td><td>x</td><td>x</td><td>x</td></tr>
<tr><th>Function</th><td>bit7</td><td>bit6</td><td>bit5</td><td>X</td><td>X</td><td>X</td><td>X</td><td>X</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>X</td><td>X</td><td>X</td><td>X</td><td>X</td></tr>
</table>
Bit5: 0 – Enables all writes as permitted in bit6 or bit7
1 – Disables all writes except the WRITE_PROTECT, OPERATION and ON_OFF_CONFIG (bit 6 and bit7 must be 0)
Bit 6: 0 – Enables all writes as permitted in bit5 or bit7
1 – Disables all writes except for the WRITE_PROTECT and OPERATION commands (bit5 and bit7 must be 0)
Bit7: 0 – Enables all writes as permitted in bit5 or bit6
1 – Disables all writes except for the WRITE_PROTECT command (bit5 and bit6 must be 0)</td><td>YES</td></tr>
<tr><td>11</td><td>STORE_DEFAULT_ALL</td><td>Copies all current register settings in the module into non-volatile memory (EEPROM) on the module. Takes about 50ms for the command to execute.*</td><td></td></tr>
<tr><td>12</td><td>RESTORE_DEFAULT_ALL</td><td>Restores all current register settings in the module from values in the module non-volatile memory (EEPROM)</td><td></td></tr>
<tr><td>13</td><td>STORE_DEFAULT_CODE</td><td>Copies the current register setting in the module whose command code matches the value in the data byte into non-volatile memory (EEPROM) on the module
<table>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td></tr>
<tr><th>Function</th><td colspan="8">Command code</td></tr>
</table></td><td></td></tr>
<tr><td>14</td><td>RESTORE_DEFAULT_CODE</td><td>Restores the current register setting in the module whose command code matches the value in the data byte from the value in the module non-volatile memory (EEPROM)
<table>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td><td>w</td></tr>
<tr><th>Function</th><td colspan="8">Command code</td></tr>
</table></td><td></td></tr>
<tr><td>20</td><td>VOUT_MODE</td><td>The module has MODE set to Linear and Exponent set to -13. These values cannot be changed
<table>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="3">Mode</td><td colspan="5">Exponent</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
</table></td><td></td></tr>
</table>

***NOTE: The EEPROM memory can be completely written in entirety (for example, using STORE_DEFAULT_ALL command) only four times. During the situation of partial rewrites, numerous writes are available within the confines of the available memory (for example, using STORE_DEFAULT_CODE command).**

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>22</td><td>VOUT_TRIM</td><td>Apply a fixed offset voltage to the output voltage command value. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">High Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Low Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>25</td><td>VOUT_MARGIN_HIGH</td><td>Sets the target voltage for margining the output high. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">High Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Low Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>26</td><td>VOUT_MARGIN_LOW</td><td>Sets the target voltage for margining the output low. Exponent is fixed at -13
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">High Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Low Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>2A</td><td>VOUT_SCALE_MONITOR</td><td>Sets the scaling of the output voltage – equal to the ratio of internal reference Vref to the Output voltage. The internal reference is fixed at 0.6V. The output voltage read back gets scaled by the value of this parameter.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>35</td><td>VIN_ON</td><td>Sets the value of input voltage at which the module turns on. Exponent is fixed at -6
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
</table>

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>36</td><td>VIN_OFF</td><td>Sets the value of input voltage at which the module turns off. Exponent is fixed at -6
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>38</td><td>IOUT_CAL_GAIN</td><td>Returns the value of the gain correction term used to correct the measured output current
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td colspan="8">V: Variable based on factory calibration</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td colspan="8">V: Variable based on factory calibration</td></tr>
</table></td><td>YES</td></tr>
<tr><td>39</td><td>IOUT_CAL_OFFSET</td><td>Returns the value of the offset correction term used to correct the measured output current
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td colspan="8">V: Variable based on factory calibration</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td colspan="8">V: Variable based on factory calibration</td></tr>
</table></td><td>YES</td></tr>
<tr><td>40</td><td>VOUT_OV_FAULT_LIMIT</td><td>Sets the voltage level for an output overvoltage fault. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's compliment binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">High Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Low Byte</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>41</td><td>VOUT_OV_FAULT_RESPONSE</td><td>Instructs the module on what action to take in response to a output overvoltage fault
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td>RSP [1]</td><td>RSP [0]</td><td>RS[2]</td><td>RS[1]</td><td>RS[0]</td><td>X</td><td>X</td><td>X</td></tr>
<tr><th>Default Value</th><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

80A GigaDLynx^{TM}: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>42</td><td>VOUT_OV_WARN_LIMIT</td><td>Sets the value of output voltage at which the module generates warning for over-voltage. Exponent is fixed at -13
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>43</td><td>VOUT_UV_WARN_LIMIT</td><td>Sets the value of output voltage at which the module generates warning for under-voltage. Exponent is fixed at -13
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>44</td><td>VOUT_UV_FAULT_LIMIT</td><td>Sets the voltage level for an output undervoltage fault. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>45</td><td>VOUT_UV_FAULT_RESPONSE</td><td>Sets the voltage level for an output overvoltage fault. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td>RSP [1]</td><td>RSP [0]</td><td>RS[2]</td><td>RS[1]</td><td>RS[0]</td><td>X</td><td>X</td><td>X</td></tr>
<tr><th>Default Value</th><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>46</td><td>IOUT_OC_FAULT_LIMIT</td><td>Sets the current level for an output overcurrent fault (cannot be changed)
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
</table>

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>4A</td><td>IOUT_OC_WARN_LIMIT</td><td>Sets the value of current level at which the module generates warning for overcurrent.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>4F</td><td>OT_FAULT_LIMIT</td><td>Sets the temperature level above which over-temperature fault occurs.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>50</td><td>OT_FAULT_RESPONSE</td><td>Configures the over temperature fault response
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td>RSP [1]</td><td>RSP [0]</td><td>RS[2]</td><td>RS[1]</td><td>RS[0]</td><td>X</td><td>X</td><td>X</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>51</td><td>OT_WARN_LIMIT</td><td>Sets the over temperature warning level in °C
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>55</td><td>VIN_OV_FAULT_LIMIT</td><td>Sets the input overvoltage fault limit. Exponent is fixed at -6.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary. Exponent is fixed at -13.</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>56</td><td>VIN_OV_FAULT_RESPONSE</td><td>Configures the VIN overvoltage fault response.
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td>RSP [1]</td><td>RSP [0]</td><td>RS[2]</td><td>RS[1]</td><td>RS[0]</td><td>X</td><td>X</td><td>X</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>57</td><td>VIN_OV_WARN_LIMIT</td><td>Sets the value of the input voltage that causes input voltage low warning. Exponent fixed at -6
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td></tr>
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<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>58</td><td>VIN_UV_WARN_LIMIT</td><td>Sets the value of the input voltage that causes input voltage low warning. Exponent fixed at -6
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
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<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>59</td><td>VIN_UV_FAULT_LIMIT</td><td>Sets the value of the input voltage that causes an input undervoltage fault. Exponent fixed at -6.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>5A</td><td>VIN_UV_FAULT_RESPONSE</td><td>Instructs the module on what action to take In response to an input undervoltage fault.
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td>RSP [1]</td><td>RSP [0]</td><td>RS[2]</td><td>RS[1]</td><td>RS[0]</td><td>X</td><td>X</td><td>X</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>5E</td><td>POWER_GOOD_ON</td><td>Sets the output voltage level at which the PGOOD pin is asserted high. Exponent is fixed at -13.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">High Byte</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Low Byte</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr>
</table></td><td>YES</td></tr>
<tr><td>5F</td><td>POWER_GOOD_OFF</td><td>Sets the output voltage level at which the PGOOD pin is de-asserted low. Exponent is fixed at -13.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">High Byte</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Low Byte</td></tr>
<tr><td>Default Value</td><td>0</td><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>60</td><td>TON_DELAY</td><td>Sets the delay time in ms of the output voltage during startup.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>61</td><td>TON_RISE</td><td>Sets the rise time in ms of the output voltage during startup
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>64</td><td>TOFF_DELAY</td><td>Sets the delay time in ms of the output voltage during turn-off
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>65</td><td>TOFF_FALL</td><td>Sets the fall time in ms of the output voltage during turn-off
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>78</td><td>STATUS_BYTE</td><td>Returns one byte of information with a summary of the most critical module faults
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Flag</td><td>X</td><td>OFF</td><td>VOUT_OV</td><td>IOUT_OC</td><td>VIN_UV</td><td>TEMP</td><td>CML</td><td>OTHER</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>79</td><td>STATUS_WORD</td><td>Returns two bytes of information with a summary of the module's fault/warning conditions
<table>
<tr><td>Format</td><td colspan="8">Unsigned binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Flag</td><td>VOUT</td><td>IOUT_OC</td><td>X</td><td>X</td><td>PGOOD</td><td>X</td><td>X</td><td>X</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>R</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Flag</td><td>X</td><td>OFF</td><td>VOUT_OV</td><td>IOUT_OC</td><td>VIN_UV</td><td>TEMP</td><td>CML</td><td>OTHER</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>7A</td><td>STATUS_VOUT</td><td>Returns one byte of information with the status of the module's output voltage related faults
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Flag</td><td>VOUT_OV</td><td>X</td><td>X</td><td>VOUT_UV</td><td>X</td><td>X</td><td>X</td><td>X</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

Table 7 (continued)

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
7B	STATUS_IOUT	Returns one byte of information with the status of the module's output current related faults	YES

Format	Unsigned Binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Flag	IOUT_OC	X	X	X	IOUT_OC_WARN	X	X	X
Default Value	0	0	0	0	0	0	0	0

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
7C	STATUS_INPUT	Returns one byte of information with the status of the module's input related faults	YES

Format	Unsigned Binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Flag	VIN_OV_FAULT	VIN_OV_WARNING	VIN_UV_WARNING	VIN_UV_FAULT	X	X	X	X
Default Value	0	0	0	0	0	0	0	0

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
7D	STATUS_TEMPERATURE	Returns one byte of information with the status of the module's temperature related faults	

Format	Unsigned Binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Flag	OT_FAULT	OT_WARN	X	X	X	X	X	X
Default Value	0	0	0	0	0	0	0	0

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
7E	STATUS_CML	Returns one byte of information with the status of the module's communication related faults	

Format	Unsigned Binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Flag	Invalid Command	Invalid Data	PEC Fail	X	X	X	Other Comm Fault	X
Default Value	0	0	0	0	0	0	0	0

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
88	READ_VIN	Returns the value of the input voltage applied to the module. Exponent is fixed at -6	

Format	Linear, two's complement binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Function	Exponent					Mantissa		
Default Value	1	1	0	1	1	0	0	0
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Function	Mantissa							
Default Value	0	0	0	0	0	0	0	0

Hex Code	Command	Brief Description	Non-Volatile Memory Storage
8B	READ_VOUT	Returns the value of the output voltage of the module. Exponent is fixed at -13	

Format	Linear, two's complement binary							
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Function	Mantissa							
Default Value	0	0	0	0	0	0	0	0
Bit Position	7	6	5	4	3	2	1	0
Access	r	r	r	r	r	r	r	r
Function	Mantissa							
Default Value	0	0	0	0	0	0	0	0

Table 7 (continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>8C</td><td>READ_IOUT</td><td>Returns the value of the output current of the module
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td></td></tr>
<tr><td>8D</td><td>READ_TEMPERATURE_1</td><td>Returns the module inductor temperature in °C
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr>
</table></td><td></td></tr>
<tr><td>8E</td><td>READ_TEMPERATURE_2</td><td>Returns the module PWM controller temperature in °C
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr>
</table></td><td></td></tr>
<tr><td>95</td><td>READ_FREQUENCY</td><td>Returns the switching Frequency of the converter. The Frequency is in Kilohertz and is read only, consisting of two bytes.
<table>
<tr><td>Format</td><td colspan="8">Linear, two's complement binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><td>Default Value</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Function</td><td colspan="8">Mantissa</td></tr>
<tr><td>Default Value</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td></td></tr>
<tr><td>98</td><td>PMBUS_REVISION</td><td>Returns one byte indicating the module is compliant to PMBus Spec. 1.1 (read only)
<table>
<tr><td>Format</td><td colspan="8">Unsigned Binary</td></tr>
<tr><td>Bit Position</td><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><td>Access</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><td>Default Value</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr>
</table></td><td>YES</td></tr>
</table>

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Manufacturer Specific PMBus commands:

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>A0</td><td>MFR_VIN_MIN</td><td>Returns the minimum input voltage the module is specified to operate at (read only)
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>A4</td><td>MFR_VOUT_MIN</td><td>Returns the minimum output voltage possible from the module (read only)
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>D0</td><td>MFR_SPECIFIC_00</td><td>Returns module name information (read only)
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Reserved</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="6">Module Name</td><td colspan="2">Reserved</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>D1</td><td>MFR_SPECIFIC_01</td><td>Returns module information (read only)
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Module Name</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
</table>

Manufacturer specific PMBus commands (Continued)

<table>
<tr><th>Hex Code</th><th>Command</th><th>Brief Description</th><th>Non-Volatile Memory Storage</th></tr>
<tr><td>DB</td><td>MFR_VOUT_MARGIN_HIGH</td><td>Sets the target voltage for margining the output high. Exponent is fixed at -13.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>DC</td><td>MFR_VOUT_MARGIN_LOW</td><td>Sets the target voltage for margining the output low. Exponent is fixed at -13
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr>
</table></td><td>YES</td></tr>
<tr><td>DD</td><td>MFR_RTUNE_INDEX</td><td>Returns the index derived from the resistor strapped to the RTUNE pin of the module.
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Reserved</td></tr>
<tr><th>Default Value</th><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr>
</table></td><td>YES</td></tr>
<tr><td>D1</td><td>MFR_WRITE_PROTECT</td><td>Gets or sets the write protection status of various PMBus commands. When a bit is set, the corresponding PMBus command is write protected and can only be read.
<table>
<tr><th>Format</th><td colspan="8">Unsigned Binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="8">Reserved</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
<tr><th>Bit Position</th><td>15</td><td>14</td><td>13</td><td>12</td><td>11</td><td>10</td><td>9</td><td>8</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="6">Module Name</td><td colspan="2">Reserved</td></tr>
<tr><th>Default Value</th><td>0</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td></tr>
</table>
Bit 0: ON_OFF_CONFIG
Bit 1: IOUT_CAL command comprising both IOUT_CAL_GAIN and IOUT_CAL_OFFSET
Bit 2: IOUT_OC_FAULT_LIMIT
Bit 3: IOUT_OC_FAULT_RESPONSE
Bit 4: OT_FAULT_LIMIT
Bit 5: OT_FAULT_RESPONSE
Bit 6: TOFF_MAX_WARN_LIMIT
Bit 7: MFR_EXT_TEMP_CAL_OFFSET
Bit 8: MFR_PHASE_CONTROL
Bit 9: MFR_SPECIFIC_01</td><td>YES</td></tr>
<tr><td>DB</td><td>MFR_VOUT_OFF</td><td>Gets or sets the target output voltage when switching off the device, in volts. Setting a non-zero value here will enable shut-down into pre-bias.
<table>
<tr><th>Format</th><td colspan="8">Linear, two's complement binary</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r</td><td>r</td></tr>
<tr><th>Function</th><td colspan="5">Exponent</td><td colspan="3">Mantissa</td></tr>
<tr><th>Default Value</th><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>V</td></tr>
<tr><th>Bit Position</th><td>7</td><td>6</td><td>5</td><td>4</td><td>3</td><td>2</td><td>1</td><td>0</td></tr>
<tr><th>Access</th><td>r</td><td>r</td><td>r</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td><td>r/w</td></tr>
<tr><th>Function</th><td colspan="8">Mantissa</td></tr>
<tr><th>Default Value</th><td>V</td><td>V</td><td>V</td><td>V</td><td>V</td><td>V</td><td>V</td><td>V</td></tr>
</table></td><td>YES</td></tr>
</table>

Thermal Considerations

Power modules operate in a variety of thermal environments; however, sufficient cooling should always be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel. The test set-up is shown in Figure 38. The preferred airflow direction for the module is shown in Figure 39.

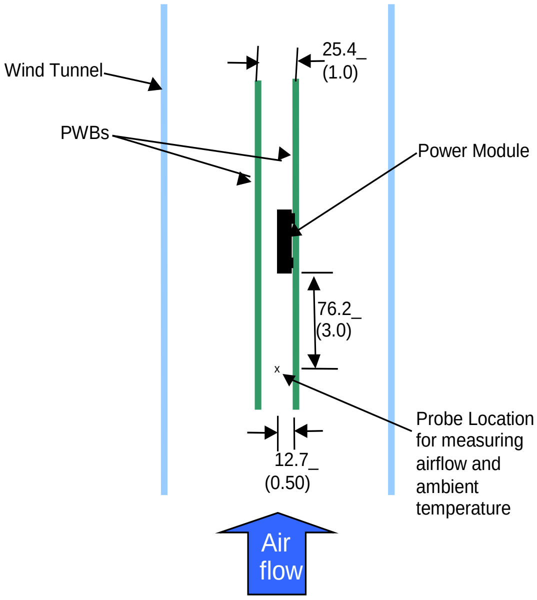


Figure 38. Thermal Test Setup.

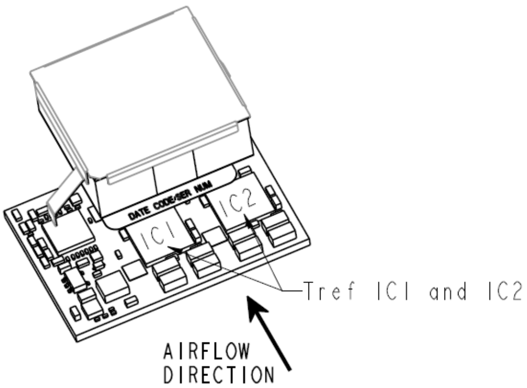


Figure 39. Preferred airflow direction and location of hot-spot of the module (Tref).

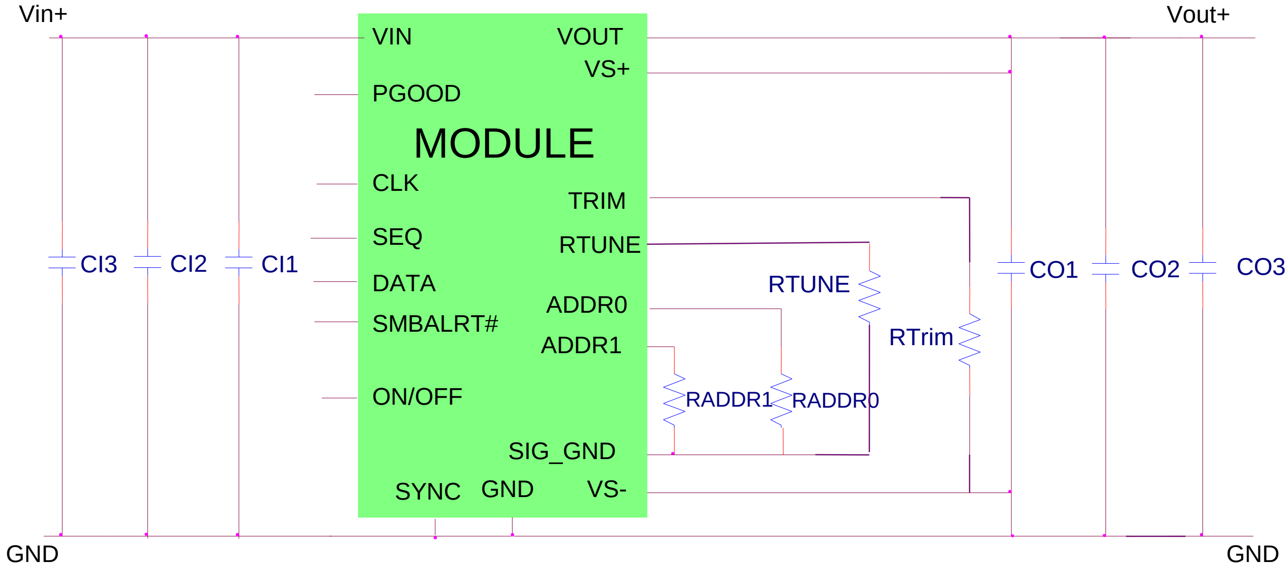
The thermal reference points, T_{ref} used in the specifications are also shown in Figure 39. For reliable operation the temperatures at these points should not exceed 120°C. The output power of the module should not exceed the rated power of the module (Vo,set x Io,max).

Please refer to the Application Note "Thermal Characterization Process For Open-Frame Board-Mounted Power Modules" for a detailed discussion of thermal aspects including maximum device temperatures.

Example Application Circuit

Requirements:

Vin:	**12V**
Vout:	**1.2V**
Iout:	**80A max., worst case load transient is from 20A to 60A**
ΔVout:	**1.5% of Vout (18mV) for worst case load transient**
Vin, ripple	**2% of Vin (220mV p-p)**



CI1	Decoupling cap - 1x0.1µF/16V ceramic capacitor (e.g. Murata LLA215R71A224MA14)
CI2	6x22µF/16V ceramic capacitor (e.g. Murata GRM32ER61C226KE20)
CI3	470µF/16V bulk electrolytic
CO1	Decoupling cap - 1x0.047µF/16V ceramic capacitor (e.g. Murata LLA215R71A224MA14)
CO2	6 x 47µF/6.3V ceramic capacitor (e.g. Murata GRM31CR60J476ME19)
CO3	10 x 1000µF/2.5V Polymer (e.g. Sanyo Poscap)
RTune	4.22kohms SMT resistor (can be 1206, 0805 or 0603 size)
RTrim	2kΩ SMT resistor (can be 1206, 0805 or 0603 size, recommended tolerance of 0.1%)

Note: The DATA, CLK and SMBALRT pins do not have any pull-up resistors inside the module. Typically, the PMBus master controller will have pull-up resistors as well as provide the driving source for these signals.

80A GigaDLynx^{TM}: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

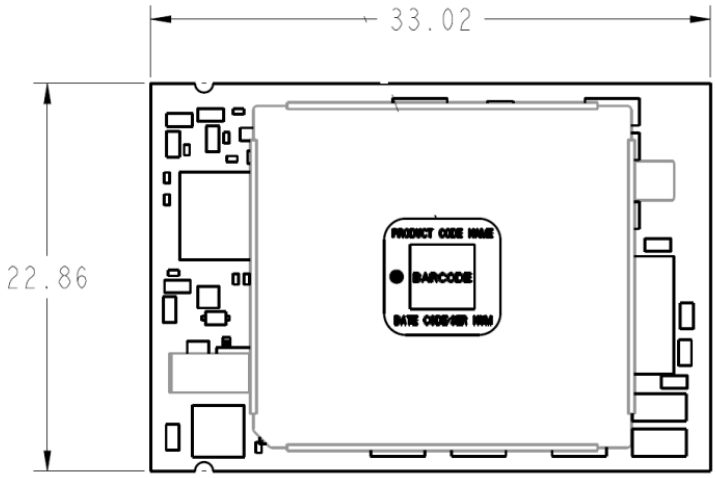
Mechanical Outline

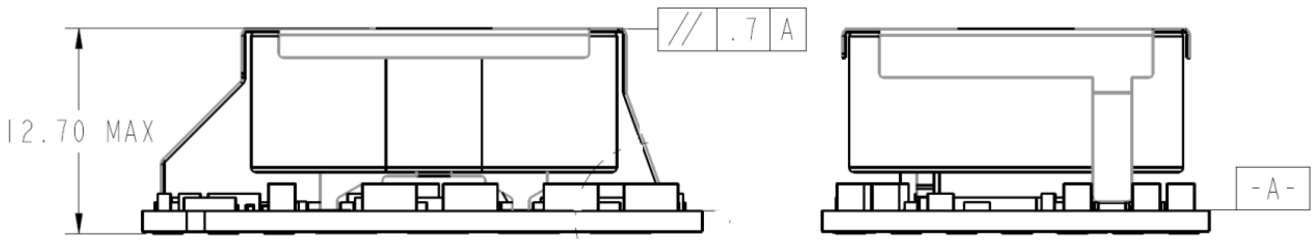
Dimensions are in millimeters and (inches).

Tolerances: x.x mm ± 0.5 mm (x.xx in. ± 0.02 in.) [unless otherwise indicated]

x.xx mm ± 0.25 mm (x.xxx in ± 0.010 in.)

TOP VIEW



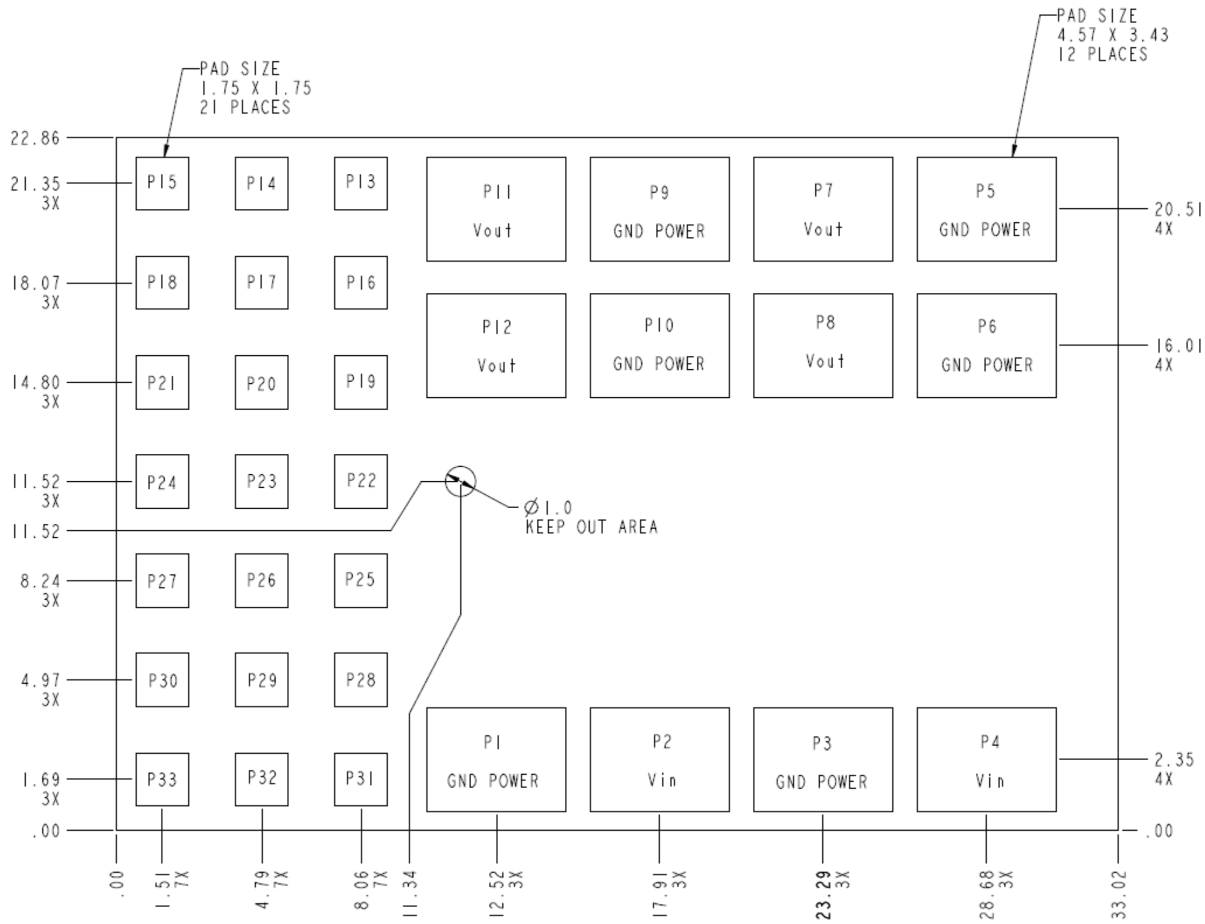


SIDE VIEWS

80A GigaDLynx™: Non-Isolated DC-DC Power Modules

4.5Vdc –14Vdc input; 0.6Vdc to 2.0Vdc output; 80A Output Current

Recommended Pad Layout



Pinout Details

PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	GND	15	SEQ	29	SMBALERT#
2	VIN	16	VS+	30	NC
3	GND	17	SIG_GND	31	NC
4	VIN	18	NC	32	NC
5	GND	19	PGOOD	33	NC
6	GND	20	ADDR0		
7	VOUT	21	NC		
8	VOUT	22	SYNC		
9	GND	23	ADDR1		
10	GND	24	RTUNE		
11	VOUT	25	ON/OFF		
12	VOUT	26	CLK		
13	VS-	27	NC		
14	TRIM	28	DATA		

Ordering Information

Please contact your GE Sales Representative for pricing, availability and optional features.

Table 3. Device Codes

Device Code	Input Voltage Range	Output Voltage	Output Current	On/Off Logic	Sequencing	Comcodes
GDT080A0X3-SRZ	4.5 – 14Vdc	0.6 – 2.0 Vdc	80A	Negative	Yes	150037110
GDT080A0X43-SRZ	4.5 – 14Vdc	0.6 – 2.0 Vdc	80A	Positive	Yes	150044134

-Z refers to RoHS compliant parts

Table 4. Coding Scheme

Package Identifier	Family	Sequencing Option	Output current	Output voltage	On/Off logic	Remote Sense	Options		ROHS Compliance
G	**D**	**T**	**080A0**	**X**		**3**	**-SR**	**-H**	**Z**
P=Pico U=Micro M=Mega G=Giga	D=Dlynx Digital V = DLynx Analog.	T=with EZ Sequence X=without sequencing	80A	X = programmable output	4 = positive No entry = negative	3 = Remote Sense	S = Surface Mount R = Tape & Reel	Extra Ground Pins	Z = ROHS6

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