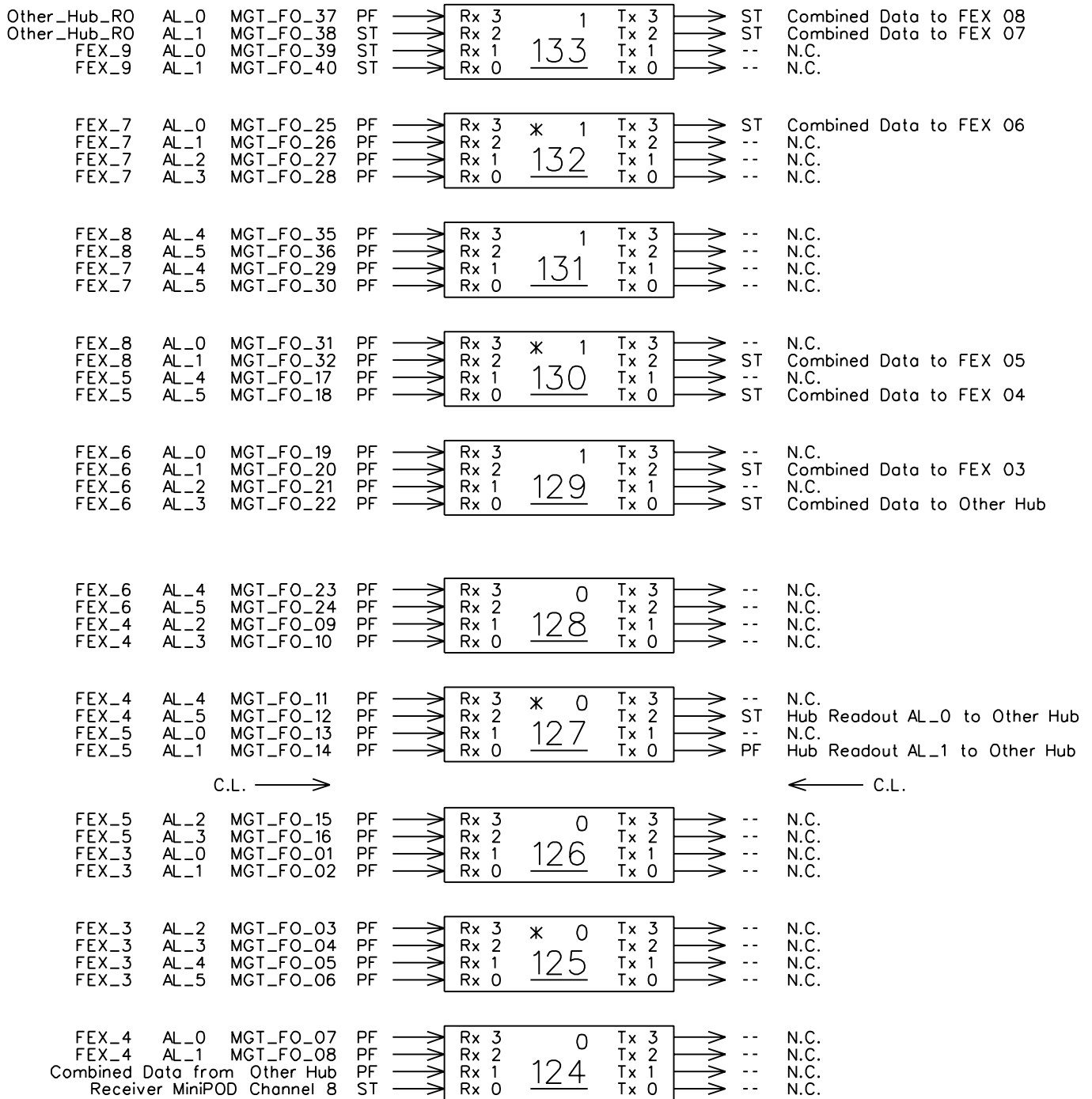


# Hub - GTY Transceivers - QUADs 124:133



\* → These Quads receive the LHC locked 320.6296 MHz reference clock.

MGT\_FO\_ → MGT Data Fanout Channel Number

ST → Straight Through

PF → Polarity Flip

AL → Aurora Lane Number

0,1 → UltraScale FPGA Super Logic Region

N.C. → No Connection

C.L. → Center Line of BGA