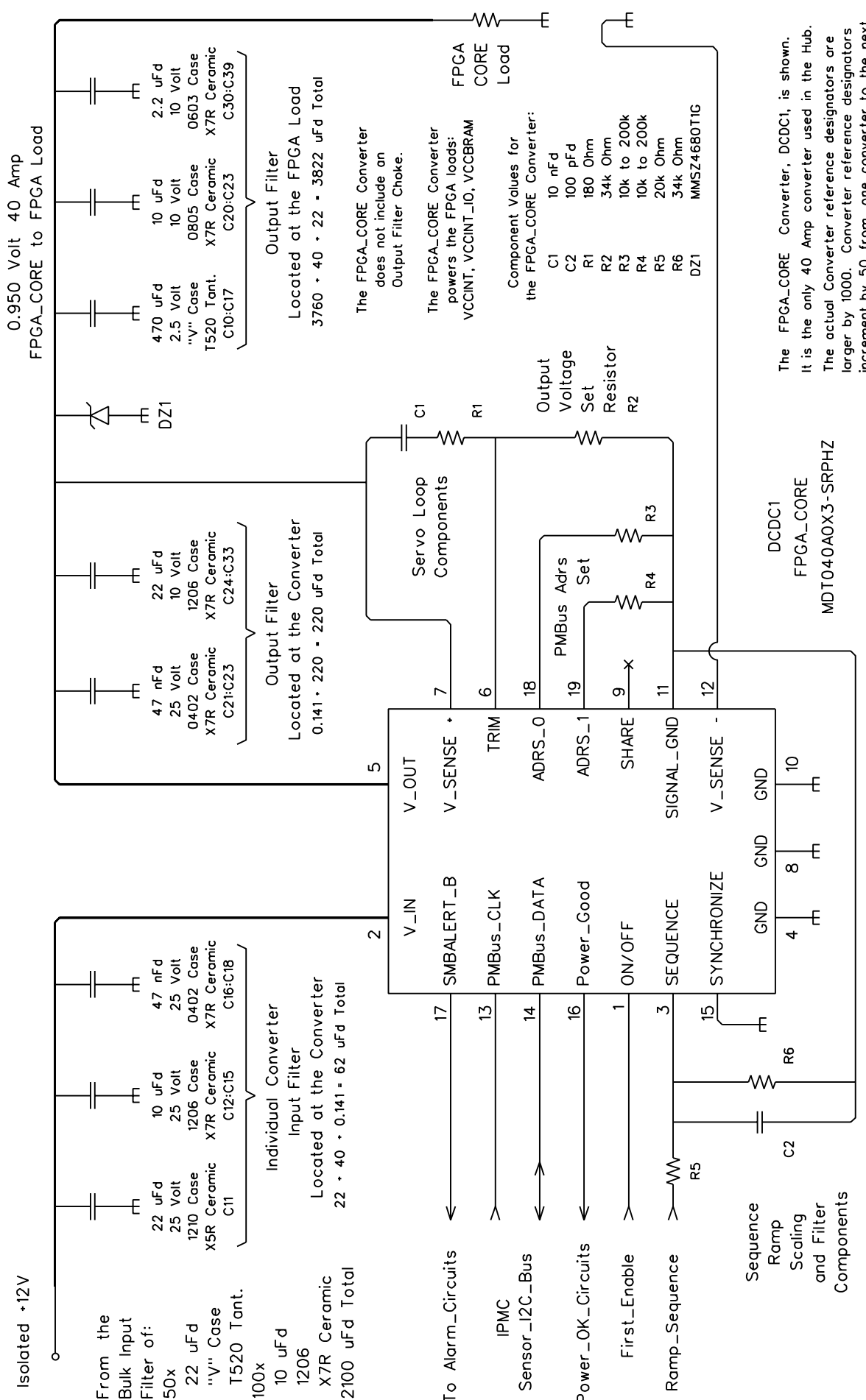


Hub Module 40 Amp DC-DC Converter Design



The FPGA_CORE Converter, DCDC1, is shown. It is the only 40 Amp converter used in the Hub. The actual Converter reference designators are larger by 1000. Converter reference designators increment by 50 from one converter to the next.