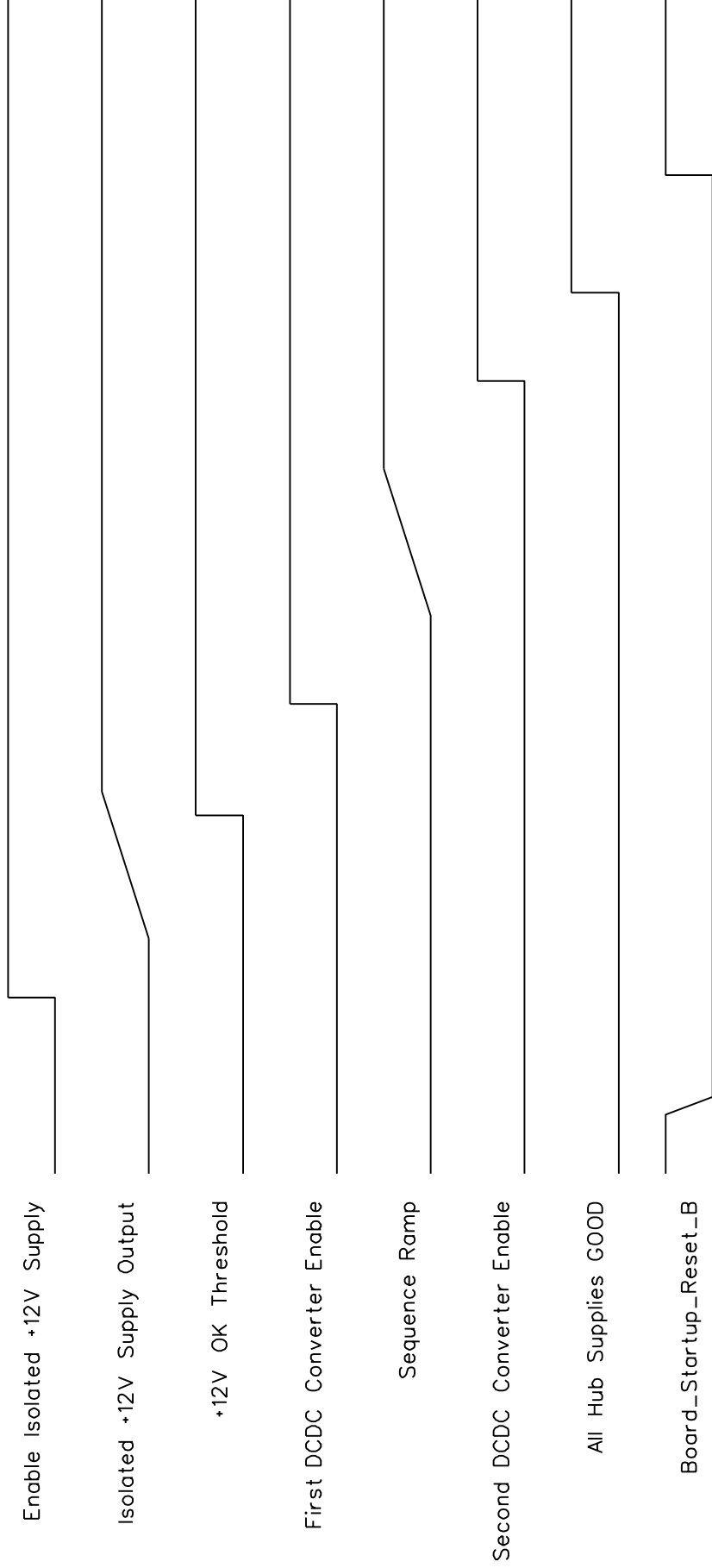


# Hub Power Supply Startup



## Requirements

Allow the Isolated +12V Supply to stabilize for 500 msec before asserting the First DCDC Converter Enable.

First DCDC Converter Enable must be asserted for 20 msec minimum before the Sequence Ramp starts.

The Sequence Ramp should bring up the FPGA supplies in about 5 to 9 msec.

Once the Sequence Ramp is complete must wait a minimum of 10 msec before asserting the Second DCDC Converter Enable.

The SWCH\_1V2 supply must ramp up within 2 msec maximum.

All supplies must be up and stable for 100 msec minimum before the Startup Reset is dropped.