

FMC pin	FMC line	TTC FMC name	function	note	IO standard	default
C30	FMC_SCL	SCL	typical FMC I2C clock	CDR I2C clock when jumper J8 in position 2-3	LVTTTL 3.3V	pull-up
C31	FMC_SDA	SDA	typical FMC I2C data	CDR I2C data when jumper J9 in position 2-3	LVTTTL 3.3V	pull-up
H4	CLK0_M2C_P	4Y	x-point out4 (p)		LVDS	
H5	CLK0_M2C_N	4Z	x-point out4 (n)		LVDS	
G2	CLK1_M2C_P	2Y	x-point out2 (p)		LVDS	
G3	CLK1_M2C_N	2Z	x-point out2 (n)		LVDS	
K4	CLK2_BIDIR_P	3Y	x-point out3 (p)		LVDS	
K5	CLK2_BIDIR_N	3Z	x-point out3 (n)		LVDS	
G6	LA00_P	S10	x-point out1 select (msb)		LVC MOS	pull-down
G7	LA00_N	3DE	x-point out3 enable	active high	LVC MOS	pull-down
D8	LA01_P	2DE	x-point out2 enable	active high	LVC MOS	pull-down
D9	LA01_N	S41	x-point out3 select (lsb)	only when autoswitch=0	LVC MOS	pull-up
H7	LA02_P	S11	x-point out1 select (lsb)		LVC MOS	pull-up
H8	LA02_N	S20	x-point out2 select (msb)		LVC MOS	pull-up
G9	LA03_P	S21	x-point out2 select (lsb)		LVC MOS	pull-up
G10	LA03_N	S40	x-point out4 select (msb)		LVC MOS	pull-down
H10	LA04_P	S30	x-point out3 select (msb)		LVC MOS	pull-up
H11	LA04_N	S31	x-point out3 select (lsb)		LVC MOS	pull-up
D11	LA05_P	AUTOSWITCH	x-point out4 autoswitch to in1 when CDR not locked	active high	LVC MOS	pull-up
D12	LA05_N	USER_LED_N	user led (active low)		LVC MOS	pull-up
C10	LA06_P	USER_IN_P	the lemo provided input converted to LVDS (p)		LVDS	
C11	LA06_N	USER_IN_N	the lemo provided input converted to LVDS (n)		LVDS	
H13	LA07_P	CDR_SCL	CDR I2C clock	CDR I2C clock when jumper J8 in position 1-2	LVC MOS	pull-up
H14	LA07_N	CDR_SDA	CDR I2C data i/o	CDR I2C data when jumper J9 in position 1-2	LVC MOS	pull-up
G12	LA08_P	CDR_LOL	CDR loss-of-link output	active high	LVC MOS	
G13	LA08_N	CDR_LOS	CDR loss-of-sync output	active high	LVC MOS	
D14	LA09_P	CDR_DATAOUT_P	CDR recovered data output (p)		LVDS	
D15	LA09_N	CDR_DATAOUT_N	CDR recovered data output (n)		LVDS	
C14	LA10_P	DIVIDER_RST_B	clock divider reset	active low	LVC MOS	pull-up
C15	LA10_N	DIVIDER_DIV4	clock divider select ratio	when 1: ÷4 , when 0: ÷2	LVC MOS	pull-up

LVC MOS: 1.8V/2.5V/3.3V compatible