J2 Pin usage for D0 Level 2

Numbers in bold are pins on J2 (or J3 for VBD)

3/12/2002 10:17

	Alpha J2 #	Administrator	Alpha J2 #	Worker	MBT	FIC	VBD (J3)
IN	A24	SCL Initialize Interrupt	A24	Worker_Interrupt			
	A23	L2 Answer Ready	C7	VME Idle (Scaler bit 30)			
	C25	VBD DONE*	C8	MBus Idle (Scaler bit 31)	Busy **A1** (O.C.)		SRDY* **C8**
OUT	A21	VBD START*		reserved	SCL Initialize **A2**	Busy **A2**	DONE* **C10**
	C21	Worker_1 Interrupt	C21	Test Out 1	L2 Answer Ready **A3**		
	C22	Worker_2 Interrupt	C22	Test Out 2			
	C32	Worker_3 Interrupt	C32	Test Out 3			
	C24	Worker_4 Interrupt	C24	Test Out 4			
	A 12	Worker_5 Interrupt	A 12	Test Out 5			

MBT, FIC pins need not correspond to Alpha since pins are not bussed, but hand-wired

may not be needed
Will try Prog. I/O + Poll

Inputs active low: connection to Busy interpreted as Busy*
Thus: ECL scalers should read %Idle, not % Busy