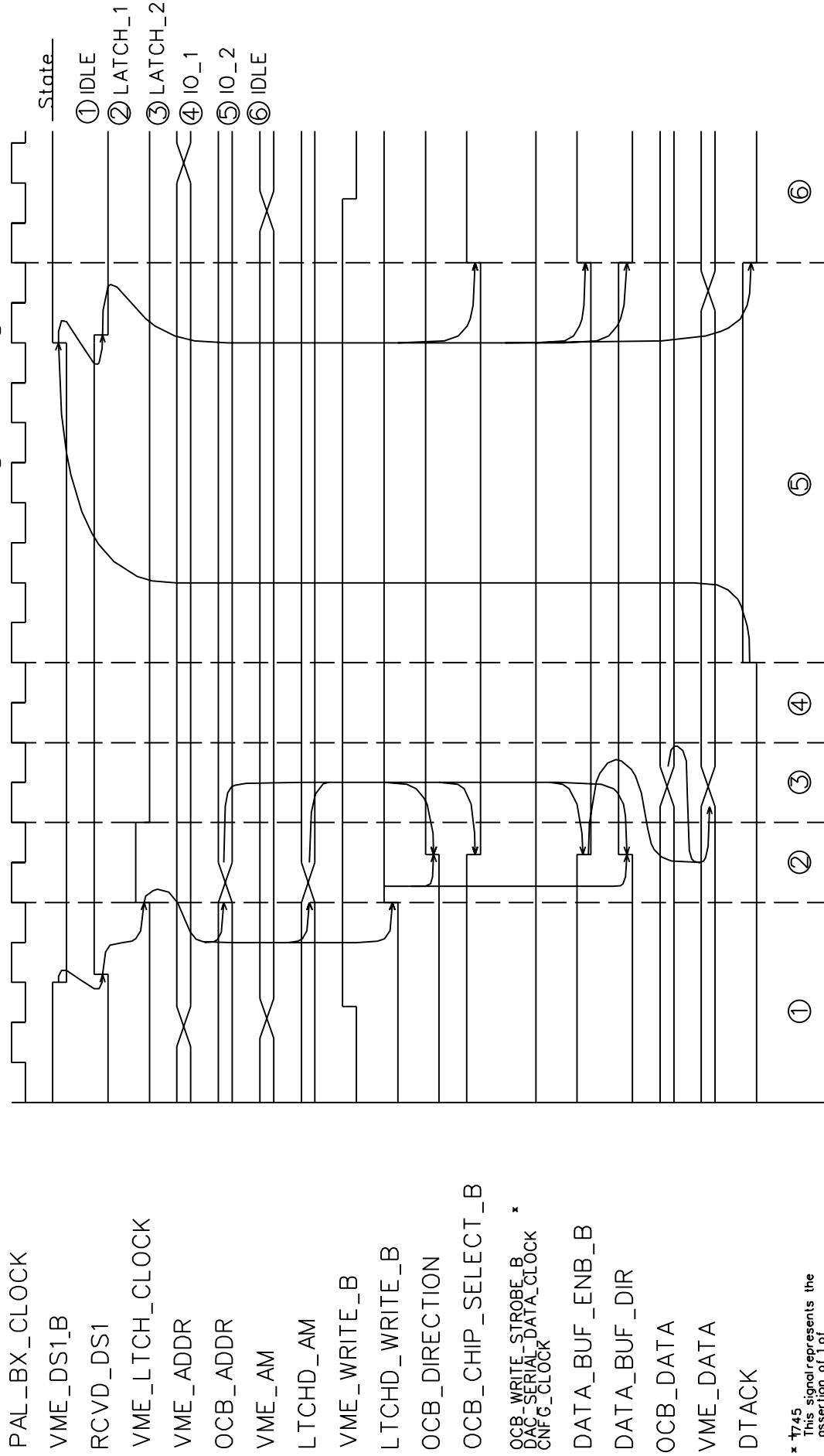


ADF-2 PAL VME READ Timing Diagram



*1745 This signal represents the assertion of the OCB chip selects OCB_WRITE_STROBE_B and DAC_SERIAL_DATA_CLOCK are active on the falling edge and CNFG_CLOCK is active on the rising edge.