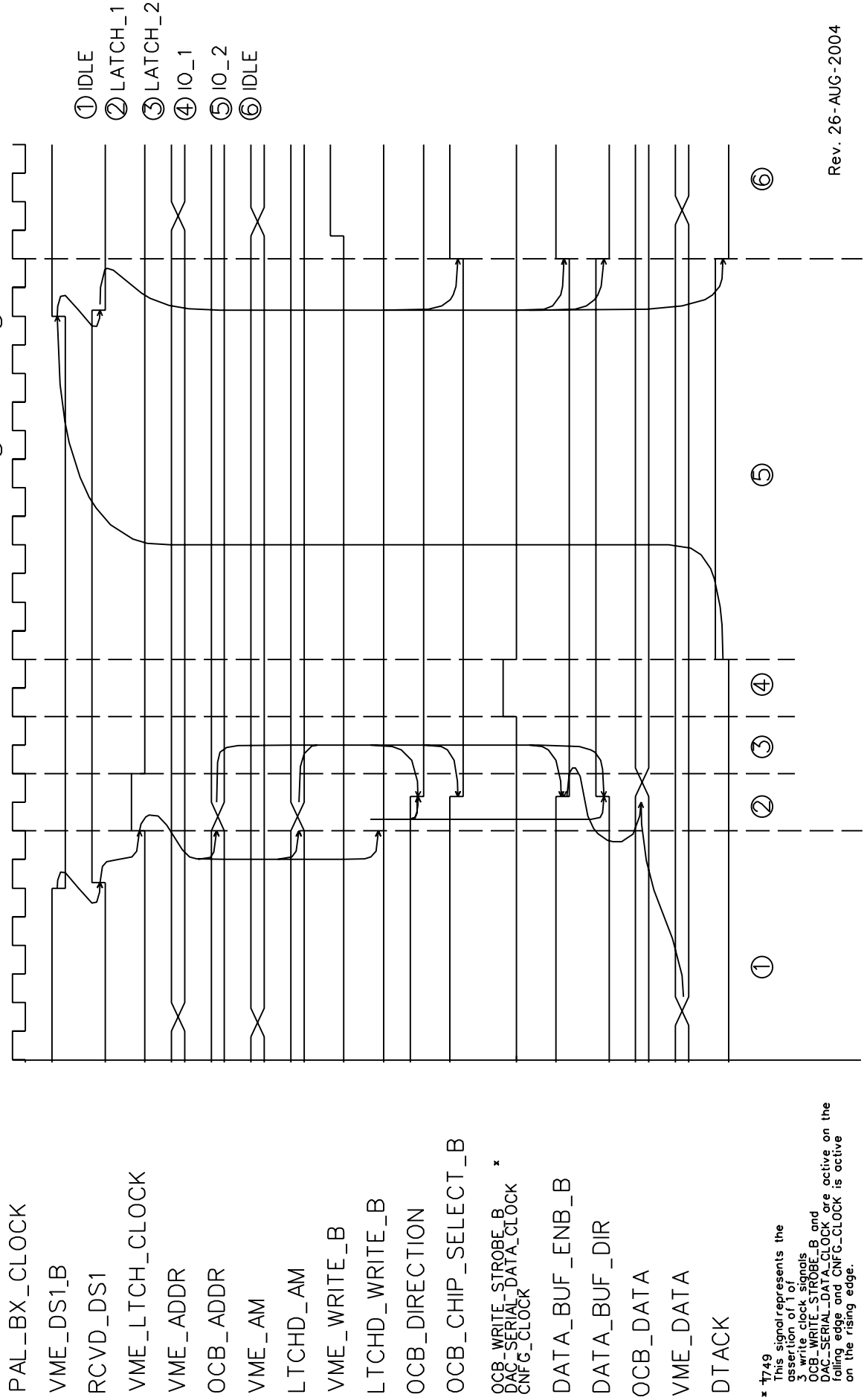


ADF-2 PAL VME WRITE Timing Diagram



* T749 This signal represents the assertion of 1 of 3 write clock signals OCB_WRITE_STROBE_B and DAC_SERIAL_DATA_CLOCK are active on the falling edge and CNFG_CLOCK is active on the rising edge.