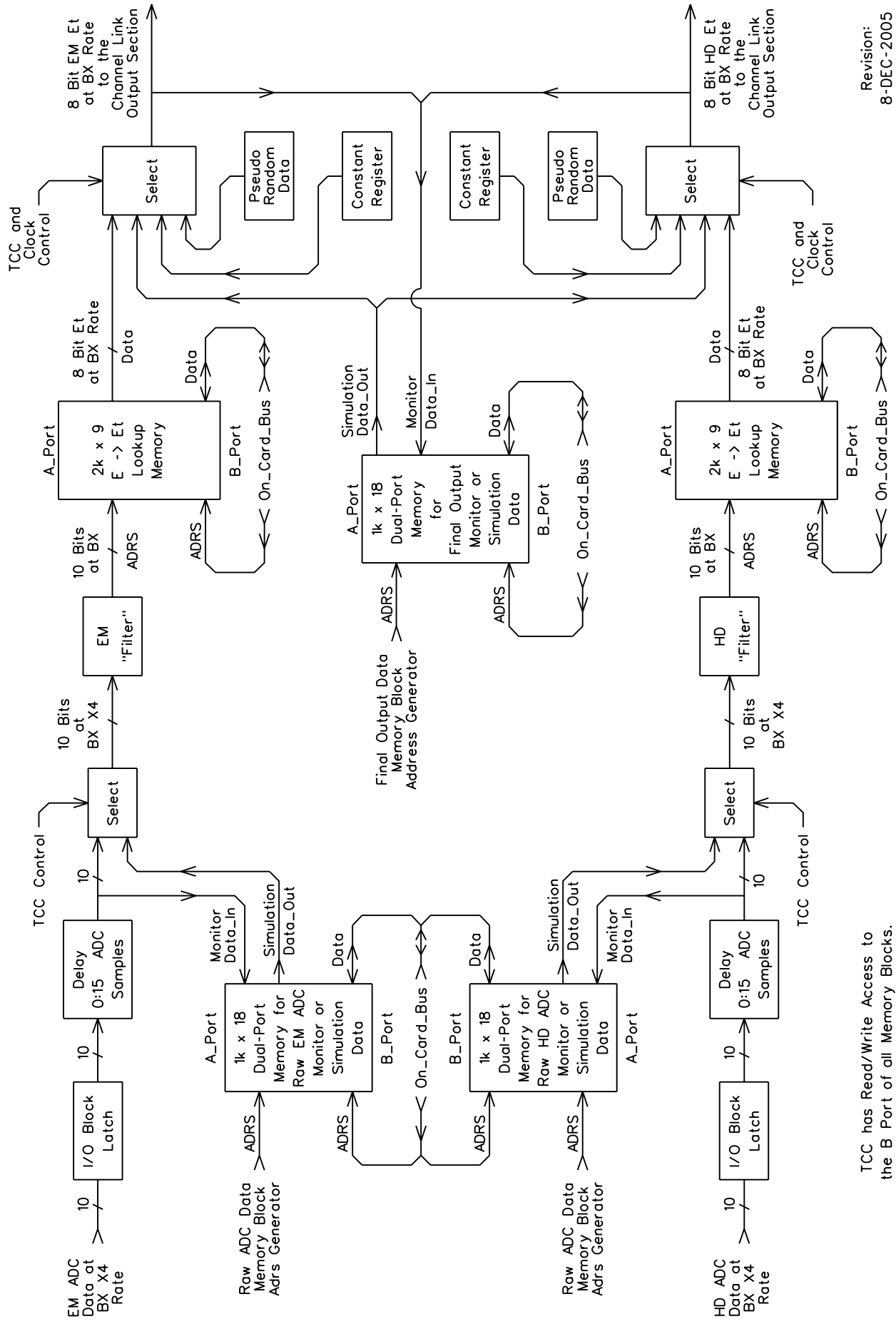


Data Path FPGA Channel Pair Signal Processing



TCC has Read/Write Access to the B Port of all Memory Blocks.