

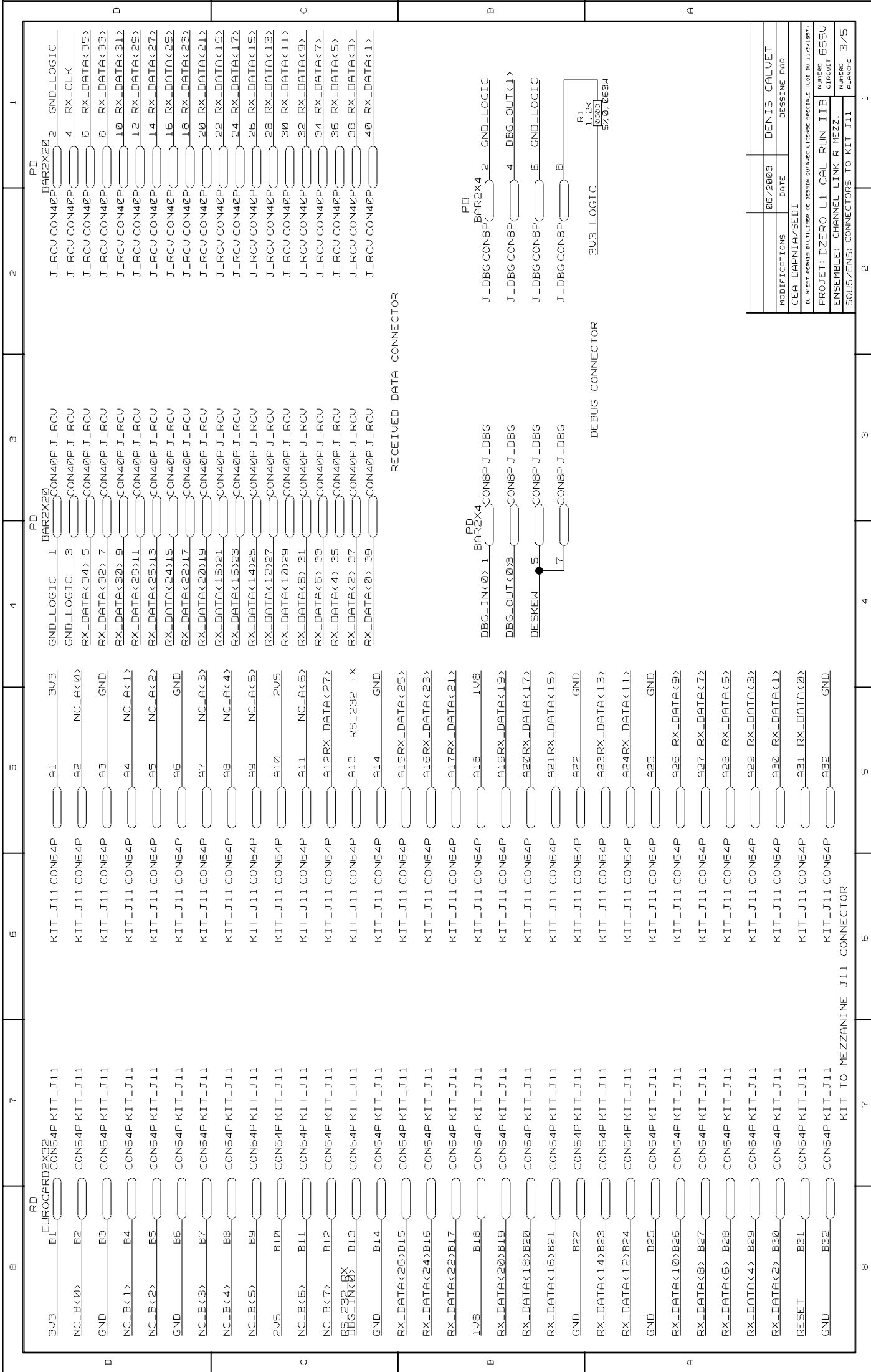
BC<1>	R11 47K 05603	GND_LOGIC
DATA_BUS_WIDTH = 8 BIT		
BC<3>	R13 47K 05603	GND_LOGIC
BC<2>	R12 47K 05603	GND_LOGIC
CARD_ID = "00"		

MODIFICATIONS	DATE	DENIS CALVET
CEA DAPNIA/SEDI		
IL N°057 PERMIS D'UTILISER CE DESSIN POUR UNE LICENCE SPECIALE (LOT DU 11/2/1987)		
PROJET:	DZERO LI CAL RUN IIB	NUMERO CIRCUIT 665V
ENSEMBLE:	CHANNEL LINK R MEZZ.	NUMERO PLANCHE I/S
SOUS/ENS:	BUS TRANSCEIVERS	

8	7	6	5	4	3	2	1
D	1V5 RD EUROCARD2X32 CON54P KIT_J12	KIT_J12 CON54P	RD EUROCARD2X32 AL	2V5	GND	HE10_50 CON50P J10	HE10_50 CON50P J10
	1V8 CON54P KIT_J12	KIT_J12 CON54P	A2	3V3	BA<0>	CON50P J10	49 SU
	GND CON54P KIT_J12	KIT_J12 CON54P	A3	GND	BA<1>	CON50P J10	47 AA<0>
	MEZZ_TCK CON54P KIT_J12	KIT_J12 CON54P	A4	MEZZ_TDI	BA<2>	CON50P J10	45 AA<1>
	MEZZ_TMS CON54P KIT_J12	KIT_J12 CON54P	A5	MEZZ_TDO	BA<3>	CON50P J10	43 AA<2>
	GND CON54P KIT_J12	KIT_J12 CON54P	A6	GND	BA<4>	CON50P J10	41 AA<3>
	DATA<0> CON54P KIT_J12	KIT_J12 CON54P	A7	DATA<1>	BA<5>	CON50P J10	39 AA<4>
	DATA<2> CON54P KIT_J12	KIT_J12 CON54P	A8	DATA<3>	BA<6>	CON50P J10	37 AA<5>
	DATA<4> CON54P KIT_J12	KIT_J12 CON54P	A9	DATA<5>	BA<7>	CON50P J10	35 AA<6>
	DATA<6> CON54P KIT_J12	KIT_J12 CON54P	A10	DATA<7>	BB<0>	CON50P J10	33 AA<7>
	GND CON54P KIT_J12	KIT_J12 CON54P	A11	GND	BB<1>	CON50P J10	31 AB<0>
C	LED_USER CON54P KIT_J12	KIT_J12 CON54P	A12RX_DATA<2B>		BB<2>	CON50P J10	29 AB<1>
	CS_B CON54P KIT_J12	KIT_J12 CON54P	A13RX_DATA<29>		BB<3>	CON50P J10	27 AB<2>
	DBG_OUT<0> CON54P KIT_J12	KIT_J12 CON54P	A14RX_DATA<30>		BB<4>	CON50P J10	25 AB<3>
	DBG_OUT<1> CON54P KIT_J12	KIT_J12 CON54P	A15RX_DATA<31>		BB<5>	CON50P J10	23 AB<4>
	GND CON54P KIT_J12	KIT_J12 CON54P	A16	GND	BB<6>	CON50P J10	21 AB<5>
	NC<2> CON54P KIT_J12	KIT_J12 CON54P	A17RX_DATA<32>		BB<7>	CON50P J10	19 AB<6>
	RX_DATA<33> CON54P KIT_J12	KIT_J12 CON54P	A18	NC<0>	BC<0>	CON50P J10	17 AB<7>
	NC<3> CON54P KIT_J12	KIT_J12 CON54P	A19	NC<1>	BC<1>	CON50P J10	15 AC<0>
	ADDR<1> CON54P KIT_J12	KIT_J12 CON54P	A20	ADDR<0>	BC<2>	CON50P J10	13 AC<1>
	ADDR<3> CON54P KIT_J12	KIT_J12 CON54P	A21	ADDR<2>	BC<3>	CON50P J10	11 AC<2>
	ADDR<5> CON54P KIT_J12	KIT_J12 CON54P	A22	ADDR<4>	BC<4>	CON50P J10	9 AC<3>
	ADDR<7> CON54P KIT_J12	KIT_J12 CON54P	A23	ADDR<6>	BC<5>	CON50P J10	7 AC<4>
	ADDR<9> CON54P KIT_J12	KIT_J12 CON54P	A24	ADDR<8>	BC<6>	CON50P J10	5 AC<5>
	GND CON54P KIT_J12	KIT_J12 CON54P	A25	GND	BC<7>	CON50P J10	3 AC<6>
	ADDR<11> CON54P KIT_J12	KIT_J12 CON54P	A26	ADDR<10>	BC<7>	CON50P J10	1 AC<7>
	DTACK_B CON54P KIT_J12	KIT_J12 CON54P	A27RX_DATA<34>				
	WE CON54P KIT_J12	KIT_J12 CON54P	A28RX_DATA<35>				
	CLK_BC_PLL CON54P KIT_J12	KIT_J12 CON54P	A29	RX_CLK			
	CLK_BC_AX_INB0 CON54P KIT_J12	KIT_J12 CON54P	A30	CLK_BC_IN			
	SCL_CLK CON54P KIT_J12	KIT_J12 CON54P	A31	WCLK			
	PARDWN CON54P KIT_J12	KIT_J12 CON54P	A32	GND			
	KIT TO MEZZANINE J12 CONNECTOR						

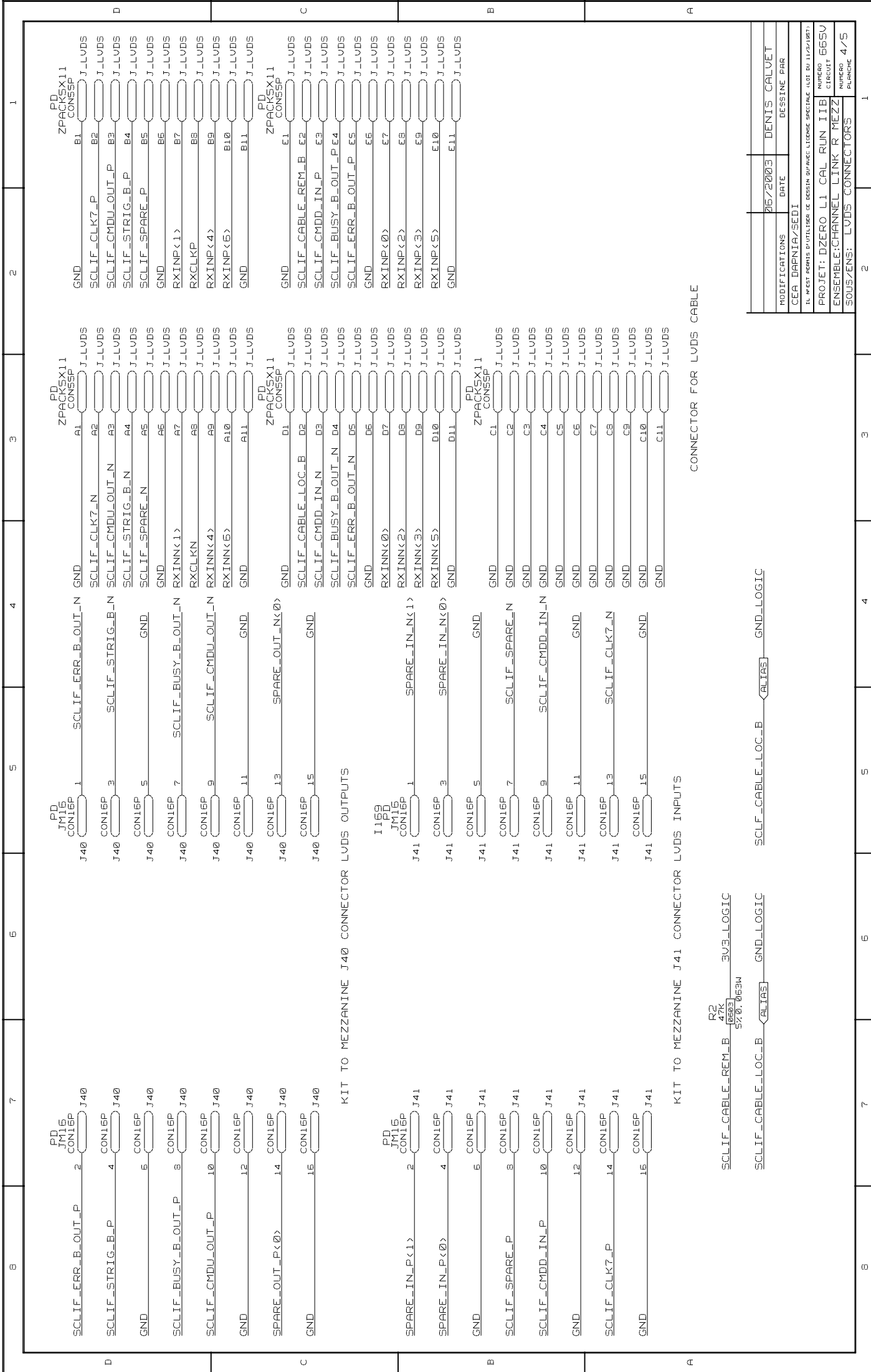
PCI IO CARD CONNECTOR

MODIFICATIONS	26/2003	DATE	DENIS CALVET
CEA DAPNIA/SEDI			DESSINE PAR
IL N'EST PERMIS D'UTILISER CE DESIN QU'AVEC LICENCE SPECIALE (LOT DU 11/1987)			
PROJET: DZERO L1 CAL RUN IIB		NUMERO CIRCUIT	665V
ENSEMBLE: CHANNEL LINK R MEZZ.		NUMERO PLANCH	2/5
SOUS/ENS:CONNECTORS TO KIT J12			



MODIFICATIONS	06/2003	DATE	DENIS CALVET
CEA DAPNIA/SEDI			DESINE PAR
IL N'EST PERMIS D'UTILISER CE DESIN QU'AVEC LICENCE SPECIALE ALOT DU 11/01/1987.			
PROJET: DZERO L1 CAL RUN IIB			NUMERO CIRCUIT 665V
ENSEMBLE: CHANNEL LINK R MEZZ.			NUMERO PLANCHE 3/5
SOUSS/ENS: CONNECTORS TO KIT J11			

1	2	3	4	5	6	7	8
KIT TO MEZZANINE J11 CONNECTOR							
3V3	EUROCARD2X32 CON64P	KIT_J11_CONBP	A1	3V3	KIT_J11_CONBP	B1	3V3
NC_B<0>	CON64P	KIT_J11_CONBP	A2	NC_A<0>	KIT_J11_CONBP	B2	NC_B<0>
GND	CON64P	KIT_J11_CONBP	A3	GND	KIT_J11_CONBP	B3	GND
NC_B<1>	CON64P	KIT_J11_CONBP	A4	NC_A<1>	KIT_J11_CONBP	B4	NC_B<1>
NC_B<2>	CON64P	KIT_J11_CONBP	A5	NC_A<2>	KIT_J11_CONBP	B5	NC_B<2>
GND	CON64P	KIT_J11_CONBP	A6	GND	KIT_J11_CONBP	B6	GND
NC_B<3>	CON64P	KIT_J11_CONBP	A7	NC_A<3>	KIT_J11_CONBP	B7	NC_B<3>
NC_B<4>	CON64P	KIT_J11_CONBP	A8	NC_A<4>	KIT_J11_CONBP	B8	NC_B<4>
NC_B<5>	CON64P	KIT_J11_CONBP	A9	NC_A<5>	KIT_J11_CONBP	B9	NC_B<5>
2V5	CON64P	KIT_J11_CONBP	A10	2V5	KIT_J11_CONBP	B10	2V5
NC_B<6>	CON64P	KIT_J11_CONBP	A11	NC_A<6>	KIT_J11_CONBP	B11	NC_B<6>
NC_B<7>	CON64P	KIT_J11_CONBP	A12	RX_DATA<27>	KIT_J11_CONBP	B12	NC_B<7>
RS_232_RX	CON64P	KIT_J11_CONBP	A13	RS_232_TX	KIT_J11_CONBP	B13	RS_232_RX
DBG_IN<0>	CON64P	KIT_J11_CONBP	A14	GND	KIT_J11_CONBP	B14	DBG_IN<0>
GND	CON64P	KIT_J11_CONBP	A15	RX_DATA<25>	KIT_J11_CONBP	B15	GND
RX_DATA<26>	CON64P	KIT_J11_CONBP	A16	RX_DATA<23>	KIT_J11_CONBP	B16	RX_DATA<26>
RX_DATA<27>	CON64P	KIT_J11_CONBP	A17	RX_DATA<21>	KIT_J11_CONBP	B17	RX_DATA<27>
1V8	CON64P	KIT_J11_CONBP	A18	1V8	KIT_J11_CONBP	B18	1V8
RX_DATA<20>	CON64P	KIT_J11_CONBP	A19	RX_DATA<19>	KIT_J11_CONBP	B19	RX_DATA<20>
RX_DATA<19>	CON64P	KIT_J11_CONBP	A20	RX_DATA<17>	KIT_J11_CONBP	B20	RX_DATA<19>
RX_DATA<16>	CON64P	KIT_J11_CONBP	A21	RX_DATA<15>	KIT_J11_CONBP	B21	RX_DATA<16>
GND	CON64P	KIT_J11_CONBP	A22	GND	KIT_J11_CONBP	B22	GND
RX_DATA<14>	CON64P	KIT_J11_CONBP	A23	RX_DATA<13>	KIT_J11_CONBP	B23	RX_DATA<14>
RX_DATA<12>	CON64P	KIT_J11_CONBP	A24	RX_DATA<11>	KIT_J11_CONBP	B24	RX_DATA<12>
GND	CON64P	KIT_J11_CONBP	A25	GND	KIT_J11_CONBP	B25	GND
RX_DATA<10>	CON64P	KIT_J11_CONBP	A26	RX_DATA<9>	KIT_J11_CONBP	B26	RX_DATA<10>
RX_DATA<8>	CON64P	KIT_J11_CONBP	A27	RX_DATA<7>	KIT_J11_CONBP	B27	RX_DATA<8>
RX_DATA<6>	CON64P	KIT_J11_CONBP	A28	RX_DATA<5>	KIT_J11_CONBP	B28	RX_DATA<6>
RX_DATA<4>	CON64P	KIT_J11_CONBP	A29	RX_DATA<3>	KIT_J11_CONBP	B29	RX_DATA<4>
RX_DATA<2>	CON64P	KIT_J11_CONBP	A30	RX_DATA<1>	KIT_J11_CONBP	B30	RX_DATA<2>
RESET	CON64P	KIT_J11_CONBP	A31	RX_DATA<0>	KIT_J11_CONBP	B31	RESET
GND	CON64P	KIT_J11_CONBP	A32	GND	KIT_J11_CONBP	B32	GND



PD
 JM16
 CON16P
 J40 1 SCLIF_ERR_B_OUT_N GND
 CON16P 3 SCLIF_STRIG_B_N J40
 CON16P 5 GND J40
 CON16P 7 SCLIF_BUSY_B_OUT_N J40
 CON16P 9 SCLIF_CMDU_OUT_N J40
 CON16P 11 GND J40
 CON16P 13 SPARE_OUT_N<0> J40
 CON16P 15 GND J40

PD
 ZPACKSX11
 CONSSP
 A1 J.LVDS
 A2 J.LVDS
 A3 J.LVDS
 A4 J.LVDS
 A5 J.LVDS
 A6 J.LVDS
 A7 J.LVDS
 A8 J.LVDS
 A9 J.LVDS
 A10 J.LVDS
 A11 J.LVDS

PD
 JM16
 CON16P
 J41 1 SPARE_IN_N<1> J41
 CON16P 3 SPARE_IN_N<0> J41
 CON16P 5 GND J41
 CON16P 7 SCLIF_SPARE_P J41
 CON16P 9 SCLIF_CMDD_IN_P J41
 CON16P 11 GND J41
 CON16P 12 CON16P J41
 CON16P 14 CON16P J41
 CON16P 16 CON16P J41

PD
 JM16
 CON16P
 J40 2 CON16P J40
 CON16P 4 CON16P J40
 CON16P 6 CON16P J40
 CON16P 8 CON16P J40
 CON16P 10 CON16P J40
 CON16P 12 CON16P J40
 CON16P 14 CON16P J40
 CON16P 16 CON16P J40

PD
 ZPACKSX11
 CONSSP
 B1 J.LVDS
 B2 J.LVDS
 B3 J.LVDS
 B4 J.LVDS
 B5 J.LVDS
 B6 J.LVDS
 B7 J.LVDS
 B8 J.LVDS
 B9 J.LVDS
 B10 J.LVDS
 B11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 E1 J.LVDS
 E2 J.LVDS
 E3 J.LVDS
 E4 J.LVDS
 E5 J.LVDS
 E6 J.LVDS
 E7 J.LVDS
 E8 J.LVDS
 E9 J.LVDS
 E10 J.LVDS
 E11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 D1 J.LVDS
 D2 J.LVDS
 D3 J.LVDS
 D4 J.LVDS
 D5 J.LVDS
 D6 J.LVDS
 D7 J.LVDS
 D8 J.LVDS
 D9 J.LVDS
 D10 J.LVDS
 D11 J.LVDS

PD
 JM16
 CON16P
 J41 1 SPARE_IN_N<1> J41
 CON16P 3 SPARE_IN_N<0> J41
 CON16P 5 GND J41
 CON16P 7 SCLIF_SPARE_P J41
 CON16P 9 SCLIF_CMDD_IN_P J41
 CON16P 11 GND J41
 CON16P 12 CON16P J41
 CON16P 14 CON16P J41
 CON16P 16 CON16P J41

PD
 JM16
 CON16P
 J40 2 CON16P J40
 CON16P 4 CON16P J40
 CON16P 6 CON16P J40
 CON16P 8 CON16P J40
 CON16P 10 CON16P J40
 CON16P 12 CON16P J40
 CON16P 14 CON16P J40
 CON16P 16 CON16P J40

PD
 ZPACKSX11
 CONSSP
 C1 J.LVDS
 C2 J.LVDS
 C3 J.LVDS
 C4 J.LVDS
 C5 J.LVDS
 C6 J.LVDS
 C7 J.LVDS
 C8 J.LVDS
 C9 J.LVDS
 C10 J.LVDS
 C11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 F1 J.LVDS
 F2 J.LVDS
 F3 J.LVDS
 F4 J.LVDS
 F5 J.LVDS
 F6 J.LVDS
 F7 J.LVDS
 F8 J.LVDS
 F9 J.LVDS
 F10 J.LVDS
 F11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 G1 J.LVDS
 G2 J.LVDS
 G3 J.LVDS
 G4 J.LVDS
 G5 J.LVDS
 G6 J.LVDS
 G7 J.LVDS
 G8 J.LVDS
 G9 J.LVDS
 G10 J.LVDS
 G11 J.LVDS

PD
 JM16
 CON16P
 J41 1 SPARE_IN_N<1> J41
 CON16P 3 SPARE_IN_N<0> J41
 CON16P 5 GND J41
 CON16P 7 SCLIF_SPARE_P J41
 CON16P 9 SCLIF_CMDD_IN_P J41
 CON16P 11 GND J41
 CON16P 12 CON16P J41
 CON16P 14 CON16P J41
 CON16P 16 CON16P J41

PD
 JM16
 CON16P
 J40 2 CON16P J40
 CON16P 4 CON16P J40
 CON16P 6 CON16P J40
 CON16P 8 CON16P J40
 CON16P 10 CON16P J40
 CON16P 12 CON16P J40
 CON16P 14 CON16P J40
 CON16P 16 CON16P J40

PD
 ZPACKSX11
 CONSSP
 H1 J.LVDS
 H2 J.LVDS
 H3 J.LVDS
 H4 J.LVDS
 H5 J.LVDS
 H6 J.LVDS
 H7 J.LVDS
 H8 J.LVDS
 H9 J.LVDS
 H10 J.LVDS
 H11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 I1 J.LVDS
 I2 J.LVDS
 I3 J.LVDS
 I4 J.LVDS
 I5 J.LVDS
 I6 J.LVDS
 I7 J.LVDS
 I8 J.LVDS
 I9 J.LVDS
 I10 J.LVDS
 I11 J.LVDS

PD
 ZPACKSX11
 CONSSP
 J1 J.LVDS
 J2 J.LVDS
 J3 J.LVDS
 J4 J.LVDS
 J5 J.LVDS
 J6 J.LVDS
 J7 J.LVDS
 J8 J.LVDS
 J9 J.LVDS
 J10 J.LVDS
 J11 J.LVDS

PD
 JM16
 CON16P
 J41 1 SPARE_IN_N<1> J41
 CON16P 3 SPARE_IN_N<0> J41
 CON16P 5 GND J41
 CON16P 7 SCLIF_SPARE_P J41
 CON16P 9 SCLIF_CMDD_IN_P J41
 CON16P 11 GND J41
 CON16P 12 CON16P J41
 CON16P 14 CON16P J41
 CON16P 16 CON16P J41

PD
 JM16
 CON16P
 J40 2 CON16P J40
 CON16P 4 CON16P J40
 CON16P 6 CON16P J40
 CON16P 8 CON16P J40
 CON16P 10 CON16P J40
 CON16P 12 CON16P J40
 CON16P 14 CON16P J40
 CON16P 16 CON16P J40

PD
 ZPACKSX11
 CONSSP
 K1 J.LVDS
 K2 J.LVDS
 K3 J.LVDS
 K4 J.LVDS
 K5 J.LVDS
 K6 J.LVDS
 K7 J.LVDS
 K8 J.LVDS
 K9 J.LVDS
 K10 J.LVDS
 K11 J.LVDS

CONNECTOR FOR LVDS CABLE

KIT TO MEZZANINE J41 CONNECTOR LVDS INPUTS

KIT TO MEZZANINE J40 CONNECTOR LVDS OUTPUTS

R2
 47K
 5% 0.063W
 SCLIF_CABLE_REM_B

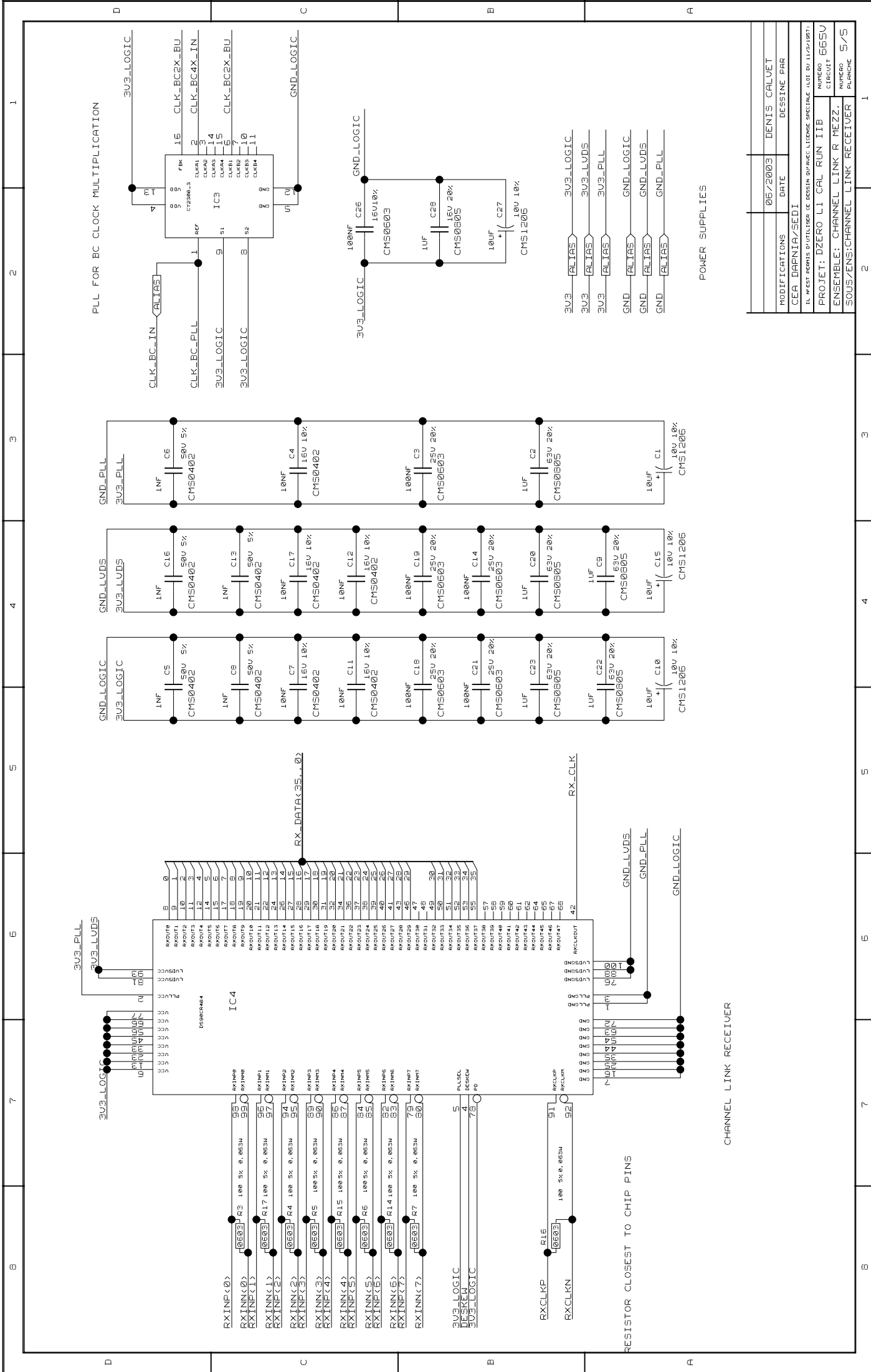
R2
 47K
 5% 0.063W
 SCLIF_CABLE_REM_B

SCLIF_CABLE_LOC_B
 GND_LOGIC

SCLIF_CABLE_LOC_B
 GND_LOGIC

SCLIF_CABLE_LOC_B
 GND_LOGIC

MODIFICATIONS	05/2003	DENIS CALVET
DATE		DESSINE PAR
CEA DAPNIA/SEDI		
IL N° EST PERMIS D'UTILISER CE DESIGN D'UN ACCES LICENCE SPECIALE (LOT DU 11/07/1987)		
PROJET:	DZERO L1 CAL RUN IIB	NUMERO
ENSEMBLE:	CHANNEL LINK R MEZZ	CIRCUIT
SOUS/SENS:	LVDS CONNECTORS	NUMERO
		PLANCHE
		4/5



CHANNEL LINK RECEIVER

RESISTOR CLOSEST TO CHIP PINS

POWER SUPPLIES

MODIFICATIONS	06/2003	DENIS GALVET
CEA DAPNIA/SEDI	DATE	DESSINE PAR
IL N°EST PERMIS D'UTILISER CE DESSIN D'UNQUE LICENCE SPECIALE LOTI DU 11/01/1987.		
PROJET:	DZERO L1 CAL RUN IIB	NUMERO CIRCUIT
ENSEMBLE:	CHANNEL LINK R MEZZ.	NUMERO PLANCHE
SOUS/ENS:	CHANNEL LINK RECEIVER	PLANCHE

D	C	B	A
1			1
2			2
3			3
4			4
5			5
6			6
7			7
8			8
D	C	B	A