# The Institute for Cyber-Enabled Research: Regional Organization to Promote Computation in Science

Dirk Colbry, Bill Punch and Wolfgang Bauer
The Institute for Cyber-Enabled Research
Michigan State University
East Lansing, USA
{colbrydi; punch; bauerw}@msu.edu

## ABSTRACT

The Institute for Cyber-Enabled Research (iCER) at Michigan State University (MSU) was established in 2009 to coordinate and support multidisciplinary resources for computation and computational sciences. iCER is the home of MSU's centralized High Performance Computing resources, which include a heterogeneous compute cluster with various hardware designed to meet the needs of different scientists. The goal of iCER is not to maximize Flops, but instead to maximize the amount of quality science being accomplished. This paper outlines many of the initiatives that iCER has taken to support researchers at MSU and throughout Michigan; describes the structure and evolution of iCER and its relationship with XSEDE; and offers insights for other institutions interested in putting together a similar computational organization.

## Categories and Subject Descriptors

K.6 Management of Computing and Information Systems.

## General Terms

Management, Measurement, Performance, Reliability, Experimentation, Human Factors

## Keywords

Keywords are your own designated keywords.

## 1. INTRODUCTION

Established in 1855, Michigan State University (MSU) is one of the top research universities in the world. Home to nationally ranked and recognized academic, residential college, and service-learning programs, MSU is a diverse community of dedicated students and scholars, athletes and artists, scientists and leaders [1].

MSU has long been a leader in computational science, establishing a computer science program on campus in 1967 and a High Performance Computing Center (HPCC) in 2005. Before 2009, researchers using the HPCC at MSU had completed less than 500 cpu-years of computation, executing 1 million jobs on the HPCC. Ongoing internal evaluations of the HPCC pointed to a

need for more than high performance computing (HPC) hardware in order to support advanced computation. We found that scientists also needed help establishing new scientific workflows and using advanced computing systems. The growing importance of computation for advances in natural science, engineering, medicine, and other areas led MSU to develop a new unit – iCER – to support computational research.

## 2. iCER Structure and Resources

In 2009, MSU established the Institute for Cyber-Enabled Research (iCER) as a subunit of the MSU Office of the Vice President of Research and Graduate Studies. iCER was created to support researchers and encourage collaborations across academia and industry, with the goal of improving the use of computation for research. iCER includes the previously existing HPCC, with a staff of four system administrators and a full-time administrative assistant working with direction from a computer science faculty member who serves as the HPCC Director.

iCER also supports two full-time Computational Research Specialists to support students, faculty, and other computational researchers through education, training, and one-on-one consulting appointments. Currently, the iCER Computational Research Specialists include a PhD in computer science and a PhD in mathematics. In addition, iCER has established a large student internship program to train undergraduates, and supports a variety of post-doctoral scholars, computational specialists, and other experts to assist in research collaborations across campus. The founding director of iCER is a faculty member in physics.

## 3. Computation at Michigan State University

Currently at MSU there are about 1500 registered iCER researchers, 50+ registered XSEDE users [2], and two Petascale Computing Resource Allocation (PRAC) award winners who are able to run on Blue Waters [3]. Figure 1 shows the numbers of active users on the MSU HPCC since iCER was founded.

The hardware capacity of the HPCC has been consistently increased every 1 to 2 years and now includes several thousand processor cores; hundreds of terabytes of high-performance, tiered data storage; large memory systems; a 10- gigabit per second Internet connection; and security, collaboration software, network management and virtualization platforms. More specifically, the HPCC currently includes:

**General Purpose Clusters**

- A 384-core distributed memory cluster utilizing 2.2 GHz AMD Opteron 275 dual core processors connected by Single Data Rate (SDR) 10 gigabit Infiniband

- A 1024-core distributed memory cluster using Intel Xeon E5345 2.3 Ghz quad core processors, also connected by SDR Infiniband

- A 1536-core distributed memory cluster using Intel Xeon E5620 processors, connected by Quad Data Rate (QDR) 40 gigabit Infiniband

**GPU Accelerated Systems**

- Thirty-two 8-core graphics nodes utilizing 2.40GHz Intel Xeon E5530 processors. Each node has two NVidia Tesla M1060 General Purpose Graphics Processing Units

- An 8-core GPU accelerated server with 18GB of RAM. This node has two Nvidia Tesla C2075, one Nvidia Tesla C2070 and one Nvidia GTX 580

- A 4-core GPU accelerated server with 8GB of RAM. This node has one Nvidia GTX 480

- An 8-core GPU accelerated server with three Nvidia GTX 480s

**Large Shared Memory Systems**

- Four 32-core AMD Opteron-based multi-processor fat nodes each with 256 GB of available RAM and one 16 core AMD Opteron-based

- Two 32-core multi-processor fat node with Intel Xeon E7- 8837 processors and 512 GB of available RAM

- A 32-core multi-processor fat node with Intel Xeon E7- 8837 processors and 1 TB of available RAM

- Two 64-core multi-processor fat node with Intel Xeon E7- 8837 processors and 2 TB of available RAM

**Condor Cluster**

- ~1000 core condor cluster with 2-4 cores and 2-6GB of memory per node

**Storage**

- High-speed parallel temporary file systems utilizing Lustre parallel file systems for fast file throughput

- Persistent mirrored storage of over two hundred terabytes

The MSU HPC system was purposely designed to accommodate a wide range of job types, in order to meet the varying needs of researchers. As demonstrated by Figure 2, during the first four years of iCER, system utilization has increased from 506 cpu-years (in 2009) to 2,175 cpu-years (in 2012)[1]. Figure 3 shows a similar increase in total number of jobs run, from 1.8 million jobs in 2009 to 4.3 million jobs in 2012.

Although both cpu-years and total jobs increased over the last four years, it is interesting to note that the total numbers of jobs dropped in 2010 (to 1.4 million jobs). This decrease is a reflection of users' increasing shift from running multiple, single-core jobs to running fewer, multi-core jobs. Consequently, the efficiency of our system utilization also increase, as shown by Figure 4: in 2009, the average CPU utilization per job is about 1 core; in 2012, average CPU utilization increased to closer to 2 cores per job. It should be noted that CPU utilization does not include file I/O, and single-core jobs are still the vast majority (by four orders of magnitude).
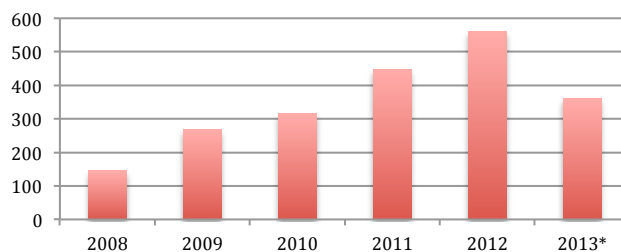
**Active Users**



Figure 1 - Increase in the number of active users since 2008, the year before iCER was first formed.
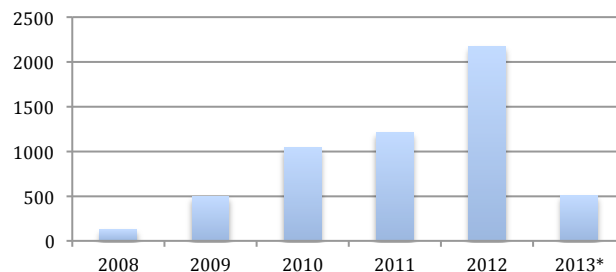
**cpu-years**



Figure 2 - Total cpu-years executed by HPCC users.
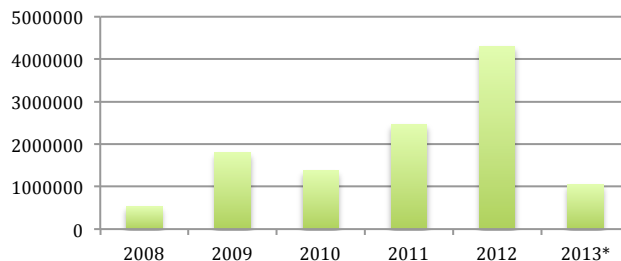
**Total Jobs**



Figure 3 - Total jobs run by HPCC users.
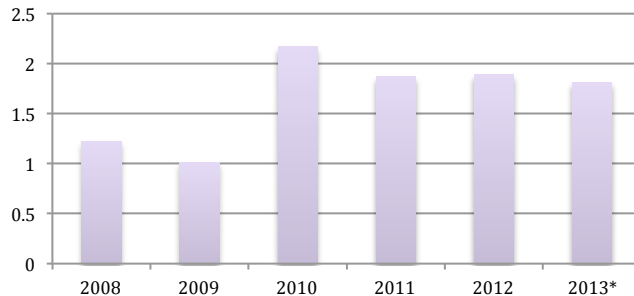
**Average CPU Utilization Per Job**



Figure 4 - Average CPU utilization per job on the HPCC.

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[1] Year to date, as of May 23, 2013

# 4. Decreasing "Mean Time to Science"

In addition to providing HPC hardware, the iCER team works hard to lower researchers' "mean time to science" by documenting and streamlining computational interfaces and reducing the barriers to entry for researchers unfamiliar with advanced computational hardware. For example, new users receive a "Portable HPC" USB flash drive that comes pre-loaded with ssh, X11 and SCP, and a plug-and-play format that allows Microsoft Windows users to gain immediate access to computational resources [4]. Developed by iCER staff, this "Portable HPC" drive allows novice users, with little or no prior HPC experience, to start using iCER resources quickly and easily. In addition to the general-purpose "Portable HPC," iCER has developed a variety of supports for domain-specific tasks. One example is providing pre-installed virtual machines for web-based biometric research, using software such as BLAST [5], Galaxy [6], and the UCSC Genome Browser [7].

Another way that iCER supports users and reduces the "mean time to science" is by developing a "one-queue" model for heterogeneous cluster scheduling [8]. In the one-queue approach, users specify what type of resources their jobs require, and the scheduler is tasked with determining which cluster resources best meet users' requests. The one-queue system is very simple for users, particularly for novices, since there is no need to parse queue names and descriptions to select the "best" resource for a specific job. In contrast, in traditional multi-queue systems, users must interpret the queue names ("short queue," "standby," etc.) and choose a queue to submit their job, then hope that they made the best choice to process the job quickly.

Although we feel that the HPC system available at MSU supports most of our researchers' needs, it is also iCER's goal to scale research to the point of exceeding the capabilities of local resources. For example, the HPCC staff has implemented an instance of a Globus Online Endpoint [9], which enables fast parallel file transfers to and from other national systems such as XSEDE [2] and Blue Waters [3]. iCER has also set up special accounts to make it easier for researchers to pay for EC2 [10] resources using internal university account numbers.

# 5. iCER Research Specialists

HPC resources are maintained by a team of system administrators and programmers, while iCER Computational Research Specialists focus on providing direct user support through task-specific training, continuing education and consulting services for researchers from all disciplines. iCER Research Specialists engage in a variety of tasks, as described in the remainder of this section.

## 5.1 Consulting

Computational Research Specialists provide consulting during office hours and scheduled appointments, working with undergraduates, graduate students, post-doctoral researchers, faculty and research staff members who need assistance with computational research projects.

iCER supports research in all disciplines, with recent projects including analyzing video data [11] and Parallel Time Integrators [12]. iCER Research Specialists work with the disciplinary researchers to evaluate the computational requirements of the research project and develop solutions for research problems. Depending on the nature of the research and the computational requirements, the iCER Research Specialist will provide or develop a solution for known computational issues, or may develop a research project focusing on a novel computational challenge related to the disciplinary research. ICER Research specialists also participate in the XSEDE campus champions program and strive to become knowledgeable of external resources such as XSEDE and Open Science Grid to help researcher scale their research beyond local resource capabilities.

## 5.2 Testing, Troubleshooting and Benchmarking

iCER Research Specialists frequently help disciplinary researchers evaluate available software and hardware and select appropriate solutions. iCER Research Specialists install software on HPC systems (including XSEDE), write tests scripts, troubleshoot system problems and run benchmarks to evaluate the performance of algorithms on different hardware configurations. Research Specialists also assist HPC system administrators in monitoring the performance and stability of the local HPCC systems.

## 5.3 Debugging and Programming

iCER's Computational Research Specialists frequently assist disciplinary researchers with debugging problems that arise when trying to compile research-related programs on HPC resources or link to HPC-specific libraries. C, C++, Python, FORTRAN, MATLAB, bash, Perl, R, openmp, mpi, pthreads, and cuda are used most frequently, but iCER Research Specialists work with other languages and programming environments as well. On occasion, Research Specialists are asked to help disciplinary researchers write code that will optimize or parallelize an existing application.

## 5.4 Training, Mentoring and Education

iCER Research Specialists teach in a variety of formats, including developing instructional and reference materials for in-person and online delivery. Examples of recent educational topics include algorithm parallelization techniques, training on how to use specific software and hardware resources, and methods for software carpentry. Training formats include one-on-one tutorials, video tutorials, seminars and for-credit classes.

## 5.5 Grant Writing

iCER Research Specialists help educate disciplinary researchers about computational resources and provide feedback on appropriate resources to include in various funding proposals. iCER Research Specialists also participate in grant development as investigators, co-investigators, senior personnel or consultants, as appropriate to the project.

## 5.6 Outreach and Assessment

iCER Research Specialists participate in campus and community outreach activities, including developing and providing educational programs related to computational science and iCER resources. In addition, Research Specialists develop communications materials (research highlights, iCER website, news articles, etc.) and tools for evaluating and improving iCER research and outreach activities.

## 5.7 User Training, Education and Support

Part of iCER's mission is to provide opportunities for user training and research education, and to support ongoing research collaborations. Computational Research Specialists at iCER provide extended, one-on-one support to disciplinary researchers. Frequently, this assistance includes identifying appropriate computational tools and resources; improving efficiency (parallelizing experiments, porting code to HPC systems, etc.);

and identifying and training students and programmers who can provide long-term assistance for computational projects.

iCER provides a number of educational seminars, classes and programs for researchers, faculty, and students. Topics from recent iCER presentations and seminars include:

- Software Carpentry [13]
- MATLAB
- Bioinformatics
- MPI Programming
- OpenMP
- GPGPU Programming

In addition to providing regular computational research training for members of the MSU community, iCER promotes collaborative research by hosting regional/national research seminars and workshops focused on computation. For example, in 2011 iCER hosted an ENZO developer workshop where 22 participants from UC San Diego, Michigan State University, and the National Center for Supercomputing Applications at the University of Illinois in Urbana-Champaign gathered to report current progress and discuss future code development. There were also discussions about grant funding among the PI-level attendees, which resulted in submitted NSF RFPs. Two of the visitors also gave seminars about ENZO to the broader MSU community.

In 2012, MSU hosted its first regional CI-Days. With more than 100 participants, this two-day event highlighted local, regional and national Cyber-Infrastructural resources including XSEDE. A second regional CI-Days event is being planned in 2013.

For the last four years, iCER has been a host site for the annual Summer Virtual School in Computing and Engineering (VSCSE) [14]. Hosted classes include: Big Data for Science (2010, 2013), Petascale Programming Environments and Tools (2011), Proven Algorithmic Techniques for Many-core Processors (2011, 2012, 2013) and Science Cloud Summer School (2012). VSCSE participants have come from MSU, the University of Michigan, and Lansing Community College.

iCER provides travel funding to assist collaborators from other locations in traveling to campus to work with MSU's computational researchers. iCER also sponsors an annual seminar series that brings computational researchers from across the country to MSU. In addition, iCER has jointly supported a number of post-doctoral scholars who are working on computational research projects in various disciplines on campus.

User support at both iCER and the HPCC are organized though an instance of Request Tracker [15] (the same system currently used by XSEDE). All staff members are responsible for ensuring that "tickets" are answered quickly and correctly. To ensure a quick turn around time, all technical staff members have regularly scheduled times when they are supposed to man the virtual "help desk". During a scheduled "help desk" time, the staff member on duty is responsible for responding to new user questions and reviewing existing tickets to ensure that issues have not fallen though the cracks. We have learned that the quantity of closed tickets is not a good measure of a successful helpdesk; instead, tickets that remain open are not viewed as a problem as long as the user's concerns are actively being addressed. We have found that leaving a ticket "open" allows researchers to get regular updates so they are aware of the progress being made.

## 6. Undergraduate Research and Internship Program

iCER is committed to training the next generation of computational scientists and system administrators, and has developed an internship program for undergraduates that employs an average of 10 students during the academic year, and 15 students during the summer months. These students work in a variety of areas, from hardware maintenance to computational research, and the iCER undergraduate intern program provides a unique experience for students to learn practical skills and gain responsibility and experience over time. iCER student interns assist MSU research scientists with computational projects, gain valuable system administration experience, and learn to install, evaluate and teach others about various high performance software. A sample of recent student projects include:

- Bigbrother, a user-level job monitoring tool that works directly with PBS
- Evaluating and deploying CONDOR for high throughput computing
- Visualization of MSU's machine room status [16]
- WIKI installation and migration
- Developing and improving web interfaces to support user interactions with iCER staff
- Creating a YUM repo server
- Node maintenance (hardware trouble shooting, replacement, burn in tests, etc.)
- Installing, testing and documenting Makeflow, a workflow engine developed at Notre Dame for executing large workflows on clusters
- Installing, testing, and documenting parallel profilers such as Tau and Scalasca [17]
- Installing, testing and documenting visualization software for Supernova Simulations using paraview and OSMesa [18]
- Assisting in parallelizing serial simulation code using OpenMP and HDF5
- Installing, testing and documenting the use of the eclipse parallel toolkit
- Integration and development of a series of tools (called Powertools) developed by iCER staff and users [19]

iCER staff also developed an undergraduate honors research seminar for first- and second-year students at MSU. The goal of this 3-credit course is to introduce novice students (with little or no prior research experience) to both the process and content of research in computational science. Students meet weekly throughout the academic year to learn about various topics in research and computation, and develop individual research projects to present at the University's undergraduate research forum. In 2010, six undergraduate from various disciplines (Astrophysics, Mathematics, Education, Electrical Engineering, Packaging, Supply Chain Management) participated in the honors research seminar, which examined ways to improve the efficiency and environmental sustainability of the HPC server room [20]. This honors research seminar was offered again for the 2012-2013 school year, with a focus on image processing and computational research [21].

## 7. Community Outreach

Part of iCER's role is to expand public understanding of the importance of computation in the advancement of science. In addition to seminars and training sessions for the MSU

community, Computational Research Specialists from iCER regularly conduct outreach activities within the broader community. For example, in spring 2012 iCER employees visited several computer science classes at Anchor Bay High School in Ira, Michigan (a northern suburb of Detroit). The students were given a brief presentation about high performance computing and the role of computation in scientific research, and then they had the opportunity to take apart several compute nodes from a retired HPC cluster. This hands-on activity allowed students see the "insides" of a computer and learn how they functioned – one student group was particularly thrilled when they were able to successfully re-assemble and power up the node they had taken apart.

Researchers associated with iCER have also developed a number of courses for the MSU Evening College, which offers non-credit personal development classes to community members [22]. Recent Evening College offerings include a course outlining advances in computational science (and a tour of the HPC server room), and a "Coffee with the Profs" event where community members have the opportunity to meet senior faculty in a conversational setting.

## 8. Regional Partnerships

In 2010, MSU partnered with Central Michigan University (CMU) to expand the HPC capacity of iCER and make high performance computing more accessible to researchers at both Universities. Researchers at MSU and CMU use iCER resources for diverse projects, including climate modeling [23]; Galaxy Formation [24]; genetic programming [25]; and numerical simulations that help explain experimental observations at MSU's Facility for Rare Isotope Beams [26].

As a founding member of the Great Lakes Consortium for Petascale Computing (GLCPC) [27], MSU hosts some of the Consortium's Summer School classes to support researchers interested in working with petascale computation. iCER research faculty have also participated extensively in the Blue Waters Undergraduate Petascale Education Program, helping mentor 3 undergraduate students in the last two years.

## 9. Research Impacts

Since iCER was founded in 2009, almost 10 million jobs have run on MSU HPC resources, utilizing more than 4,900 cpu-years. In addition, iCER's Computational Research Specialists have completed more than 400 individual consulting appointments; resolved more than 7,400 user support tickets; and assisted in training and placing 40 students and programmers with research projects across campus. With support from iCER staff and computational resources, at least 227 documented scientific papers have been published. (An updated list of many iCER-related papers, projects and grants is available online at: http://icer.msu.edu/ [28].)

External grant support is the gold standard by which all universities measure their research quality, and one explicit goal in establishing iCER at MSU was to increase our computational researchers' competitiveness for external grants. To this end, iCER provides 50% matching towards a postdoc for large external multidisciplinary or multi-investigator grants. While it is, of course, hard to quantify the impact of any single component in the overall success of a grant proposal, it is still clear that external grant support for computational sciences has sharply increased at MSU since the founding of iCER.

Several large group grants that included iCER 50/50 postdoctoral position matches have been funded. Among these is the NSF Cyber-Enabled Discovery and Innovation (CDI) grant "Modeling and Data Analysis Initiative" collaboration (MADAI) [29]. Perhaps our most important success is the NSF Science and Technology Center (STC) BEACON, which studies evolution in action through computer simulation [30]. BEACON and iCER share office space in a dedicated facility, which enables close integration and a high degree of leveraging of MSU resources. Senior personnel in iCER, including the present authors, are charged with initiation and/or supporting these and other grant opportunities as part of iCER's mission.

Another extremely important contribution of iCER to the success of the externally funded research programs at MSU is the "HPCC buy-in" program. This program enables grant-supported researchers to utilize 100% of their available hardware funding for purchasing computing hardware, without the need to budget for associated overhead expenses (hardware and software maintenance, power, cooling, and so on). Once a researcher commits funds for hardware purchases, these nodes are integrated in the general pool of resources of the HPCC. These dedicated resources are available to the purchaser at any time through priority queue access, but any surplus capacity is made available to all users. This tiered access model is made possible, in part, through a four hour guaranteed access window and our use of the Berkley Lab Checkpoint Restart (BLCR), which enables lower priority jobs to move to alternative resources when buy-in users need access to their purchased nodes [31]. Obviously, this model greatly leverages external research funds, but it also benefits the wider community of iCER's HPC users.

## 10. Discussion and Lessons Learned

The Institute for Cyber-Enabled Research at MSU was established in order to provide broad support for computational research. Rather than just providing advanced HPC hardware, the goal of iCER is to maximize the quantity and quality of computational research on campus, and assist the MSU community in leveraging regional and national resources to support large-scale research and discovery.

iCER has enjoyed many successes in its first four years, including a dramatic increase in total users and cpu-years; extensive education and outreach programs; and research collaborations on campus, throughout the Midwest, and across the nation. Users of the HPCC are no longer just members of the big three (Engineering, Physics and Chemistry): since iCER was introduced, research across campus from Agriculture Economics to Zoology have made use of the HPCC. Key elements that contributed to the success of iCER include:

- Investing in HPC hardware, computational software and system administration personnel
- Investing in Computational Research Specialists to provide user support and education
- Developing extensive education and training programs for computational researchers in various disciplines
- Reducing barriers to entry for novice users of HPC systems
- Training undergraduates and graduate students in advanced computation, and matching skilled student programmers with disciplinary research projects
- Providing cost-sharing to support programmers and post-doctoral researchers who serve as liaisons between iCER and disciplinary research projects

- Partnering locally, regionally and nationally to provide computational resources and training
- Developing outreach activities about computational research to educate and engage the public, including high school students and early undergraduates

iCER has also encountered some challenges in its first four years. For instance, computational research comes in many varieties and it can be difficult to develop training and support programs that meet the wide range of user needs. It can also be difficult to identify and hire individuals with appropriate skills for a position like "Computational Research Specialist." The job description for this position at iCER calls for someone with an earned Ph.D. in a computational area and experience with interdisciplinary research. iCER's Computational Research Specialists participate in teaching, research, and grant-writing, but are neither tenure-track faculty nor traditional post-doctoral scholars.

As the practice of computational research evolves, there is an emerging need for experts in computational research who have the ability to communicate and collaborate in multidisciplinary settings. Training programs like the Campus Champions [28] and new positions like the Computational Research Specialists at iCER are an early effort to meet the growing need for computational research support, but the field is still emerging and the career pathways are not yet clear.

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