

Figure 1a series circuit

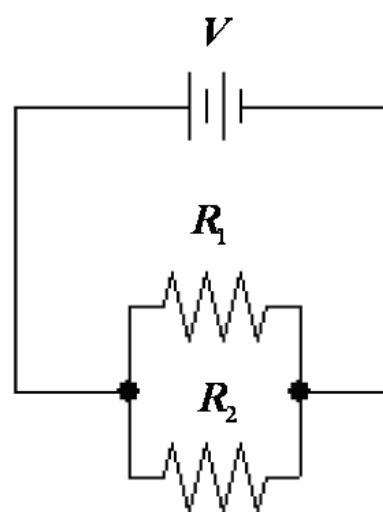


Figure 1b parallel circuit