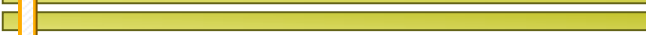
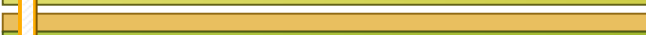
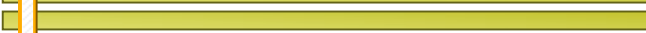


Layer	Stack up	Type	Supplier Description	Base Thickness	Processed Thickness	Isolation Distance (Summed)	Resin Content	ϵ_r
1		SolderMask			0.800			4.000
2		Copper		0.700	1.800			
3		Alternate	FR408HR 2113	4.094	3.944	3.944	57.500	3.590
4		Standard	FR408HR 1 x 106 / 1 x 1080	0.600	0.600	4.000	59.000	3.580
5		Alternate	FR408HR 2113	4.094	3.869	15.478	57.500	3.590
6		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
7		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
8		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
9		Standard	FR408HR 1 x 106 / 1 x 1080	0.600	0.600	4.000	59.000	3.580
10		Standard	FR408HR 1080	3.189	2.814	5.628	65.000	3.420
11		Standard	FR408HR 1080	3.189	2.814	-	65.000	3.420
12		Standard	FR408HR 2 x 3313	1.200	1.200	8.000	55.000	3.680
13		Standard	FR408HR 1080	3.189	2.814	5.628	65.000	3.420
14		Standard	FR408HR 1080	3.189	2.814	-	65.000	3.420
15		Standard	FR408HR 1 x 106 / 1 x 1080	0.600	0.600	4.000	59.000	3.580
16		Alternate	FR408HR 2113	4.094	3.869	15.478	57.500	3.590
17		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
18		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
19		Alternate	FR408HR 2113	4.094	3.869	-	57.500	3.590
20		Standard	FR408HR 1 x 106 / 1 x 1080	0.600	0.600	4.000	59.000	3.580
21		Alternate	FR408HR 2113	4.094	3.944	3.944	57.500	3.590
22		Copper		0.700	1.800			
23		SolderMask			0.800			4.000

Notes

Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Calculated Impedance	Target Impedance	Tol (+/- %)	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	CI Notes-1
	1	Edge Coupled Coated Microstrip 1B	1	5.512	4.512	10.236	0.000	103.520	100.000	10.000	2	0	
	2	Edge Coupled Offset Stripline 1B2A	3	5.512	4.512	10.236	0.000	100.450	100.000	10.000	2	5	

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Date: 2/6/2025	Associated Documents:						
Author: John Mildebrandt							
Department: Engineering							
Site: Alpha Circuit I LLC							



Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Lower Trace Width (W1)	Upper Trace Width (W2)	Trace Separation (S1)	Ground Strip Separation (D1)	Calculated Impedance	Target Impedance	Tol (+/- %)	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	CI Notes-1	
	3	Edge Coupled Offset Stripline 1B2A	4	5.512	4.512	10.236	0.000	100.450	100.000	10.000	2	5		
	4	Edge Coupled Offset Stripline 1B2A	9	5.512	4.512	10.236	0.000	100.450	100.000	10.000	8	11		
	5	Edge Coupled Offset Stripline 1B2A	10	5.512	4.512	10.236	0.000	100.450	100.000	10.000	8	11		
	6	Edge Coupled Coated Microstrip 1B	12	5.512	4.512	10.236	0.000	103.520	100.000	10.000	11	0		

Drill Image	1st Layer	2nd Layer	Column Position	Drill Type	Must-Not-Cut Layer No	Minimum Distance From Not-Cut Layer	Calculated Drill Depth - Back Drill Must-Cut	Drill Notes-1	
	1	12	1	Mechanical PTH	-	0.000	0.000		

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