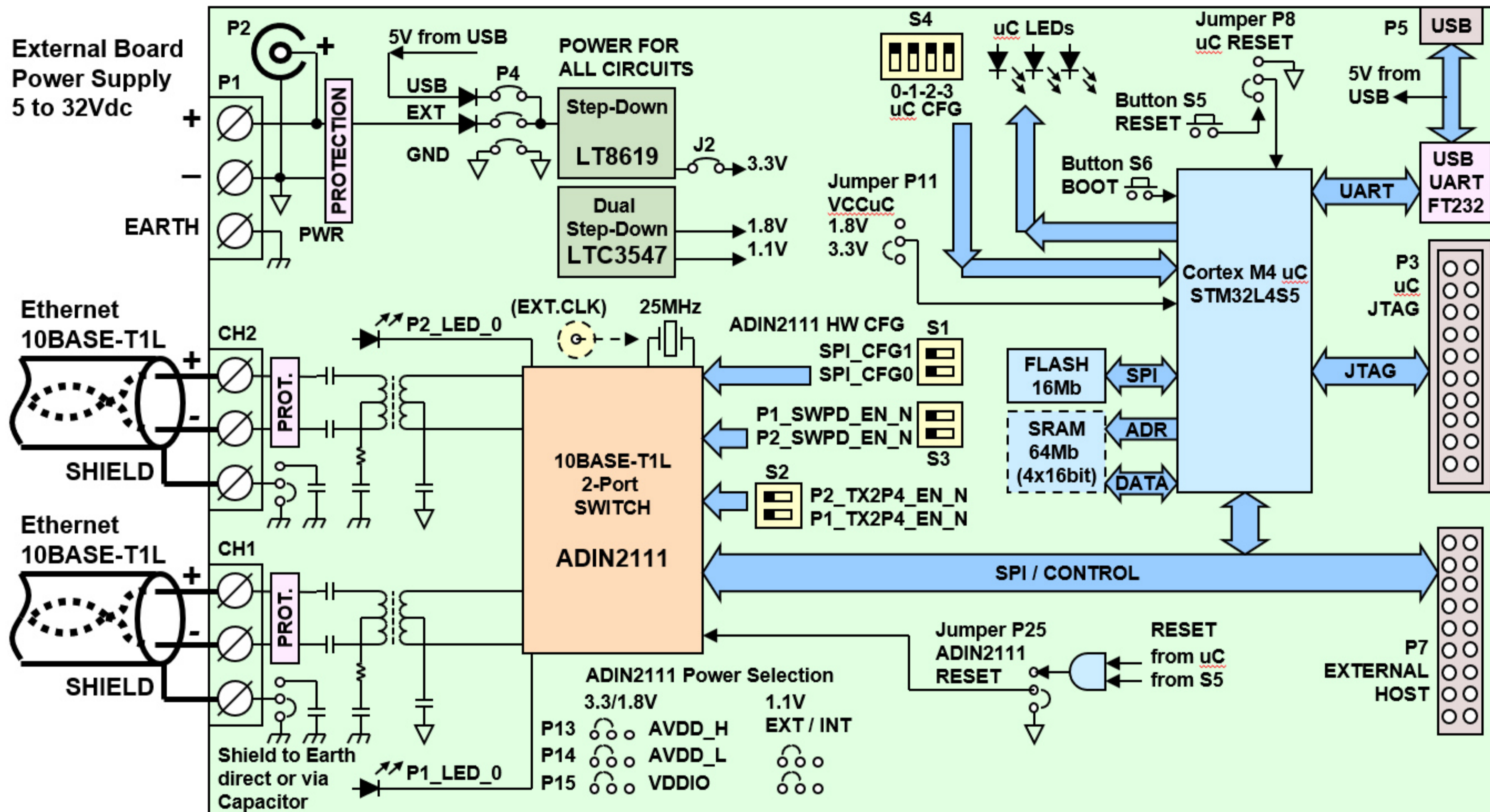


EVAL-ADIN2111EBZ

10BASE-T1L 2-PORT SWITCH APPLICATION / EVALUATION BOARD

BLOCK DIAGRAM:

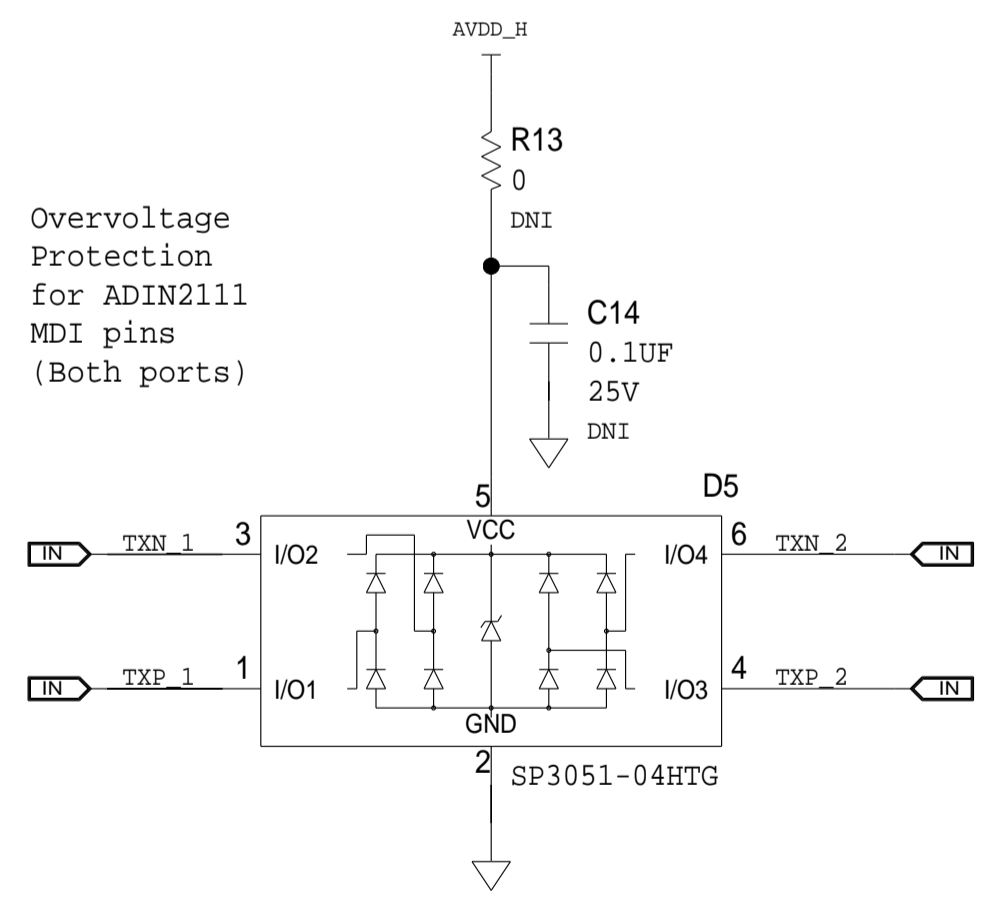
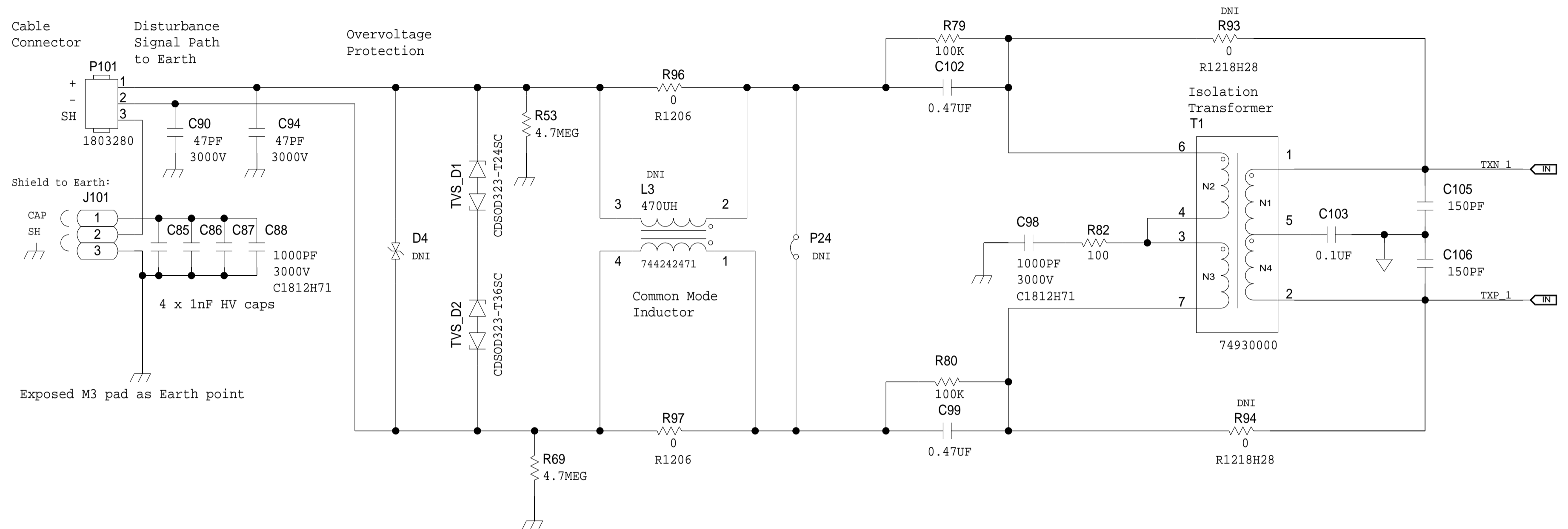
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ANALOG DEVICES		SCHEMATIC		
EVAL-ADIN2111EBZ Product(s): ADIN2111		DRAWING NO. 02_063822		REV C
DESIGN VIEW M.Ramos/W.Magnin/M.Brychta		SIZE C	SCALE 1:1	SHEET 1 OF 8
PTD ENGINEER M Madden				

PORT 1 CABLE CONNECTION

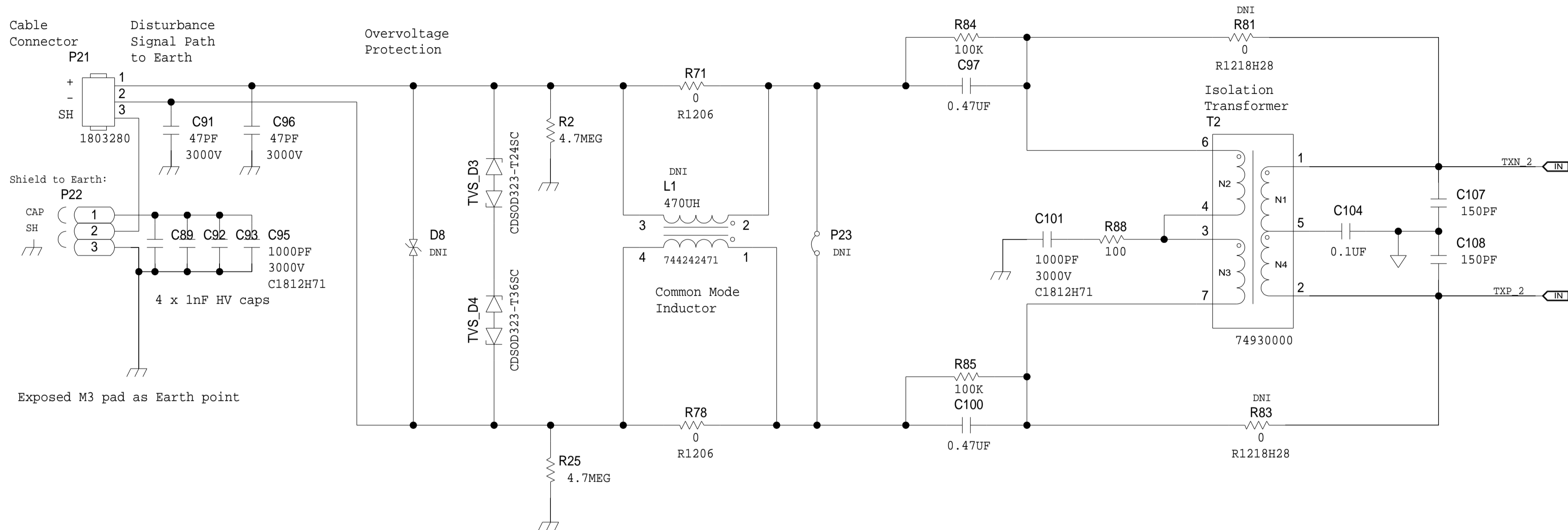
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



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	EVAL-ADIN2111EBZ Product(s): ADIN2111		
	DESIGN VIEW M.Ramos/W.Magnin/M.Brychta	DRAWING NO. 02_063822	REV C
PTD ENGINEER M Madden	SIZE C	SCALE 1:1	SHEET 2 OF 8

PORT 2 CABLE CONNECTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

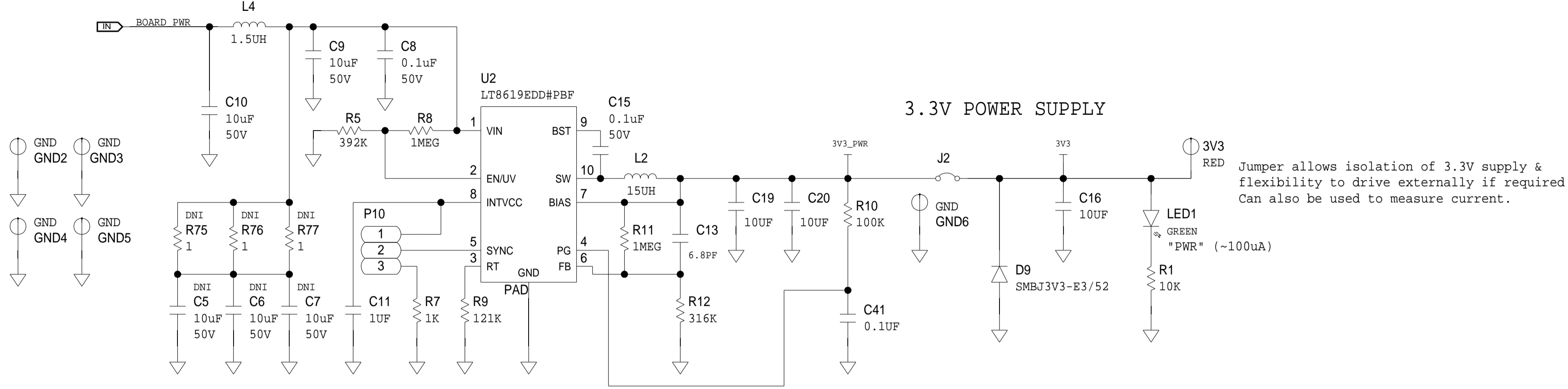


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	EVAL-ADIN2111EBZ Product(s): ADIN2111		
	DESIGN VIEW M.Ramos/W.Magnin/M.Brychta	DRAWING NO. 02_063822	REV C
PTD ENGINEER M Madden	SIZE C	SCALE 1:1	SHEET 3 OF 8

BOARD POWER SUPPLY

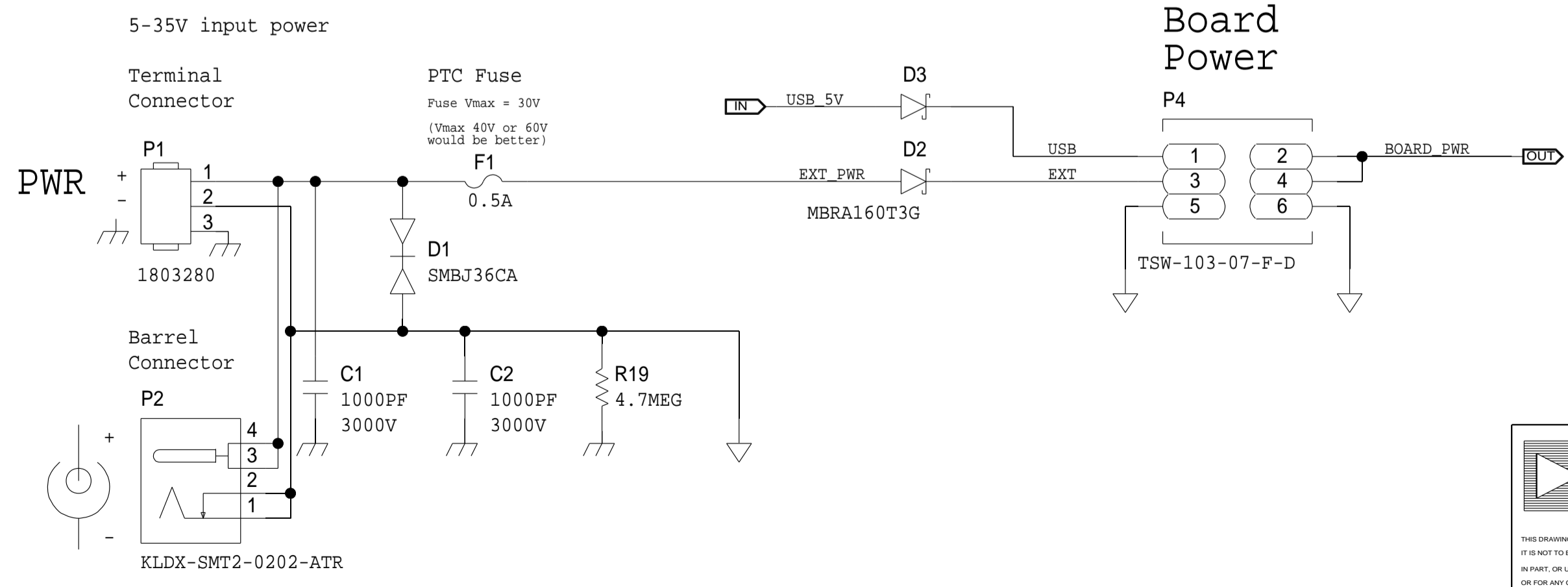
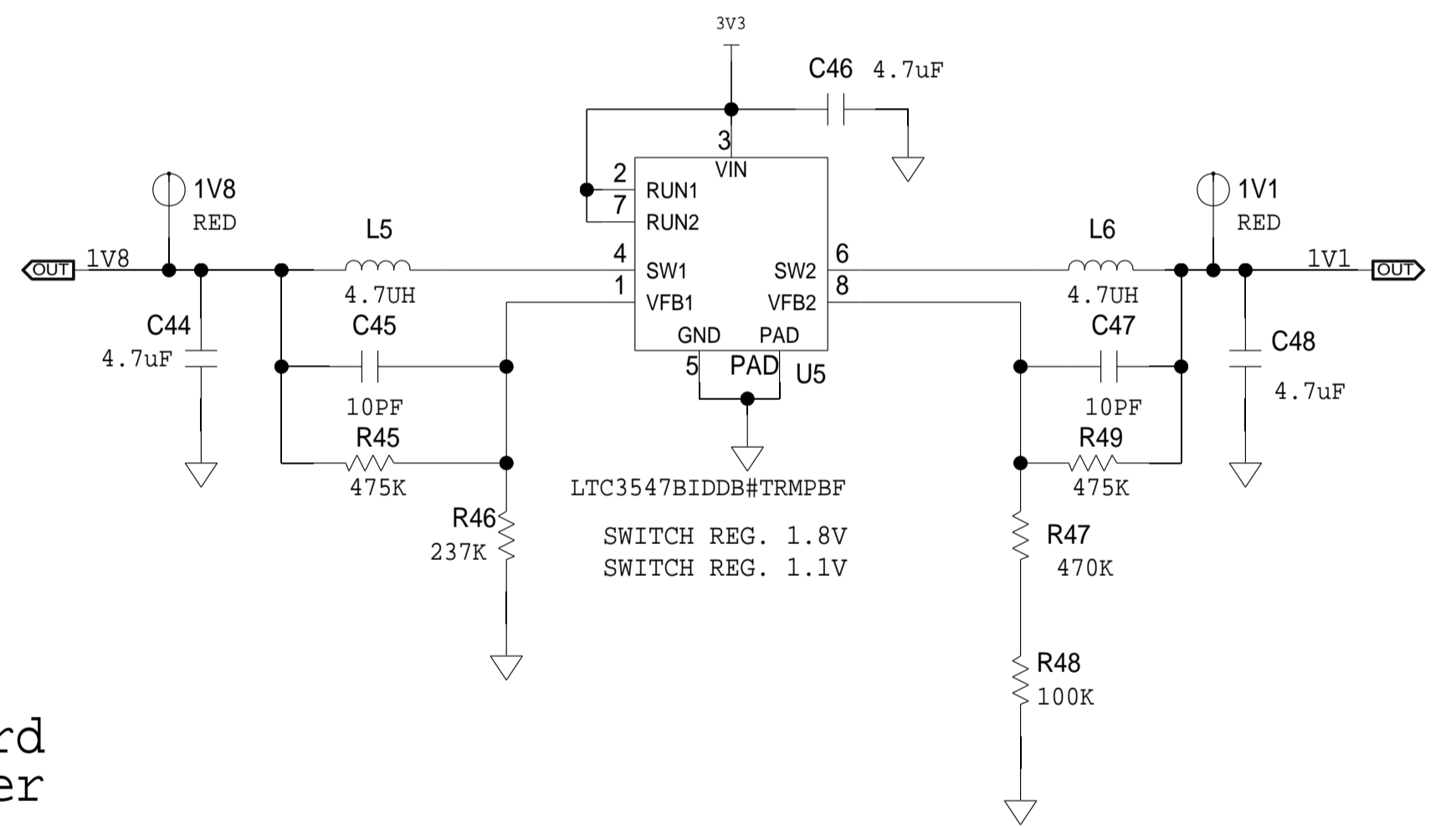
4V TO 60V INPUT
(Limited to 5V min by F1+D2 and max 35V by D1 on this board)

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



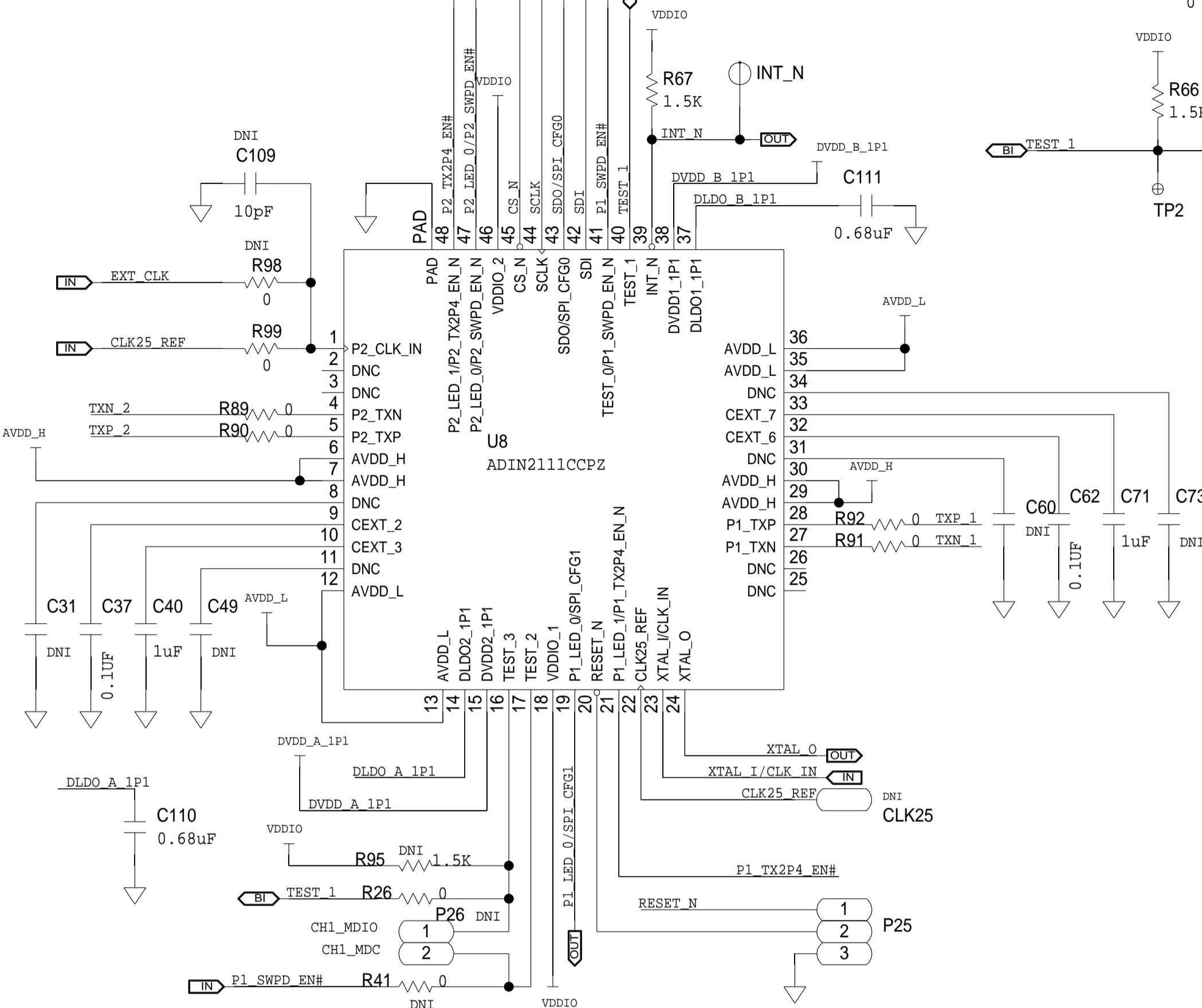
Jumper allows isolation of 3.3V supply & flexibility to drive externally if required. Can also be used to measure current.

Jumper P10:
 Inserted 1-2 (FORCED): Forced continuous mode
 Inserted 2-3 (BURST) : Burst mode (default on this board)
 Not inserted (PULSE SKIP): Pulse skipping mode
 External clock synchronisation possible to P10 pin 2

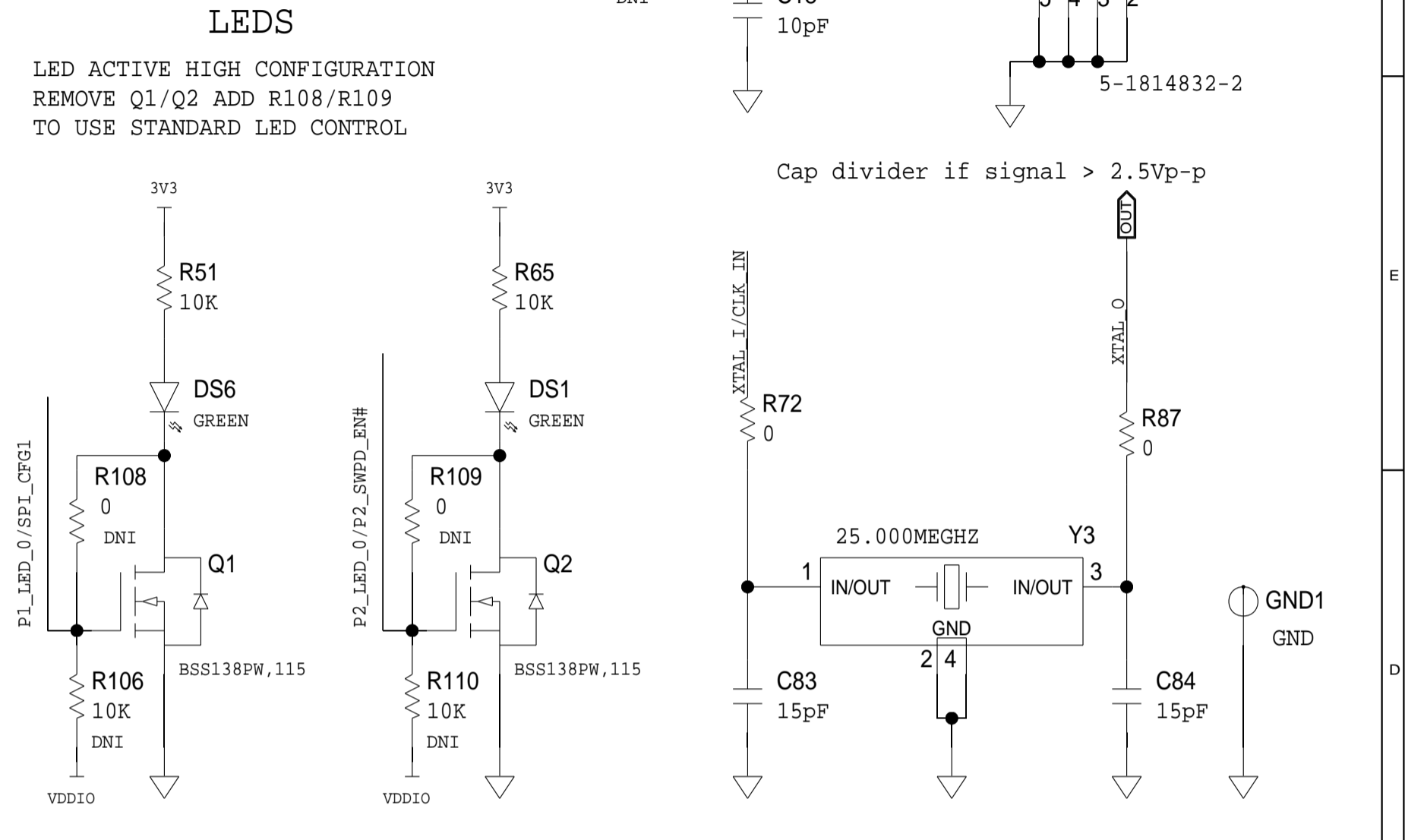
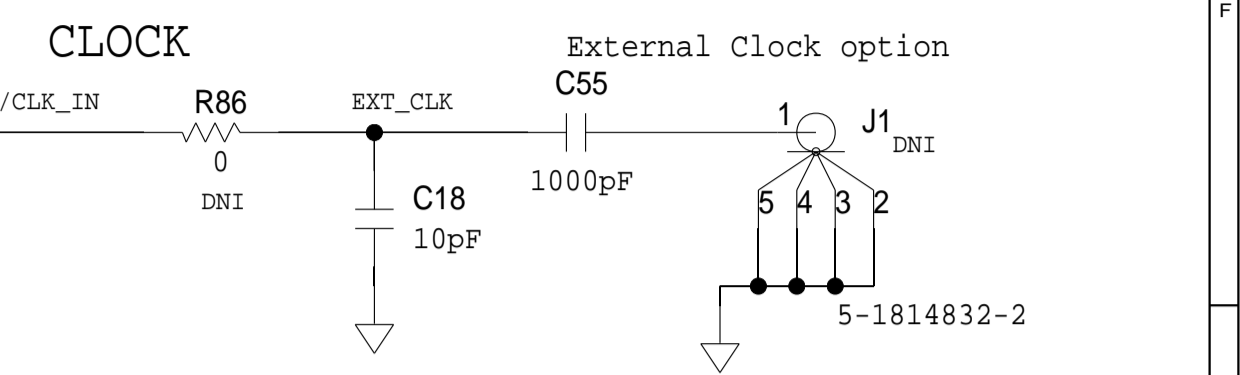


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	EVAL-ADIN2111EBZ Product(s): ADIN2111		
	DESIGN VIEW M. Ramos/W. Magnin/M. Brychta	DRAWING NO. 02_063822	REV C
PTD ENGINEER M. Madden	SIZE C	SCALE 1:1	SHEET 4 OF 8

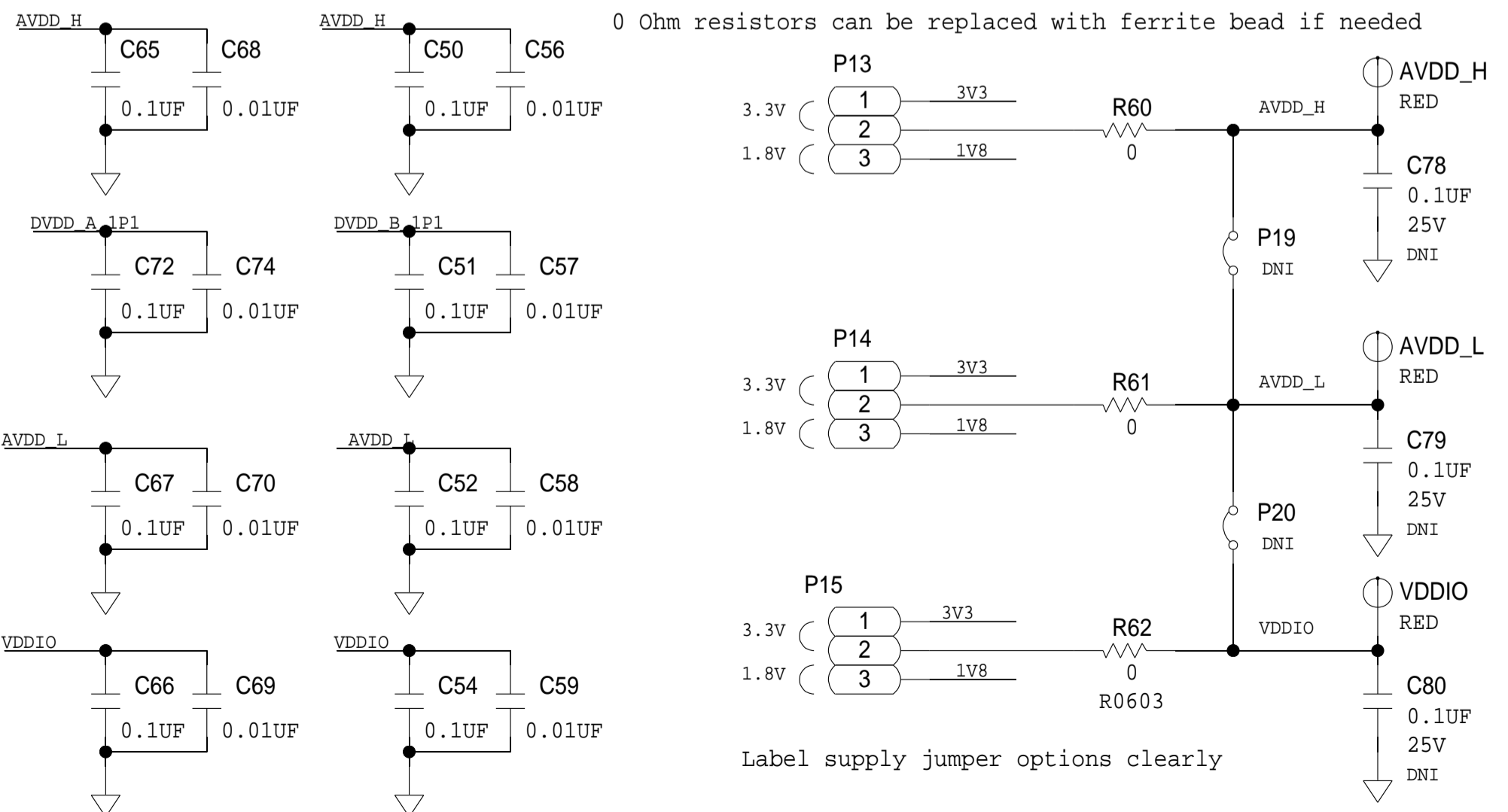
ADIN2111



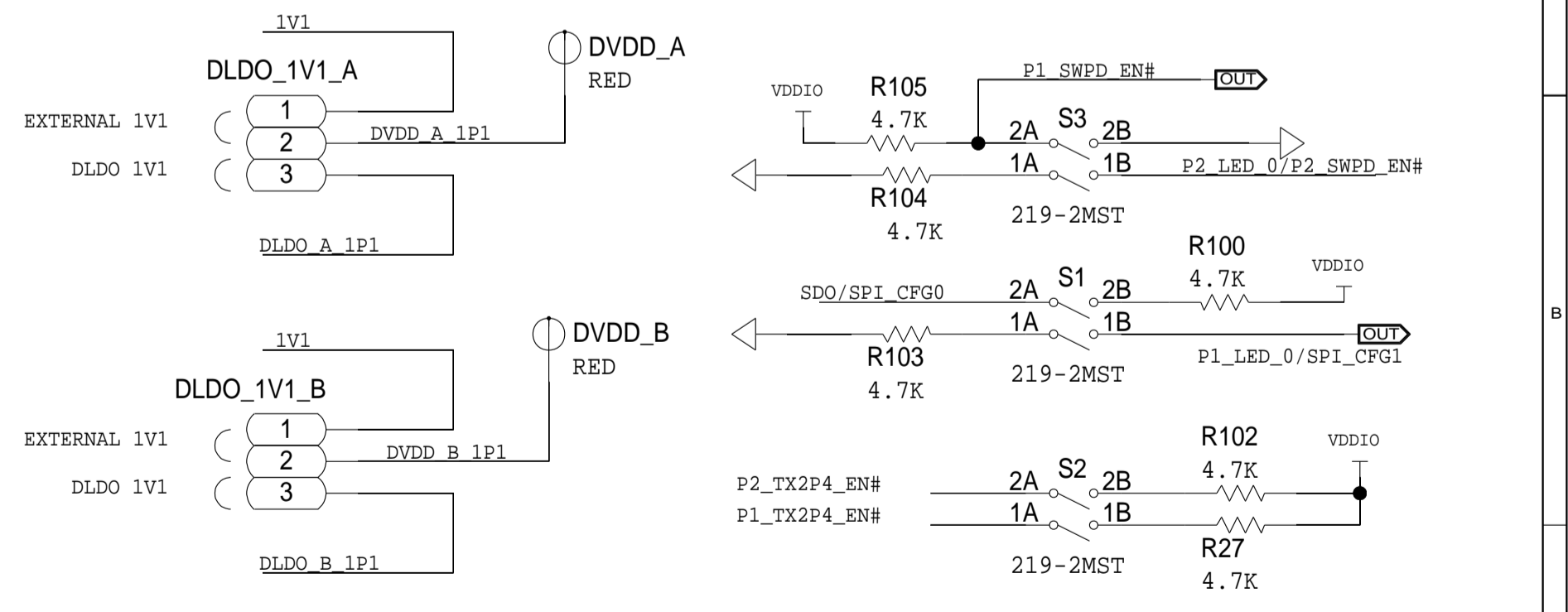
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ADIN2111 SUPPLY PIN SELECTION & DECOUPLING



H/W PINS CONFIG



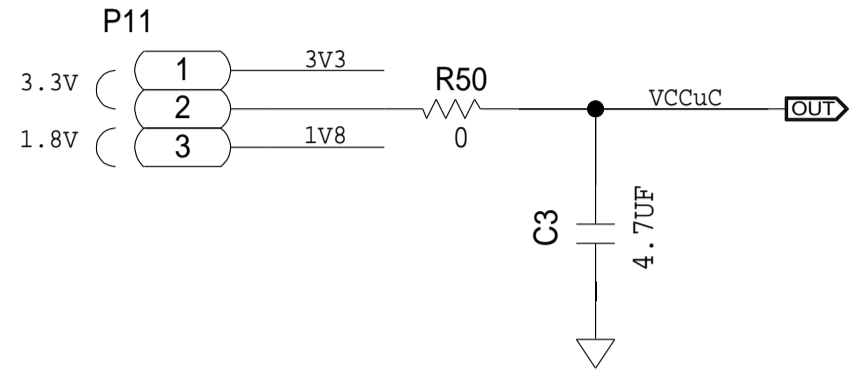
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SCHEMATIC			
EVAL-ADIN2111EBZ Product(s): ADIN2111			
DESIGN VIEW M. Ramos/W. Magnin/M. Brychta	DRAWING NO. 02_063822	REV C	
PTD ENGINEER M. Madden	SIZE C	SCALE 1:1	SHEET 5 OF 8

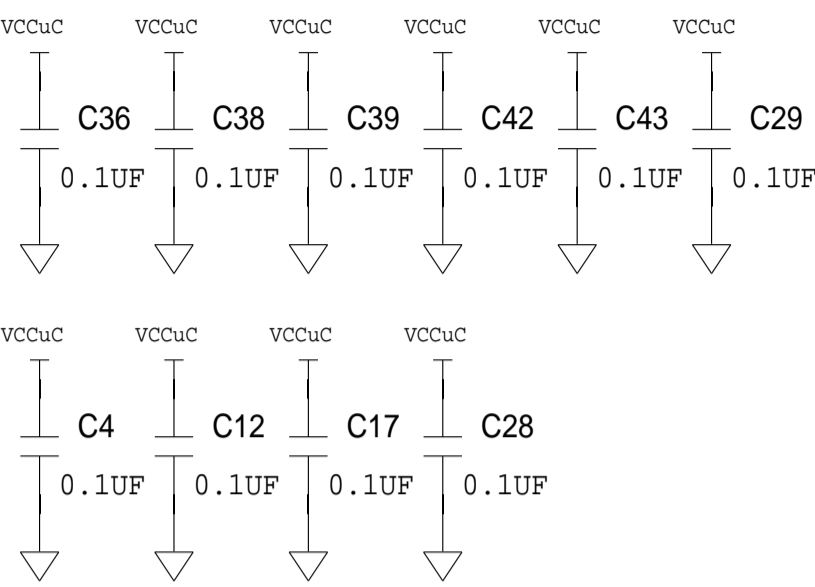
Cortex M4 uC

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

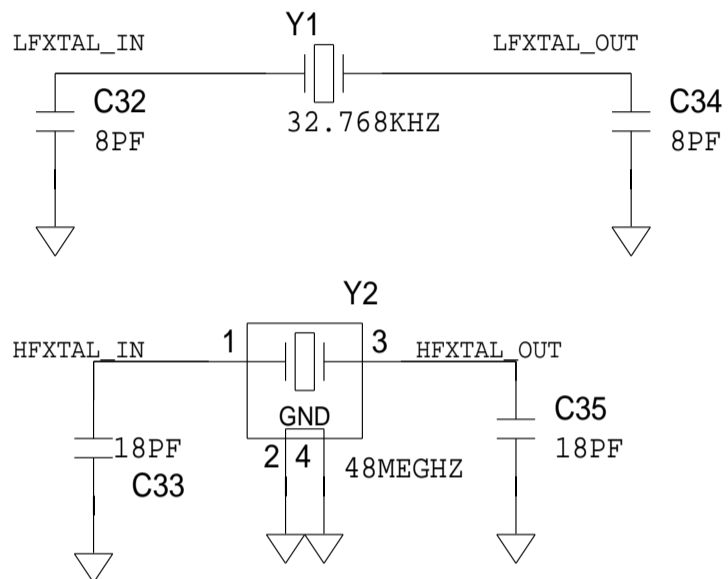
uC Power Supply



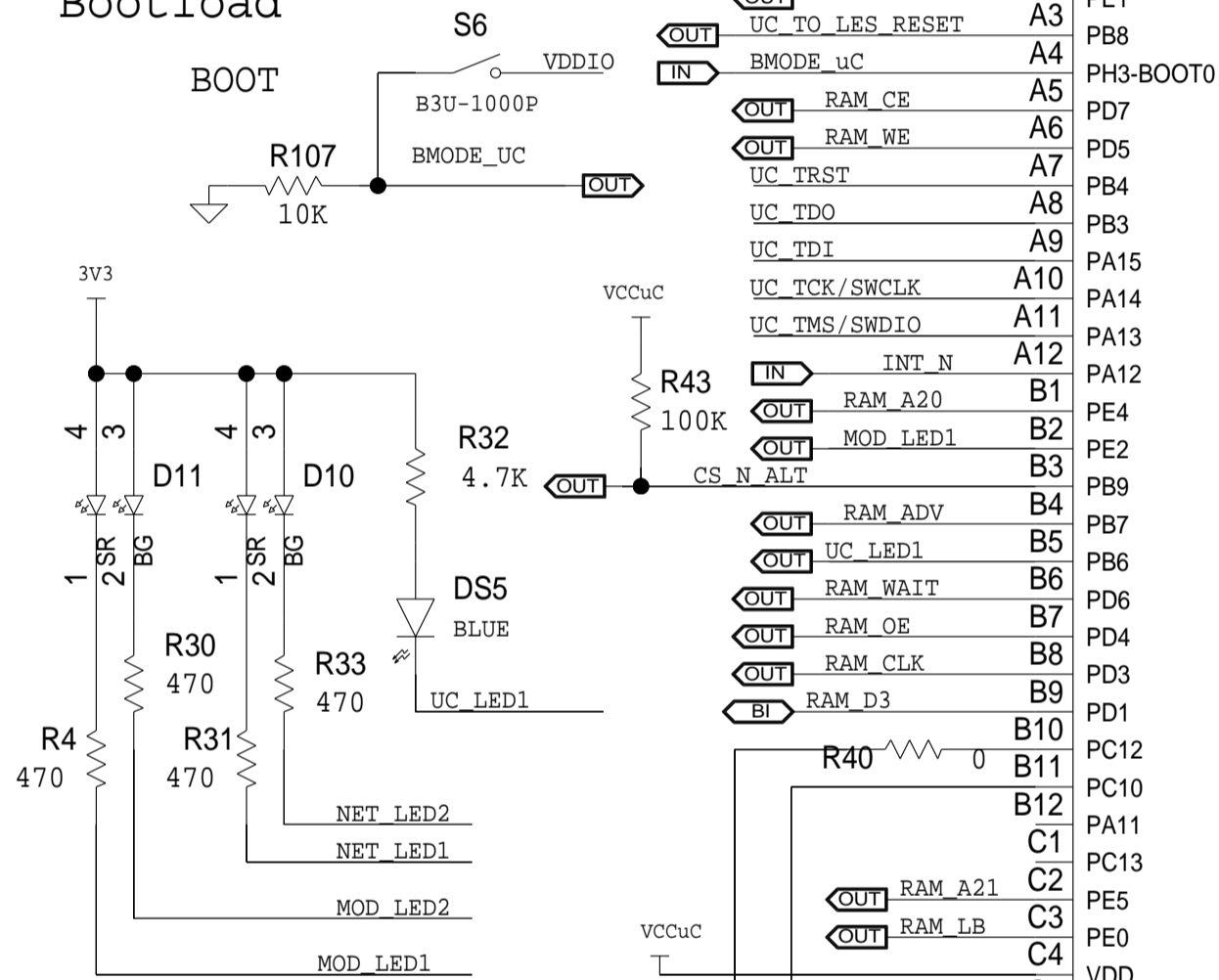
uC Decoupling Caps



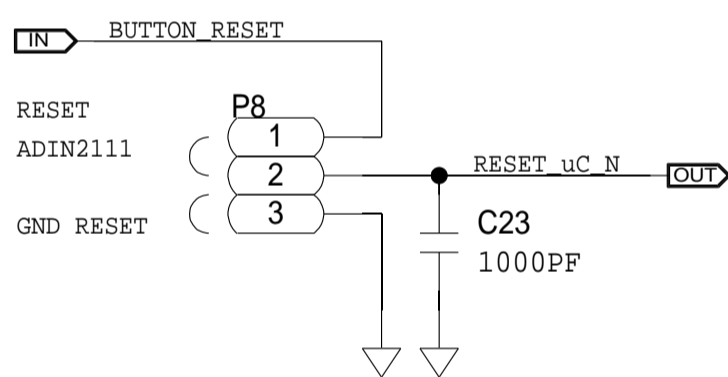
uC Clocks



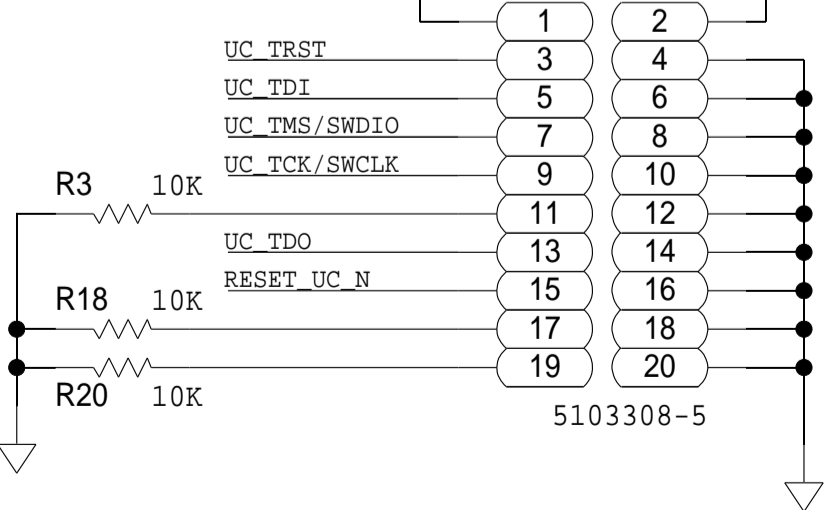
uC (UART) Bootload



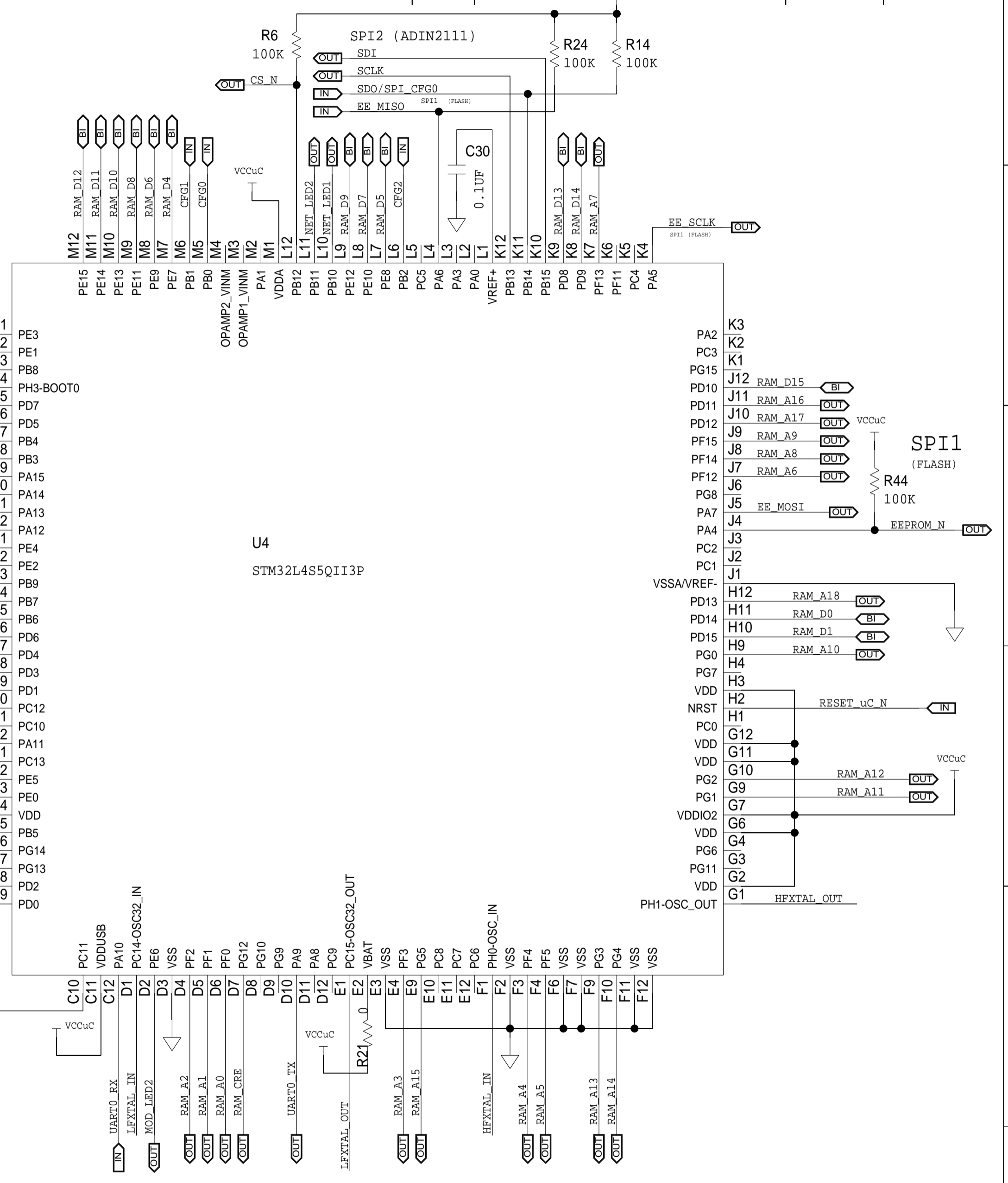
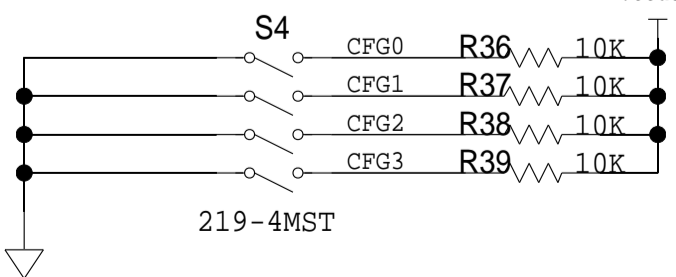
uC RESET



JTAG



Spare Config pins



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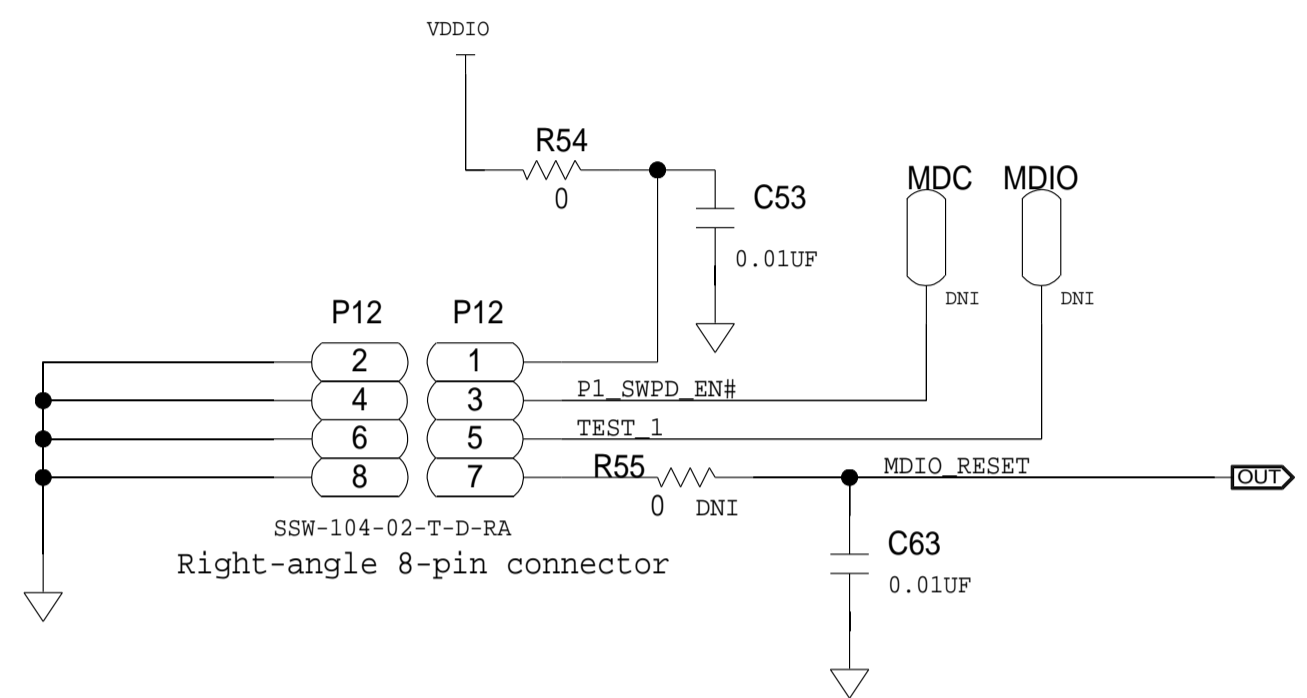
SCHMATIC

EVAL-ADIN2111EBZ
Product(s): ADIN2111

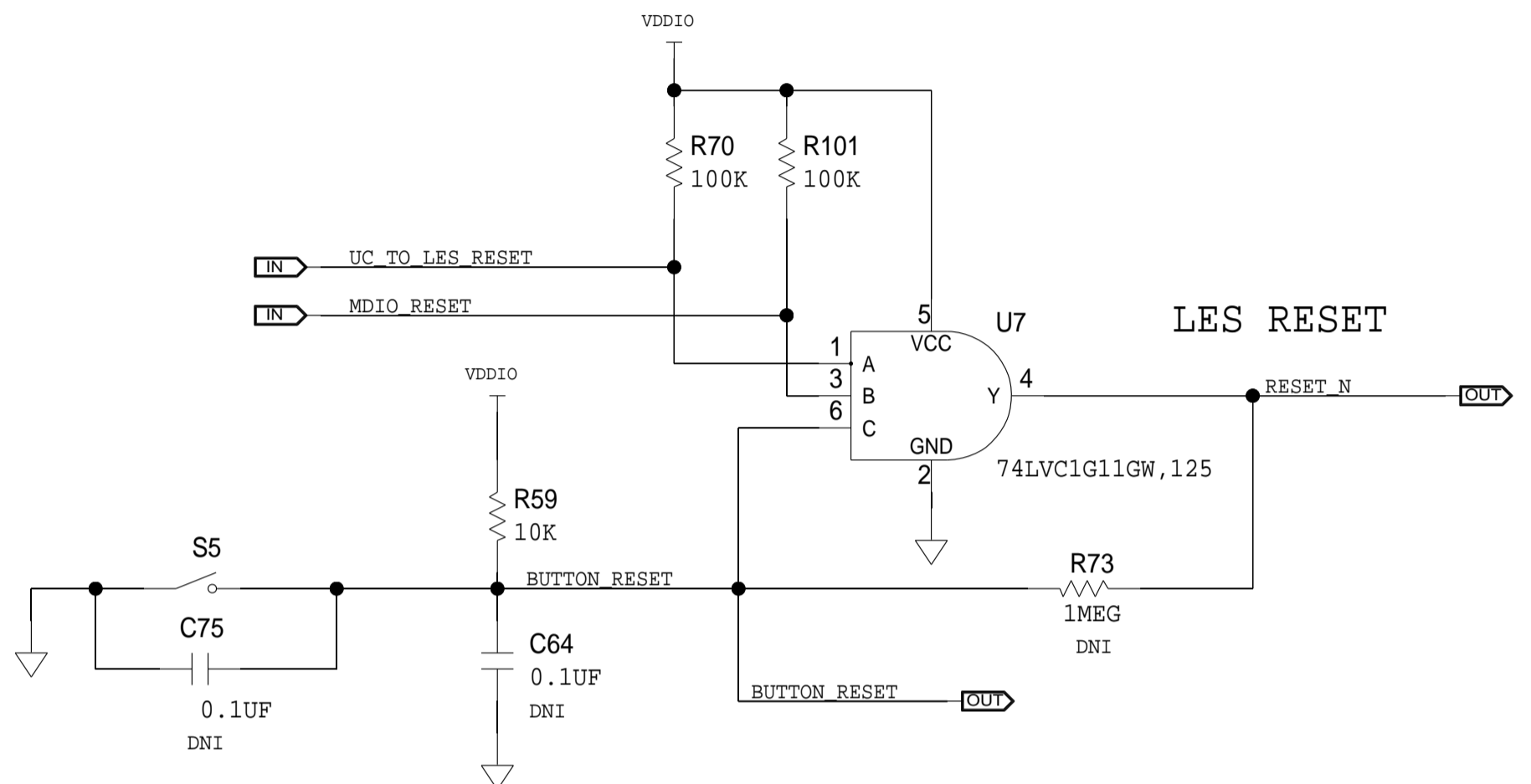
DESIGN VIEW	DRAWING NO.	REV
M.Ramos/W.Magnin/M.Brychta	02_063822	C
PTD ENGINEER M.Madden	SIZE C	SCALE 1:1
	SHEET 6	OF 8

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

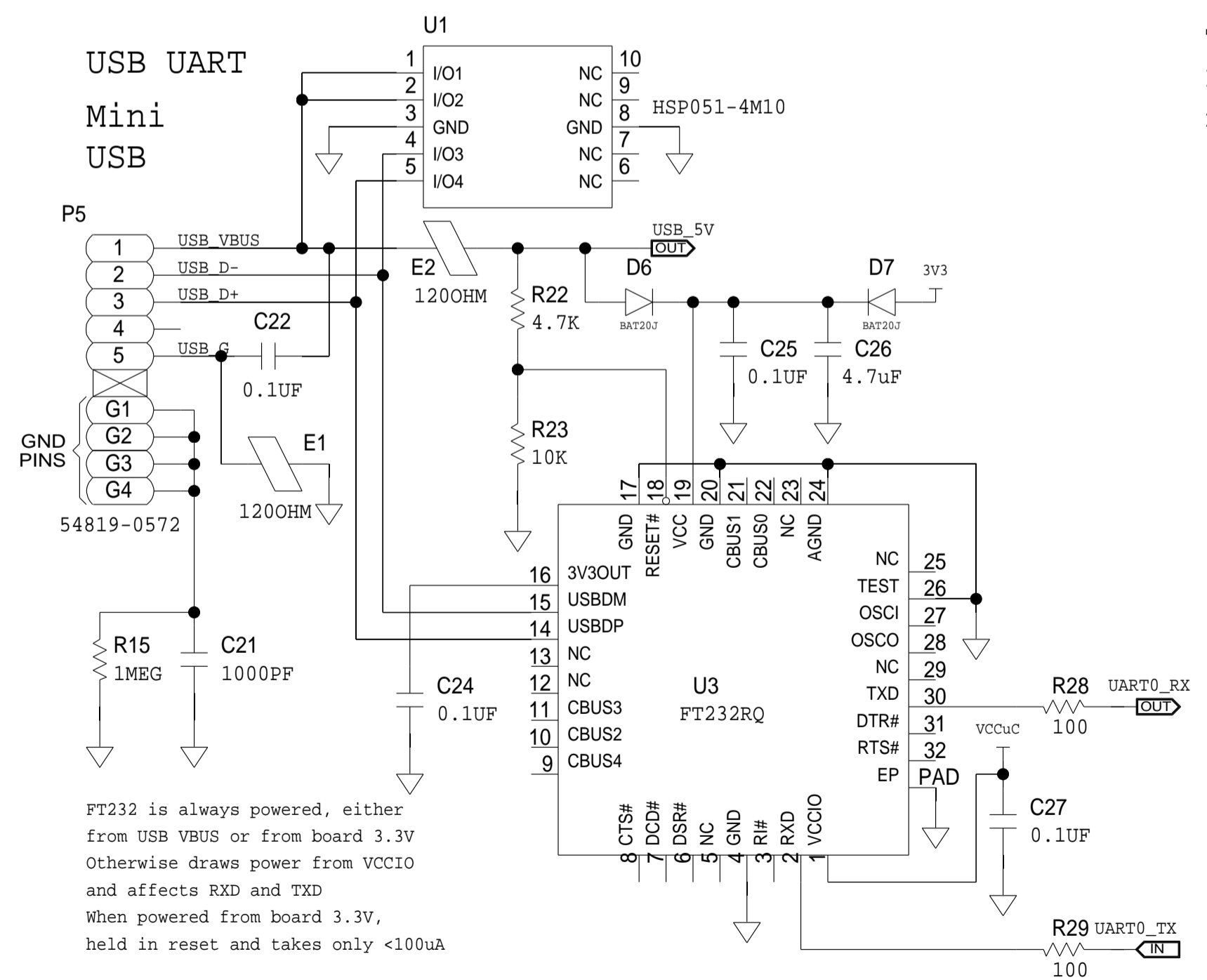
ADI test connector



BOARD RESET



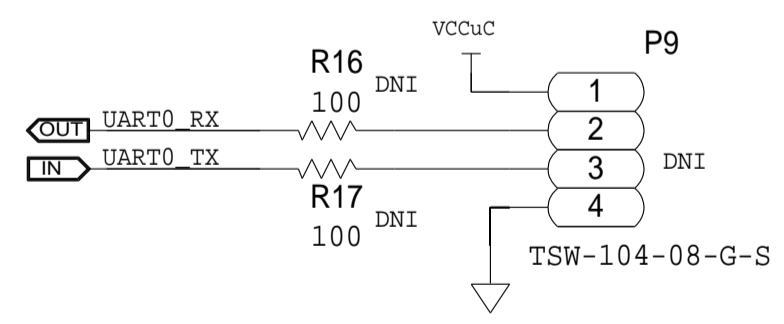
(Protection for USB)



FT232 is always powered, either from USB VBUS or from board 3.3V. Otherwise draws power from VCCIO and affects RXD and TXD. When powered from board 3.3V, held in reset and takes only <100uA.

The ADIN2111 can be reset by either the uC or the push-button switch. Provision is made on the uC schematic page (sheet 5) such that the uC can be reset via the push-button switch, or optionally held in reset (using P8).

ADI DEBUG



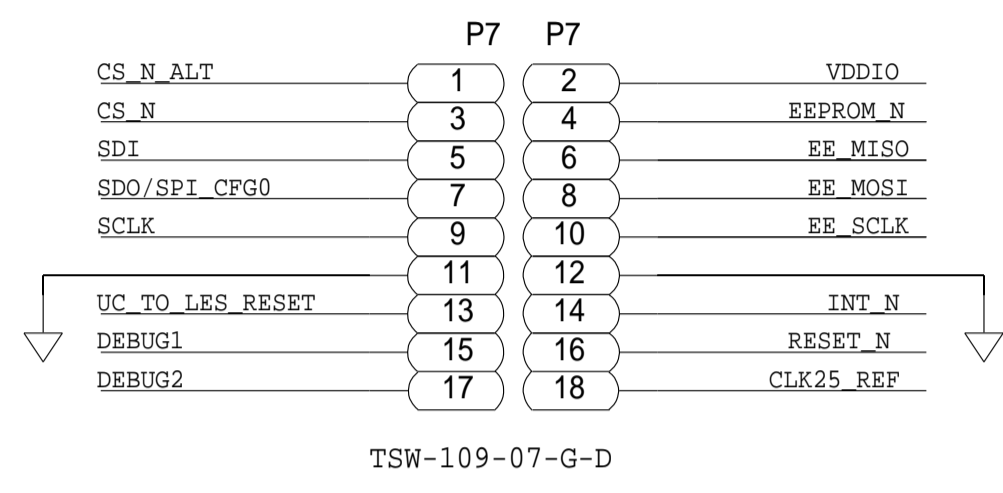
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	EVAL-ADIN2111EBZ Product(s): ADIN2111		
	DESIGN VIEW M.Ramos/W.Magnin/M.Brychta	DRAWING NO. 02_063822	REV C
	PTD ENGINEER M.Madden	SIZE C	SCALE 1:1

SHEET 7 OF 8

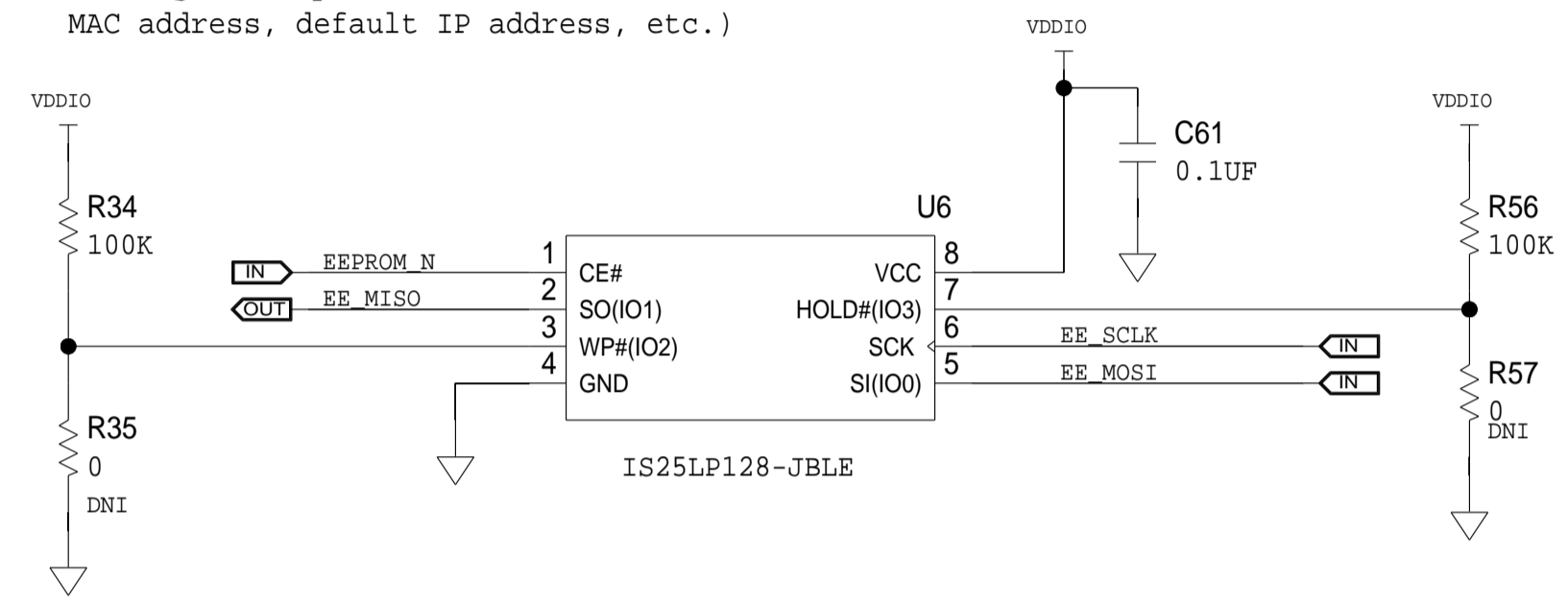
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

SPI & MISC HEADER

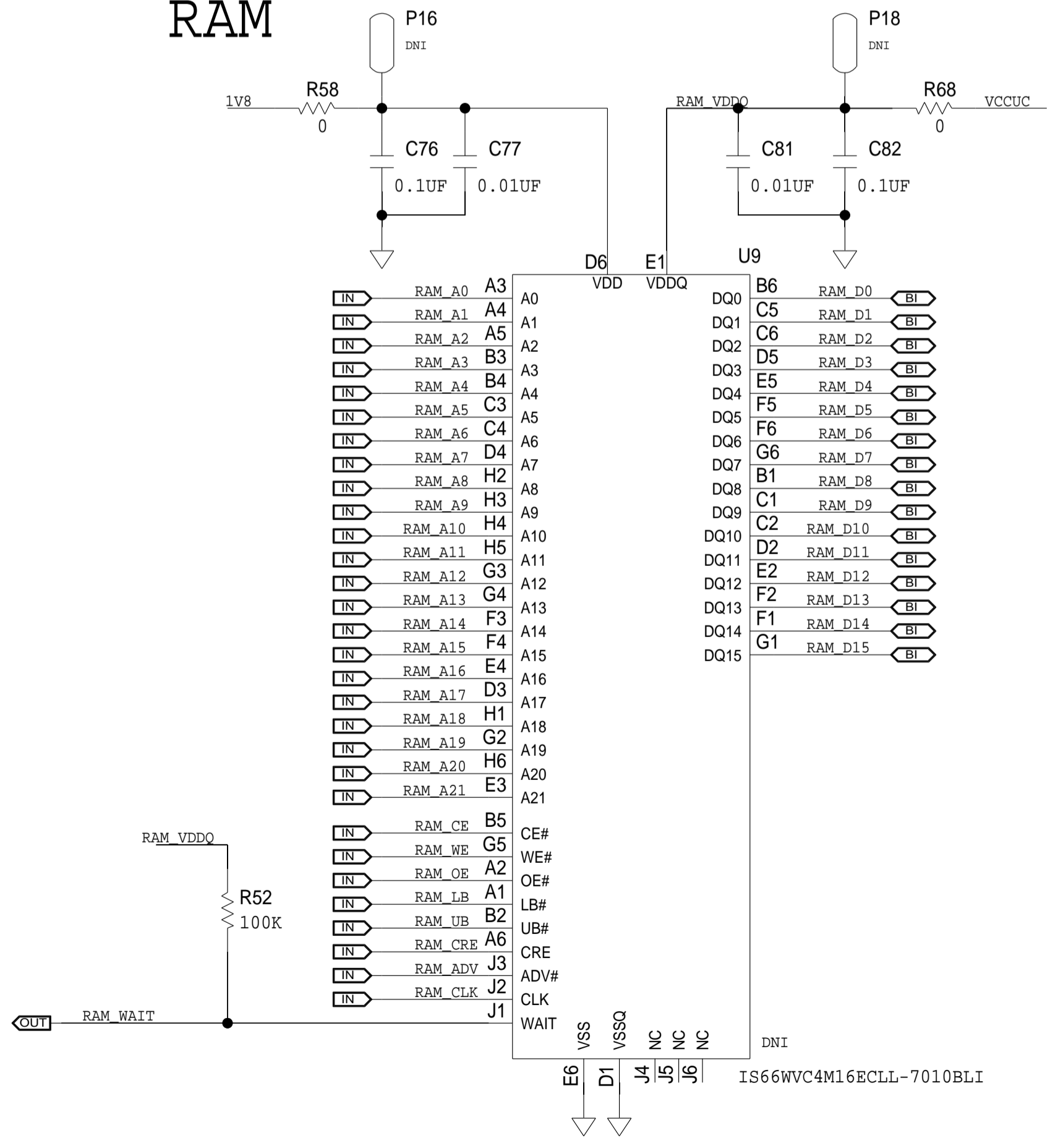
Pinout (for pins 1 to 12) matched to Digilent Pmod type 2A (expanded SPI)



External serial flash used by firmware (Storing Board production details, MAC address, default IP address, etc.)



RAM



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	EVAL-ADIN2111EBZ Product(s): ADIN2111		
	DESIGN VIEW M.Ramos/W.Magnin/M.Brychta	DRAWING NO. 02_063822	REV C
PTD ENGINEER M Madden	SIZE C	SCALE 1:1	SHEET 8 OF 8