

UARTs - DK's FPGA/CPU and ER's LPC845

DK's FPGA/CPU UARTs

MMUART_0 ↔ Header 3V3

MMUART_1 ↔ Emergency Rescue 3V3

MMUART_2 ↔ Barnacle Connector 3V3

MMUART_3 ↔ Interposer #1 1V8

MMUART_4 ↔ Interposer #2 1V8

ER's LPC845 UARTs

UART_0 ↔ RS-485 Transceiver 3V3

UART_1 ↔ Header 3V3

UART_2 ↔ DK's FPGA/CPU 3V3

UART_3 ↔ TOMcat via 3V3 <-> LVDS

UART_4 ↔ Not Used 3V3

