

Power Supply Monitor J11 & FPGA Access J12

Power Supply Monitor Connector J11

Pin Num	Function	Pin Num	Function
1	VMon_Bulk_5V0	2	Ground
3	VMon_Bulk_1V00	4	Ground
5	IMon_Pos_Bulk_1V00	6	IMon_Neg_Bulk_1V00
7	VMon_Bulk_1V50	8	Ground
9	IMon_Pos_Bulk_1V50	10	IMon_Neg_Bulk_1V50
11	VMon_Bulk_1V2	12	Ground
13	IMon_Pos_Bulk_1V2	14	IMon_Neg_Bulk_1V2
15	VMon_Bulk_1V8	16	Ground
17	IMon_Pos_Bulk_1V8	18	IMon_Neg_Bulk_1V8
19	VMon_Bulk_2V5	20	Ground
21	IMon_Pos_Bulk_2V5	22	IMon_Neg_Bulk_2V5
23	VMon_Bulk_3V3	24	Ground
25	IMon_Pos_Bulk_3V3	26	IMon_Neg_Bulk_3V3
27	VMon_Cnst_3V3	28	Ground
29	VMon_FPGA_DDR4_Term	30	Ground
31	VMon_CPU_DDR4_Term	32	Ground
33	N.C.	34	N.C.
35	N.C.	36	N.C.
37	N.C.	38	N.C.
39	N.C.	40	N.C.

FPGA/CPU Access Connector J12

Pin Num	Function	Pin Num	Function
1	JTAG_TCLK	2	Gnd
3	JTAG_TDO	4	no-conn
5	JTAG_TMS	6	Power 3V3 5V0
7	no-conn	8	JTAG_TRST
9	JTAG_TDI	10	Gnd
11	Gnd	12	Gnd
13	MMUART *0 Tx	14	MMUART *0 Rx
15	Gnd	16	Gnd
17	ER UART *1 Tx	18	ER UART *1 Rx
19	Gnd	20	Gnd
21	Access_Signal_1	22	Gnd
23	Access_Signal_2	24	Gnd
25	Access_Signal_3	26	Gnd
27	Access_Signal_4	28	Gnd
29	Access_Signal_5	30	Gnd
31	Gnd	32	Gnd
33	Diff_Signal_1_Dir	34	Diff_Signal_1_Cmp
35	Gnd	36	Gnd
37	Diff_Signal_2_Dir	38	Diff_Signal_2_Cmp
39	Gnd	40	Gnd

Pins 1 through 10 of the FPGA/CPU Access Connector match the MicroSemi standard JTAG connector pinout.