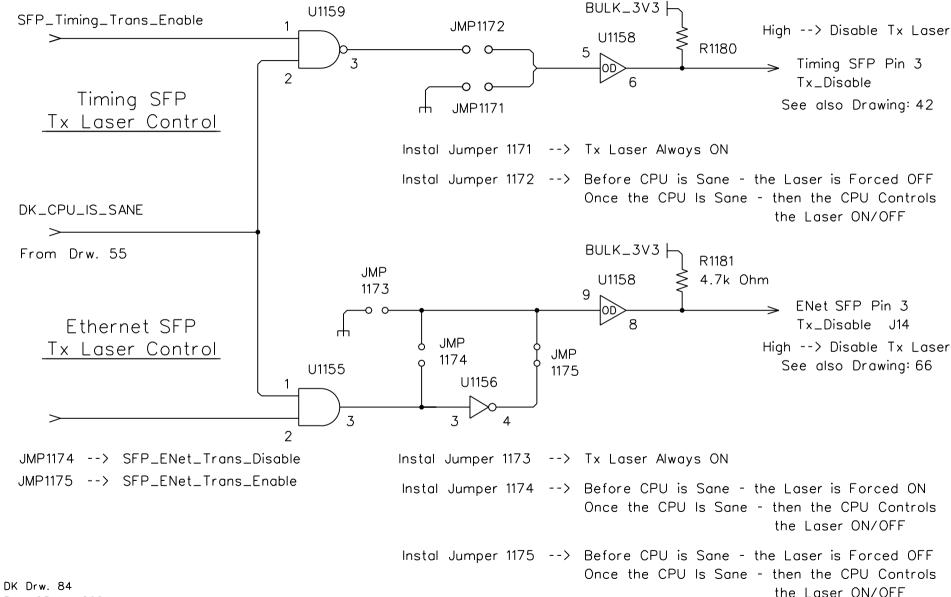
<u>Timing & ENet SFP Tx Laser Control Page 3</u>



Rev. 25-Apr-2024 See Also DK Drw. 51, 55, 56, 85, 86

These discrete logic circuits are powered by the CNST_3V3 bus.