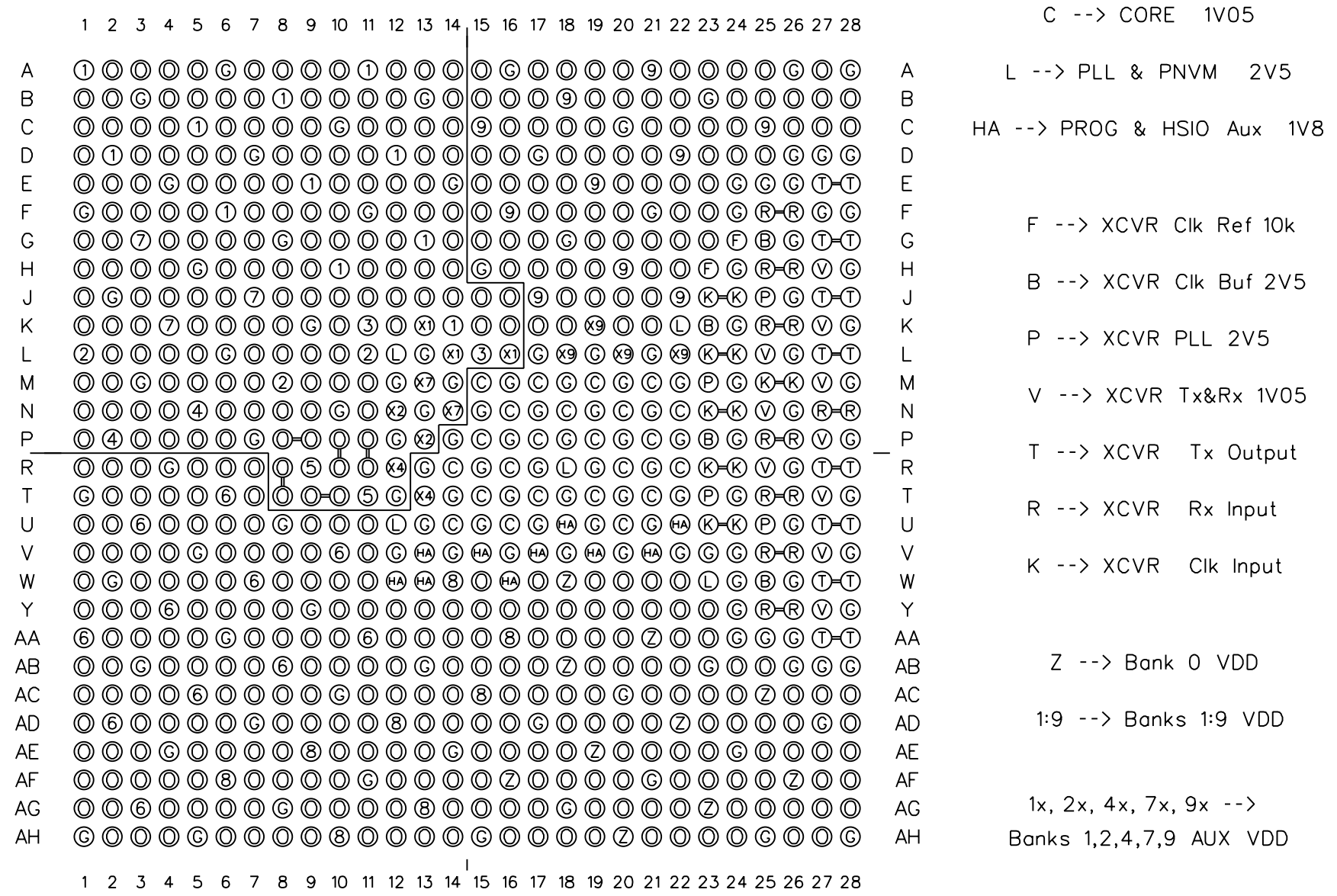


# DK Board - FPGA/CPU - Minimum BULK\_3V3 Plane

BULK\_3V3 Connects to I/O Banks: 1, 1x, 2, 2x, 3, 5, 7, 7x



TOP VIEW