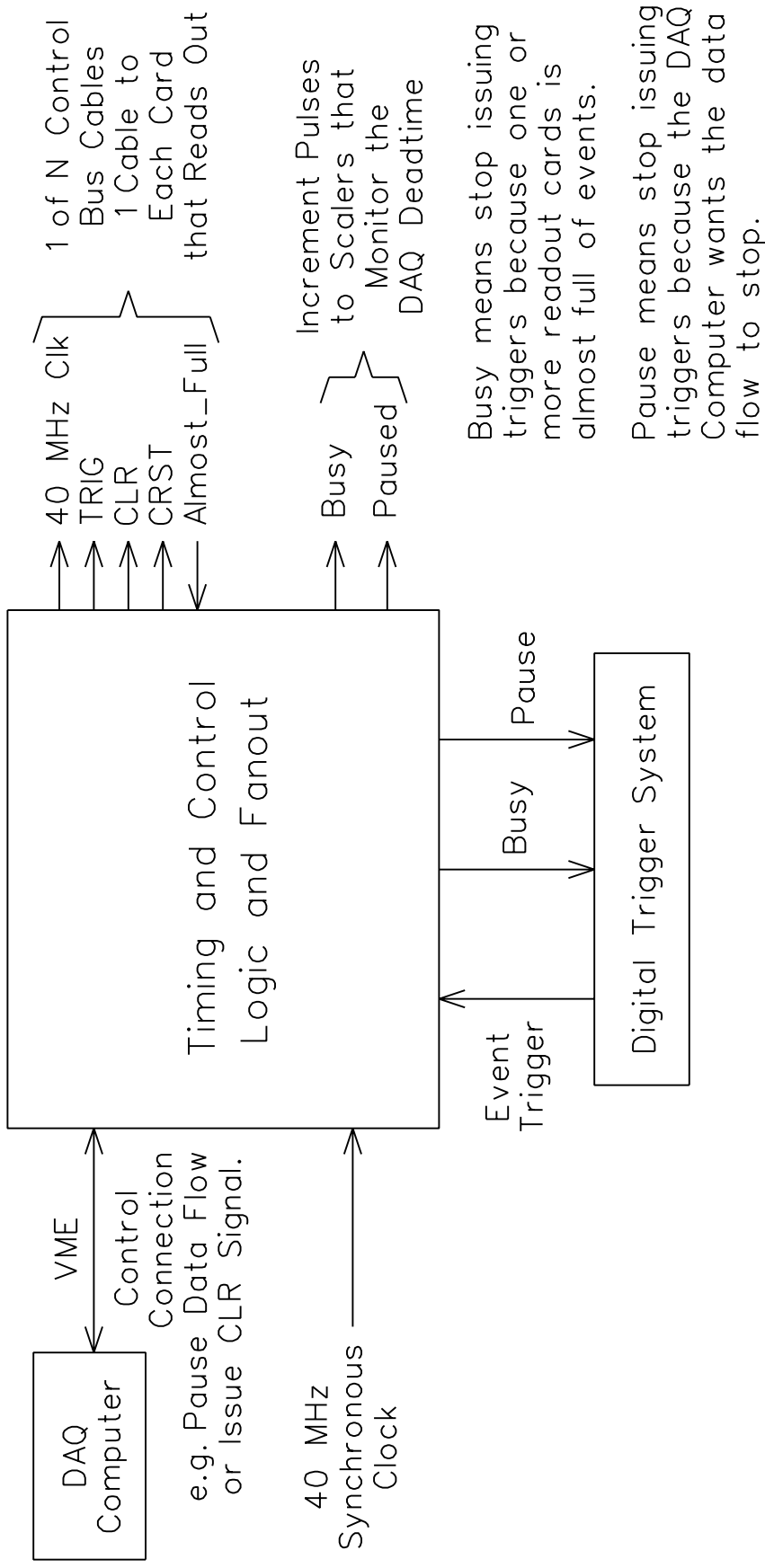


Timing and Control - Logic and Fanout



Each Control Bus cable carries the following signals from the Timing and Control Fanout to a card that reads out in the TDC DAQ System: Synchronous 40 MHz Clock, TRIG, CLR, and CRST. In addition each Control Bus cable carries the Almost_Full signal from the readout card that it services back to the Timing and Control logic.