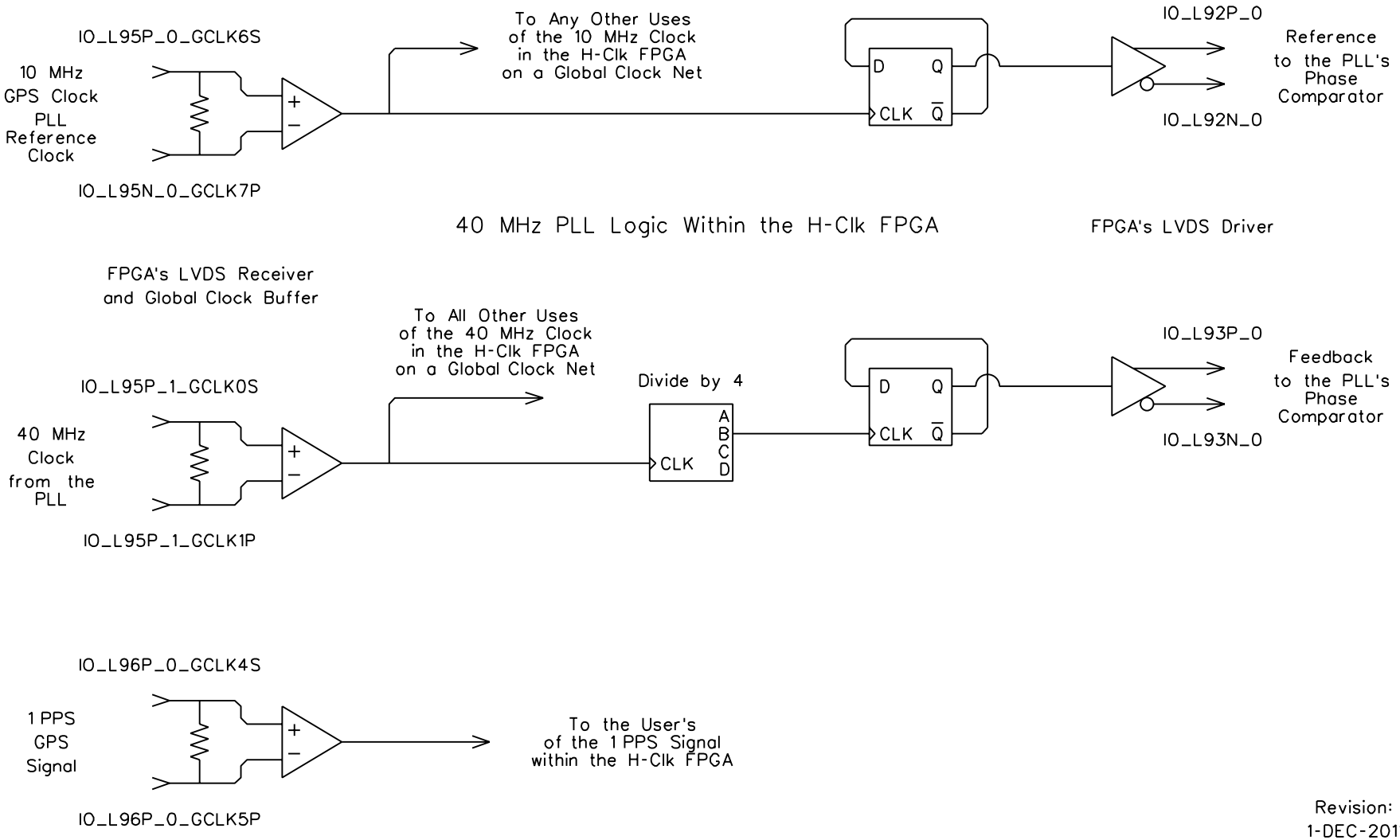
H-Clk FPGA Global Clock Receivers and 40 MHz PLL Pre-Scaler



Revision:
1-DEC-2011