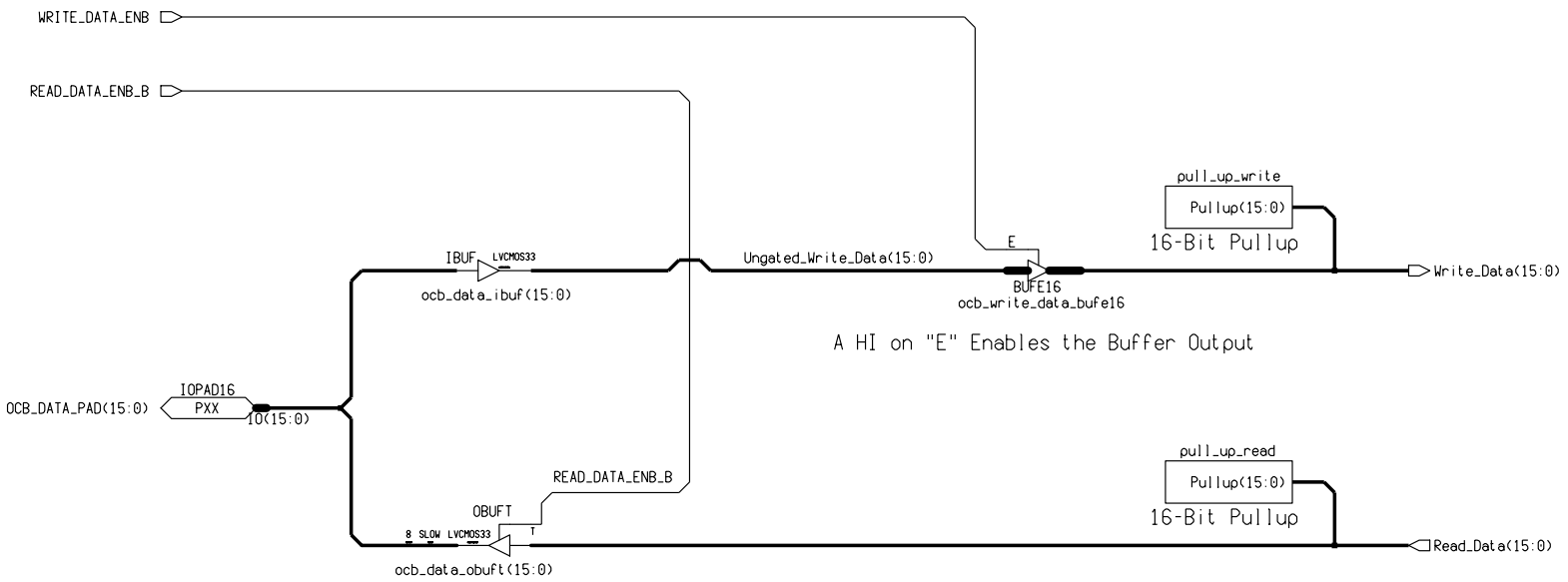

HIGH -> Read
LOW -> Write

Changed by: Edmunds	Date Changed: Sunday 7-MAR-2012	Time Changed: 11:40:01 am	QA CHK:	REV:	Drawing Number:	Page: 1

Engineer: Steve Gross
Drawn by: Steve Gross
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:

MICHIGAN STATE UNIVERSITY
HAWC Experiment
H-Clk Firmware 2

TITLE:
vme_in_buf
FPGA's VME Data I/O Buffer

Size: C

4 1 4 A