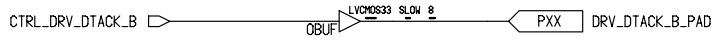
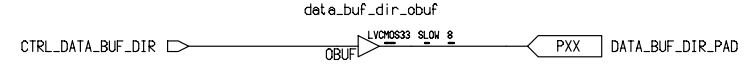
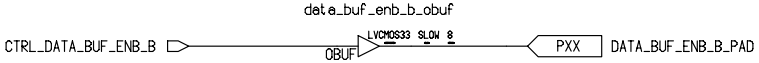


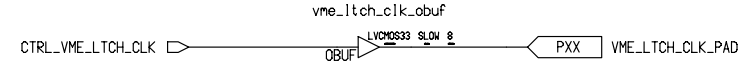
Controls the DTACK_B signal from H-Clk
HI --> H-Clk card does not assert DTACK_B
LOW --> H-CLK card asserts DTACK_B



Set the Direction of U304
the VME Data bus xcver
HI --> H-Clk data to VME Bus, Read
LOW --> VME Bus data to H-Clk, Write



HI --> Tri-State all U304
VME Data Bus XCVER Outputs
LOW --> Enable the U304 output
selected by DATA_BUF_DIR



HI --> U301 and U302 latches
are Transparent
LOW --> U301 and U302 latches
Hold at the setup value

Changed by: Edmunds	Date Changed: Monday 27-FEB-2012	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY HAWC Experiment HAWC H-Clk Firmware 2
Drawn by: Librarian	
R&D CHK:	TITLE: Control Signals to the H-Clk VME Interface Chips H-Clk Firmware 2
DOC CTRL CHK:	Size: C 4 1 4 A
MFG ENGR CHK:	