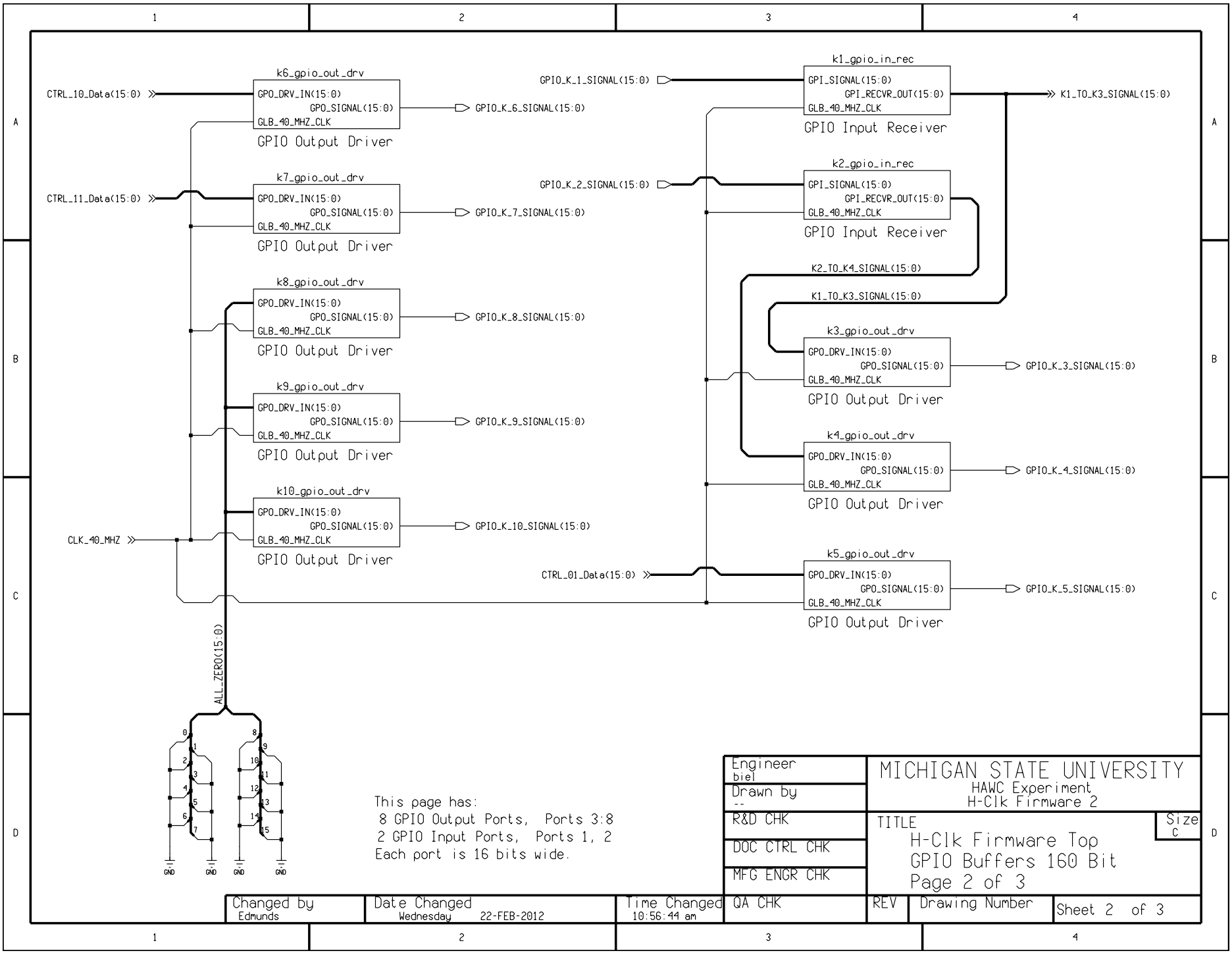

This page has:
8 GPIO Output Ports, Ports 3:8
2 GPIO Input Ports, Ports 1, 2
Each port is 16 bits wide.

Engineer: biel	MICHIGAN STATE UNIVERSITY HAWC Experiment H-Clk Firmware 2
Drawn by: --	
R&D CHK	TITLE: H-Clk Firmware Top GPIO Buffers 160 Bit Page 2 of 3
DOC CTRL CHK	Size: C
MFG ENGR CHK	
QA CHK	REV / Drawing Number / Sheet 2 of 3

Changed by	Date Changed	Time Changed
Edmunds	Wednesday 22-FEB-2012	10:56:44 am