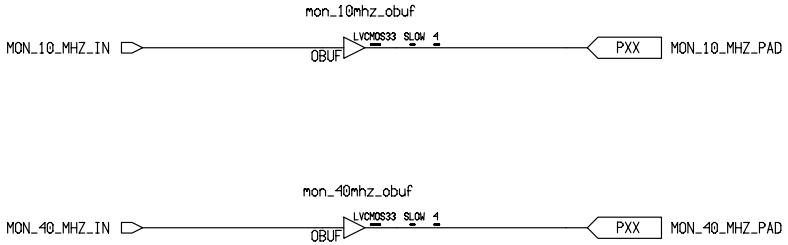

Provide the 10 MHz GPS Clock
and the 40 MHz PLL Clock
to two Access Connector monitor points.

Changed by: Edmunds	Date Changed: Thursday 8-MAR-2012	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:	Page: 1

Engineer: Librarian
Drawn by: Librarian
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:

MICHIGAN STATE UNIVERSITY
HAWC Experiment
HAWC H-Clk Firmware 2

TITLE:
Clock Monitor Driver
H-Clk Firmware 2
T7

Size: C

4 1 4 A