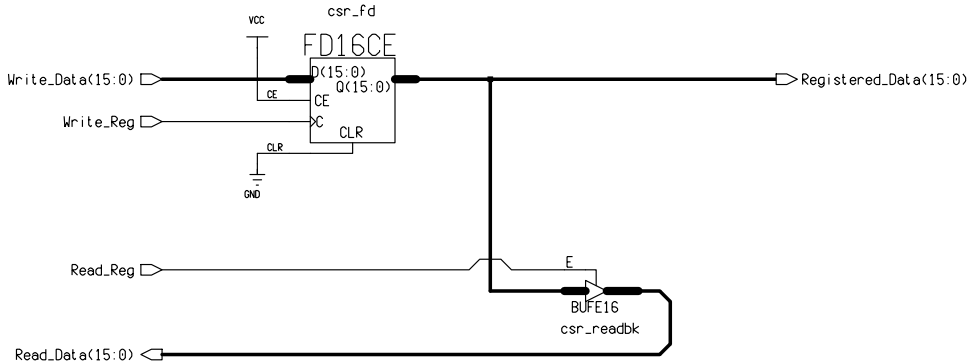

16 bit Control Register

Write data into the register on
the positive edge of "Write_Reg".

Read data from the register while
"Read_Reg" is asserted High.

Changed by: Edmunds	Date Changed: Wednesday 22-DEC-2004	Time Changed: 12:00:00	QA CHK:	REV:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: ctrl_reg_16_bit Control Register for Virtex II D.P. FPGA Design T6
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4 1 4 A