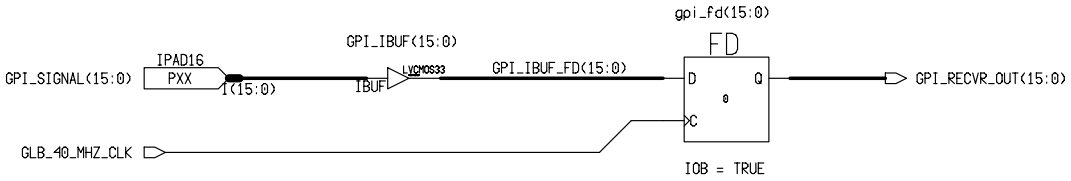

This is the Receiver for the H-Clk's 16 bit General Purpose Input Ports.

Changed by: Edmunds	Date Changed: Tuesday 21-FEB-2012	Time Changed: 12:00:00	QA CHK:	REV:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY HAWC Experiment HAWC H-Clk Firmware 1
Drawn by: Librarian	
R&D CHK:	TITLE: GPIO 16 Bit Input Port H-Clk Firmware 2 T7
DOC CTRL CHK:	Size: C 4 1 4 A
MFG ENGR CHK:	Drawing Number: