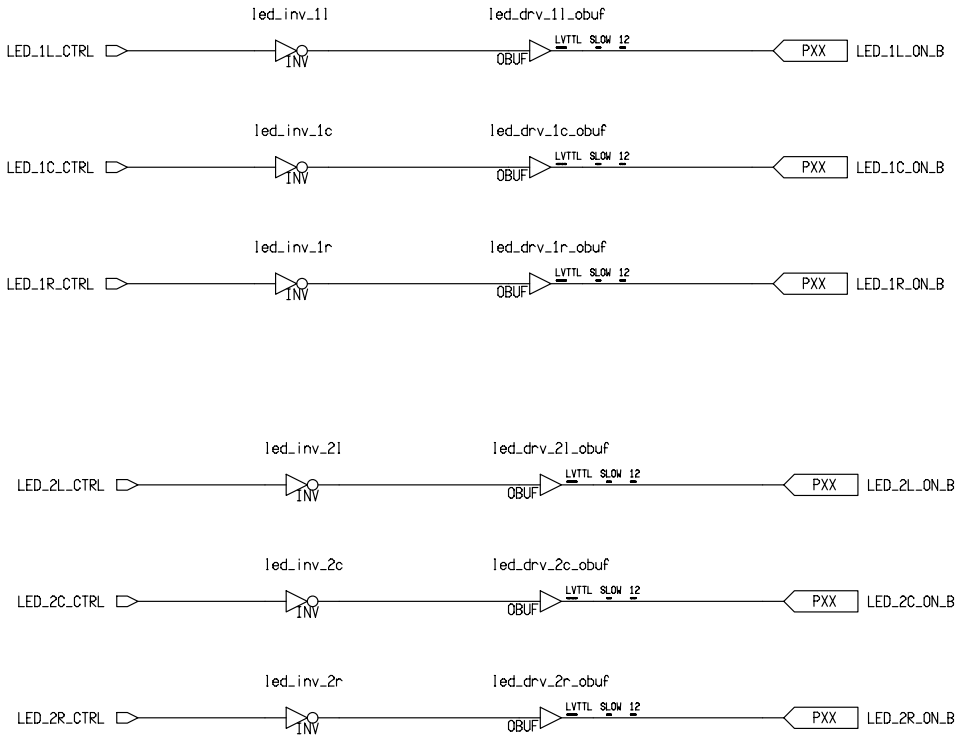

On the H-Clk circuit board the output of the FPGA is connected through a resistor to the Cathode of the LED. The Anode of the LED is connected to net VDD_LOGIC (3.3 Volts).

This module is "Voltage Inverting" so that a HIGH asserted signal into this module will cause the LED to illuminate.

Changed by: Edmunds	Date Changed: Sunday 12-FEB-2012	Time Changed: 12:00:00	QA CHK:	REV:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY HAWC Experiment HAWC H-Clk Firmware 1
Drawn by: Librarian	
R&D CHK:	TITLE: led_driver H-Clk Firmware 1 T7
DOC CTRL CHK:	Size: C 4 1 4 A
MFG ENGR CHK:	Drawing Number: