Pre-Amplifier and Its Input Circuit

Narrow Gaussian Version

D-Zero Dual FET PreAmp

Input Coupling Network

PreAmp FET Input Stage

PreAmp Output "Driver" Stage

Wire Bias Voltage

100 Meg.
2 pFd

100 Meg. Ohm
10 nFd 1600V

4.7 uFd
15 nFd
3.3k Ohm

10k Ohm

115 Ohm

Direct

115 Ohm

Complement

Signal Amplitude at the Output of the FET Input Stage is 0.5 mV per Femto Coulomb.

Differential Signal Amplitude at the "Driver" Stage Output is 3.0 mV per Femto Coulomb with no Load.

2 pFd x 100 Meg = 200 usec = 796 Hz
4.7 uFd x 3.3k = 15.6 msec = 10 Hz
0.15 uFd x 10k = 1.5 msec = 106 Hz

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