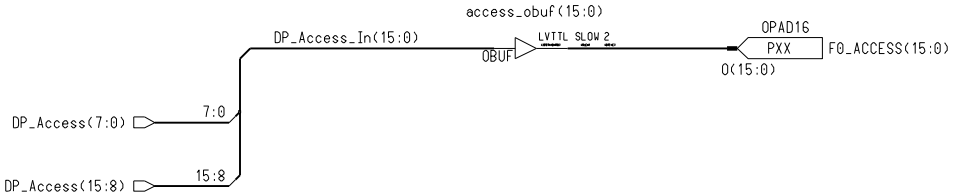

This component is the driver for
the 16 lines that provide
Access from the P5 connector
to the Data Path FPGA.

For now this Access is setup as
16 output signals from the DP FPGA.

Changed by: Edmunds	Date Changed: Thursday 28-APR-2005	Time Changed: 12:00:00	QA CHK:	REV:	Drawing Number:	Page: 1

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: access_Connector P5 Connector Access to DP FPGA T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4 1 4 A