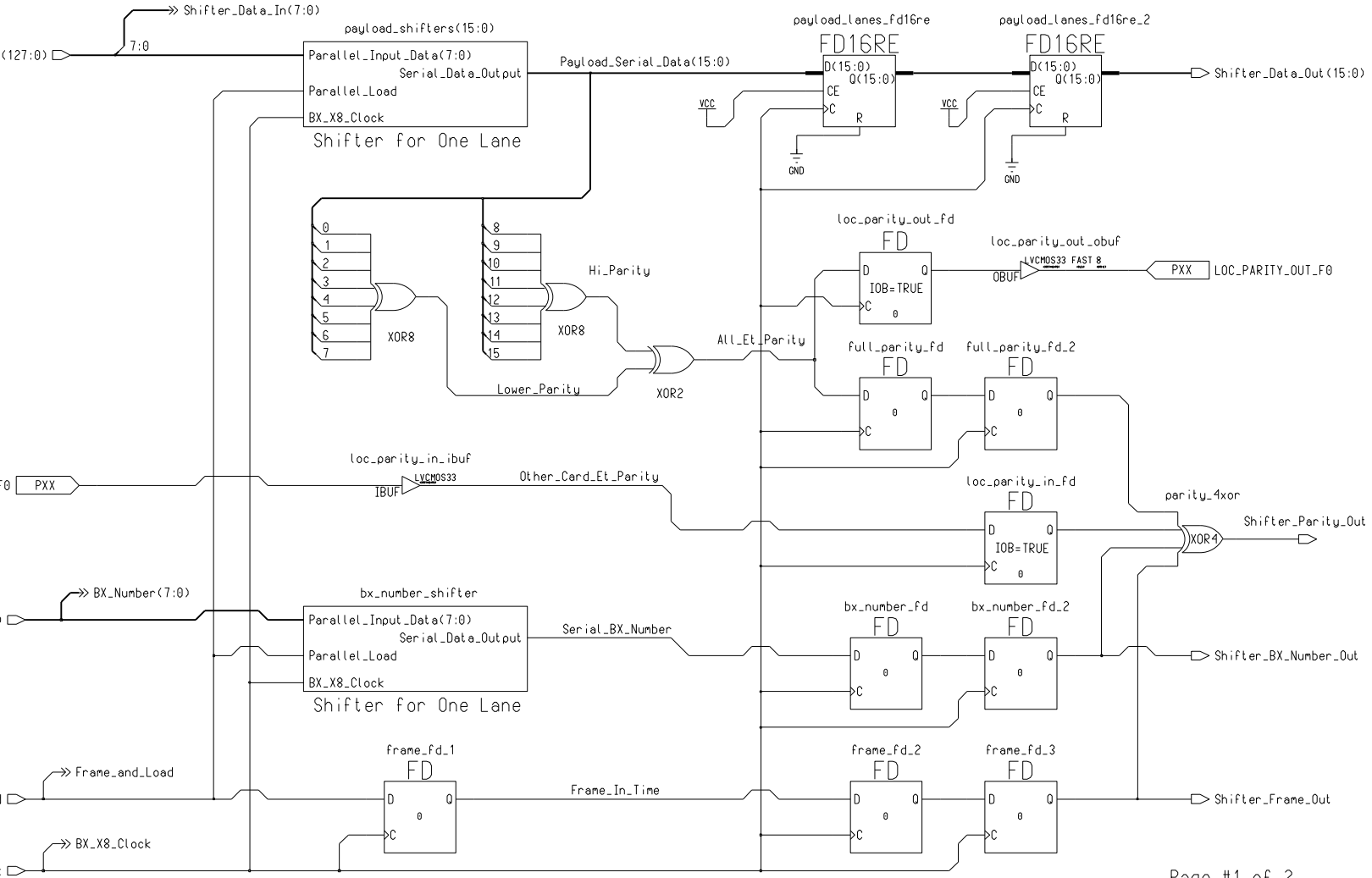

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T6 Design for the Channel Link Parity Generator and Data Shifter

Denis called this "ODD Parity"
The total number of 1's must be Even to work with the Chan Link Tester.

FRAME is asserted when the LSBit (the first bit sent) of each ADF to TAB frame is transmitted.

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger	
Drawn by: Librarian		
R&D CHK:	TITLE: chan_lnk_output_shifter Channel Link Parity Generator and Output Shifter for T7	Size: C 4144
DOC CTRL CHK:		
MFG ENGR CHK:		

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Monday 9-MAY-2005	12:00:00				1