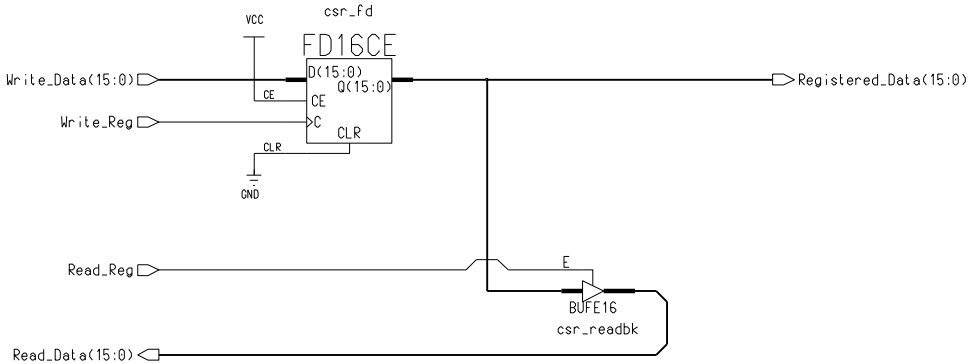


16 bit Control Register

Write data into the register on
the positive edge of "Write_Reg".

Read data from the register while
"Read_Reg" is asserted High.

| | | |
|---|---|---|
| Engineer: Librarian | MICHIGAN STATE UNIVERSITY<br>D0 Run II Upgrade<br>Run IIB L1 Calorimeter Trigger | |
| Drawn by: Librarian | | |
| R&D CHK: | TITLE: ctrl_reg_16_bit<br>Control Register for Virtex II<br>D.P. FPGA Design T6 | Size: C<br>4148 |
| DOC CTRL CHK: | | |
| MFG ENGR CHK: | | |

| Changed by: | Date Changed: | Time Changed: | QA CHK: | REV: | Drawing Number: | Page: |
|---|---|---|---|---|---|---|
| Edmunds | Wednesday 22-DEC-2004 | 12:00:00 | | | | 1 |