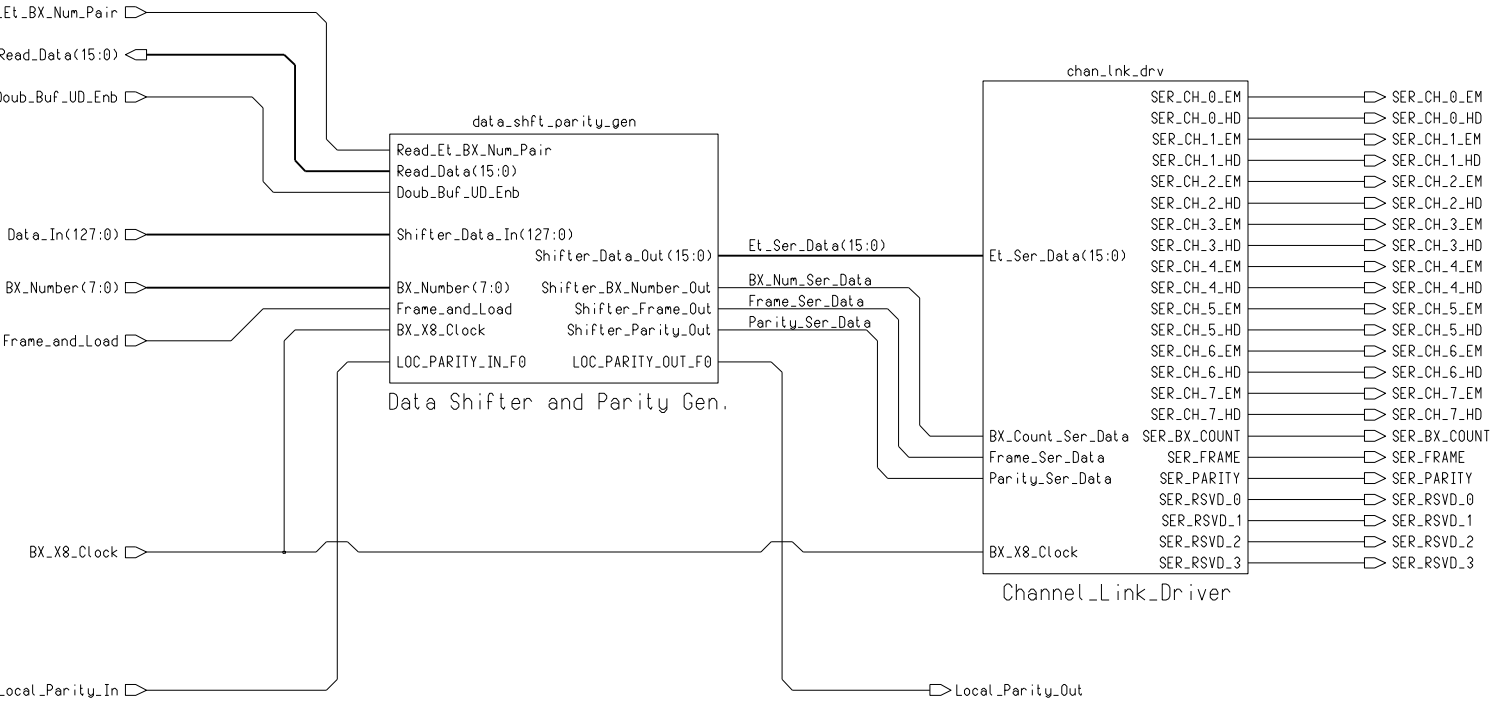

Serialize the ADF to TAB data
to make 8 Channel Link frames.
Drive this data to the 3 Channel Link chips.

Engineer: Edmunds	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger
Drawn by: Librarian	
R&D CHK:	TITLE: frame_shft_and_chan_lnk Frame Shifter and Channel Link D.P. FPGA Design T7
DOC CTRL CHK:	Size: C
MFG ENGR CHK:	4 1 4 A

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Monday 9-MAY-2005	12:00:00				1