



On the ADF-2 circuit board the output of the DP FPGA is connected through a resistor to the Cathode of the LED. The Anode of the LED is connected to net VDD_LLOGIC (3.3 Volts).

This module is "Voltage Inverting" so that a HIGH asserted signal into this module will cause the LED to illuminate.

Changed by: Edwards

Date Changed: Thursday 28 APR 2005

Time Changed: 12:40:00

Engineer: Librarian	MICHIGAN STATE UNIVERSITY		
Drawn By: Librarian	D0 Run II Upgrade		
R&D CHK:	Run IIB L1 Calorimeter Trigger		
DOC CTRL CHK:	TITLE: led_driver		
MFG ENGR CHK:	LED Driver for DP T7		
10A CHK:	REV: T7	Drawing Number:	Page: 1

Size: 414A