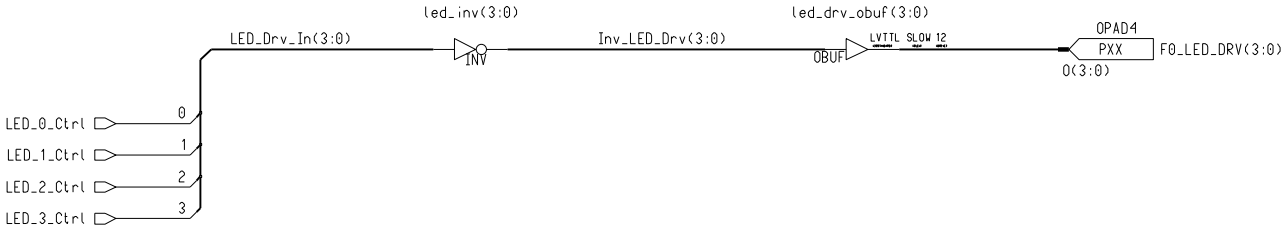

On the ADF-2 circuit board the output of the DP FPGA is connected through a resistor to the Cathode of the LED. The Anode of the LED is connected to net VDD_LOGIC (3.3 Volts).

This module is "Voltage Inverting" so that a HIGH asserted signal into this module will cause the LED to illuminate.

Engineer: Librarian	MICHIGAN STATE UNIVERSITY D0 Run II Upgrade Run IIB L1 Calorimeter Trigger	
Drawn by: Librarian		
R&D CHK:	TITLE: led_driver LED Driver for DP T7 T7	Size: C 4 1 4 A
DOC CTRL CHK:		
MFG ENGR CHK:		

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV:	Drawing Number:	Page:
Edmunds	Thursday 28-APR-2005	12:00:00				1